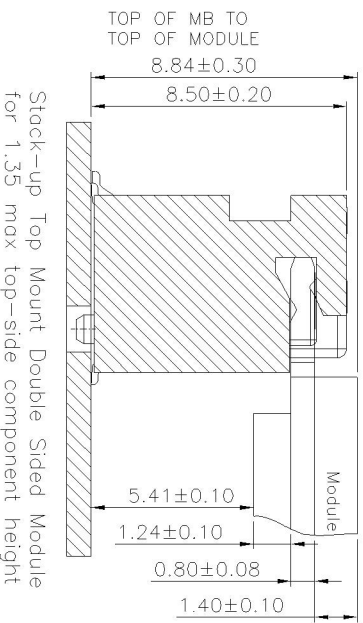
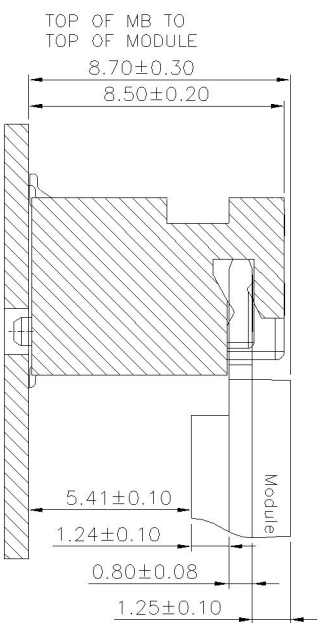
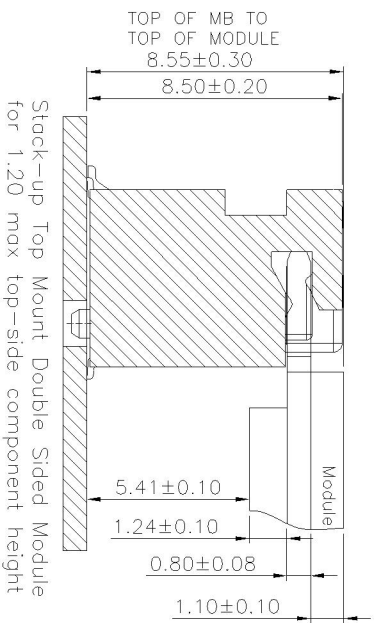
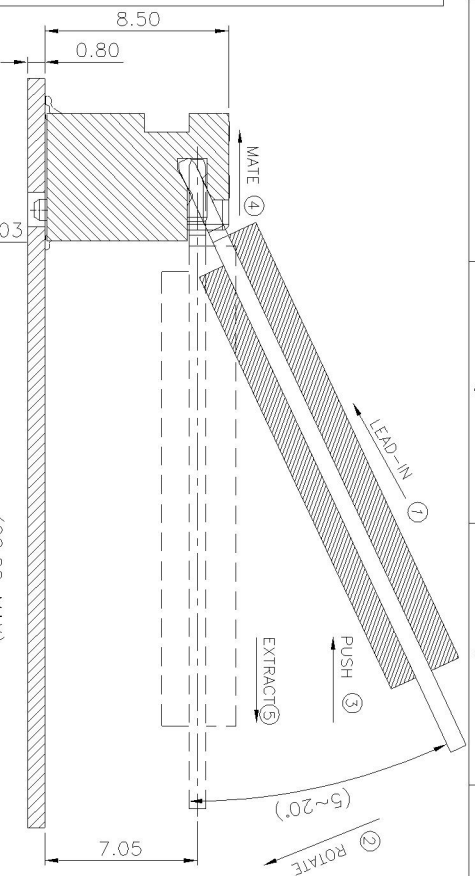
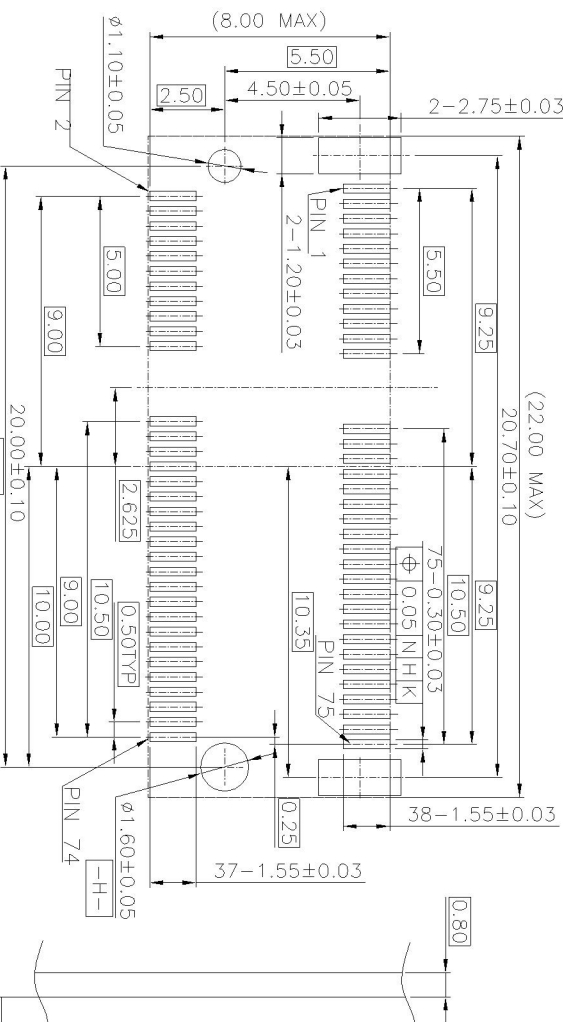




Assembly stack height schematic diagram

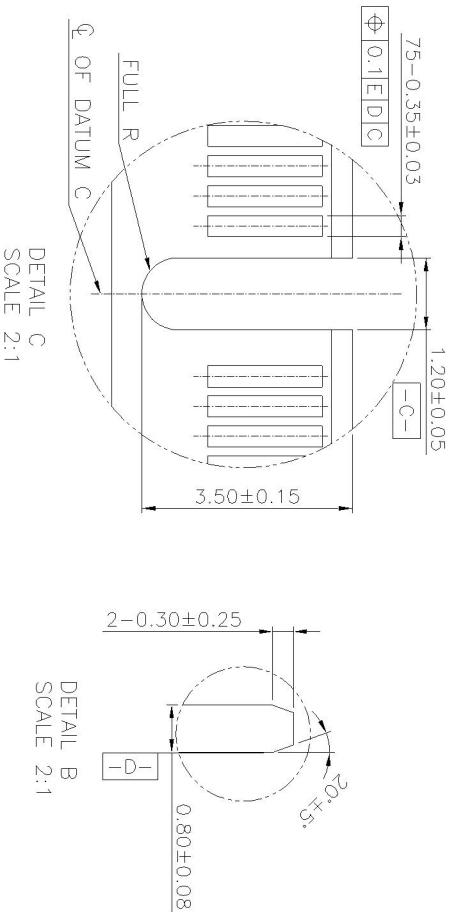
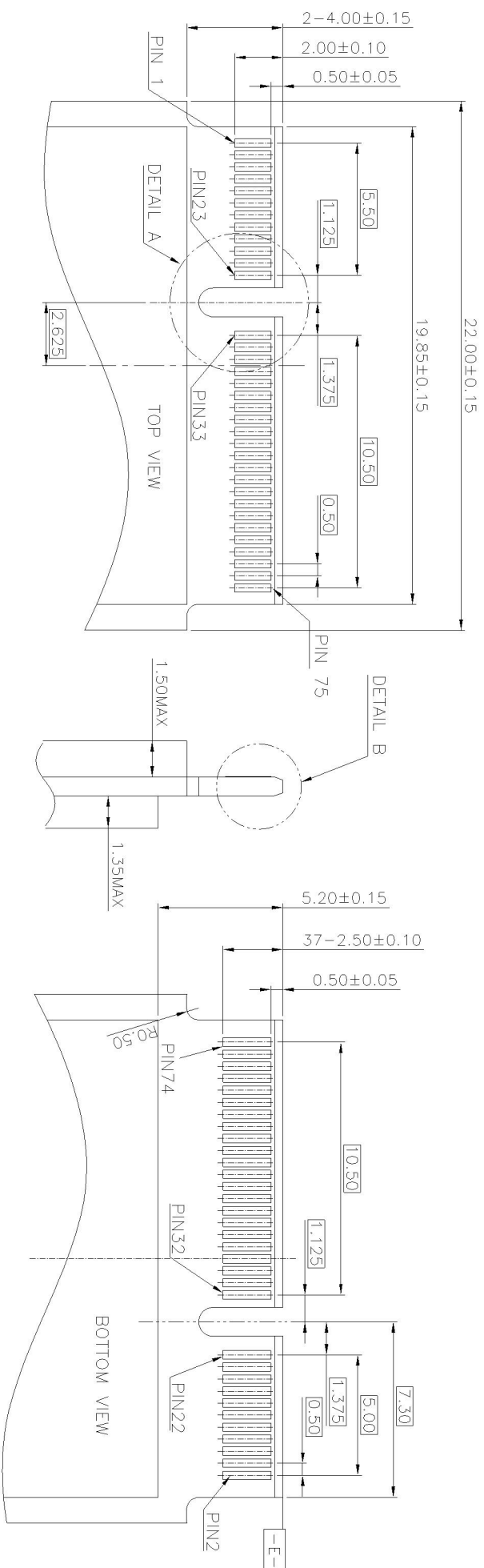


HOW TO MATE AND EXTRACT
MATE: ① → ④
EXTRACT: ⑤
NOTE: Mating force will generate from step 3 to step 4.



RECOMMENDED PCB LAYOUT(TOP VIEW)
DEFAULT TOLERANCE ±0.05MM

GENERAL TOLERANCES UNLESS SPECIFIED		DWG NO.	深圳市连欣科技有限公司	
X ± 0.35	X° ± 3°	AP-APC101083	0.5 PITCH M.2/NGFF 8.5H E-KEY 67PIN/75PIN	
X ± 0.25	X° ± 2°	APPROVED BY Barney		
XXX ± 0.15	XX° ± 1°	CHECKED BY Vito		
CUSTOMER DRAWING		PART NO. APC101083-P001A		
SIZE A4	UNITS MM [INCH]	DRAWN BY Sam	SHEET 1 / 4 SCALE 4 : 1 REV 1	



GENERAL TOLERANCES UNLESS SPECIFIED		DWG NO.	深圳市连欣科技有限公司
X ± 0.35	X ± 3°		
X ± 0.25	X ± 2°	APPROVED BY Barney	TITLE 0.5 PITCH M.2/NGFF 8.5H E-KEY 67PIN/75PIN
XXX ± 0.15	XX ± 1°		
CUSTOMER DRAWING		CHECKED BY Vito	PART NO. APCI01083-P001A
SIZE A4	UNITS MM [INCH]	DRAWN BY Sam	SHEET 1 / 4
			SCALE 4 : 1
			REV 1

