

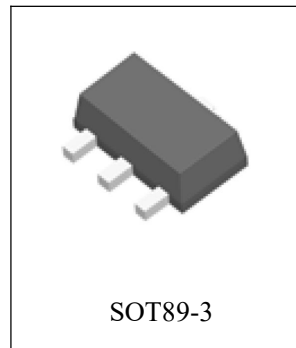
78SXXM

Three-terminal Voltage Regulator

General Description

The 78SXXM series of three terminal positive regulators employ internal current limiting and thermal shutdown, making them essentially indestructible. If adequate heat-sink is provided, they can deliver up to 200 mA output current.

They are intended as fixed voltage regulators in a wide range of applications including local or on- card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power pass elements to make high-current voltage regulators. The 78SXXM series used as Zener diode/resistor combination replacement, offers improvement along with lower quiescent current and lower noise.



Features

- Maximum Output Current: 200mA
- Output Voltage of 5V ,6V,8V,9V,12V,15V Available
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Continuous Total Dissipation 0.5W
- 78SXXM is available in SOT89-3 package

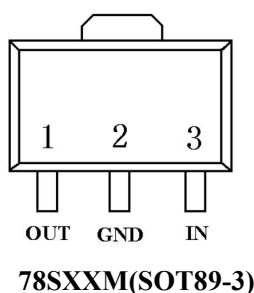
Package Information

Part NO.	Package Description	Package Marking	Package Option
78SXXM	SOT89-3	78SXXM SXXXX	1000/Reel

SXXXX:Lot NO. 78SXXM:Part NO. (XX:Output Voltage)

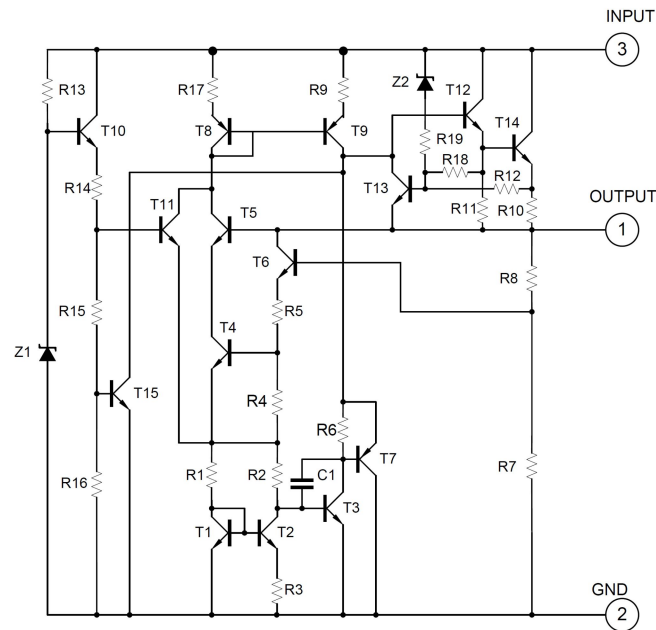
XX(Output Voltage): 05(5.0V)/06(6.0V)/08(8.0V)/09(9.0V)/12(12V)/15(15V)

Pin Configuration



Absolute Maximum Ratings ($T_a=25^{\circ}\text{C}$)

Parameter	Symbol	Value	Units
Input Voltage	V_i	35	V
Operating Temperature Range	T_{opr}	-40~ +125	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	-55 ~ +150	$^{\circ}\text{C}$

Block Diagram**Electrical Characteristics**

For 78S05M(unless otherwise noted, $V_i=10\text{V}$, $I_o=40\text{mA}$, $C_i=0.33\mu\text{F}$, $C_o=0.1\mu\text{F}$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}\text{C}$	4.8	5.0	5.2	V
		$7\text{V} \leq V_i \leq 20\text{V}$ $I_o=5\text{mA} \sim 150\text{mA}$	4.75	5.0	5.25	
Load Regulation	ΔV_o	$T_j=25^{\circ}\text{C}, I_o=1\text{mA} \sim 200\text{mA}$		30	100	mV
		$T_j=25^{\circ}\text{C}, I_o=1\text{mA} \sim 40\text{mA}$		10	50	
Line Regulation	ΔV_o	$7\text{V} \leq V_i \leq 20\text{V}, T_j=25^{\circ}\text{C}$		11	60	mV
		$8\text{V} \leq V_i \leq 20\text{V}, T_j=25^{\circ}\text{C}$		5	30	
Quiescent Current	I_q	$T_j=25^{\circ}\text{C}$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$7\text{V} \leq V_i \leq 20\text{V}, I_o=40\text{mA}$			1.5	mA
		$5\text{mA} \leq V_i \leq 150\text{mA}$			0.1	
Output noise Voltage	V_N	$10\text{Hz} \leq f \leq 100\text{kHz}$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}\text{C}$		1.7		V
Ripple Rejection	RR	$7\text{V} \leq V_i \leq 20\text{V}, f=120\text{Hz}, T_j=25^{\circ}\text{C}$	41	49		dB

For 78S06M(unless otherwise noted, $V_i=11V$, $I_o=40mA$, $C_i=0.33\mu F$, $C_o=0.1\mu F$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}C$	5.79	6.0	6.21	V
		$8V \leq V_i \leq 21V$ $I_o=5mA \sim 150mA$	5.70	6.0	6.30	
Load Regulation	ΔV_o	$T_j=25^{\circ}C, I_o=1mA \sim 200mA$		30	100	mV
		$T_j=25^{\circ}C, I_o=1mA \sim 40mA$		10	50	
Line Regulation	ΔV_o	$8V \leq V_i \leq 21V, T_j=25^{\circ}C$		11	60	mV
		$9V \leq V_i \leq 20V, T_j=25^{\circ}C$		5	30	
Quiescent Current	I_q	$T_j=25^{\circ}C$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$8V \leq V_i \leq 21V, I_o=40mA$			1.5	mA
		$5mA \leq V_i \leq 150mA$			0.1	
Output noise Voltage	V_N	$10Hz \leq f \leq 100kHz$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}C$		1.7		V
Ripple Rejection	RR	$8V \leq V_i \leq 21V, f=120Hz,$ $T_j=25^{\circ}C$	41	48		dB

For 78S08M(unless otherwise noted, $V_i=14V$, $I_o=40mA$, $C_i=0.33\mu F$, $C_o=0.1\mu F$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}C$	7.72	8.0	8.28	V
		$10.5V \leq V_i \leq 23V$ $I_o=5mA \sim 150mA$	7.60	8.0	8.40	
Load Regulation	ΔV_o	$T_j=25^{\circ}C, I_o=1mA \sim 200mA$		26	160	mV
		$T_j=25^{\circ}C, I_o=1mA \sim 40mA$		10	80	
Line Regulation	ΔV_o	$10.5V \leq V_i \leq 23V, T_j=25^{\circ}C$		11	100	mV
		$11V \leq V_i \leq 25V, T_j=25^{\circ}C$		5	50	
Quiescent Current	I_q	$T_j=25^{\circ}C$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$10.5V \leq V_i \leq 23V, I_o=40mA$			1.5	mA
		$5mA \leq V_i \leq 150mA$			0.1	
Output noise Voltage	V_N	$10Hz \leq f \leq 100kHz$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}C$		1.7		V
Ripple Rejection	RR	$10.5V \leq V_i \leq 23V, f=120Hz,$ $T_j=25^{\circ}C$	39	46		dB

For 78S09M(unless otherwise noted, $V_i=15V, I_o=40mA, C_i=0.33\mu F, C_o=0.1\mu F$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}C$	8.68	9.0	9.32	V
		$11.5V \leq V_i \leq 26V$ $I_o=5mA \sim 150mA$	8.55	9.0	9.45	
Load Regulation	ΔV_o	$T_j=25^{\circ}C, I_o=1mA \sim 200mA$		26	160	mV
		$T_j=25^{\circ}C, I_o=1mA \sim 40mA$		10	80	
Line Regulation	ΔV_o	$11.5V \leq V_i \leq 26V, T_j=25^{\circ}C$		11	100	mV
		$13V \leq V_i \leq 26V, T_j=25^{\circ}C$		5	50	
Quiescent Current	I_q	$T_j=25^{\circ}C$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$7V \leq V_i \leq 20V, I_o=40mA$			1.5	mA
		$5mA \leq V_i \leq 150mA$			0.1	
Output noise Voltage	V_N	$10Hz \leq f \leq 100kHz$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}C$		1.7		V
Ripple Rejection	RR	$11.5V \leq V_i \leq 26V, f=120Hz,$ $T_j=25^{\circ}C$	39	46		dB

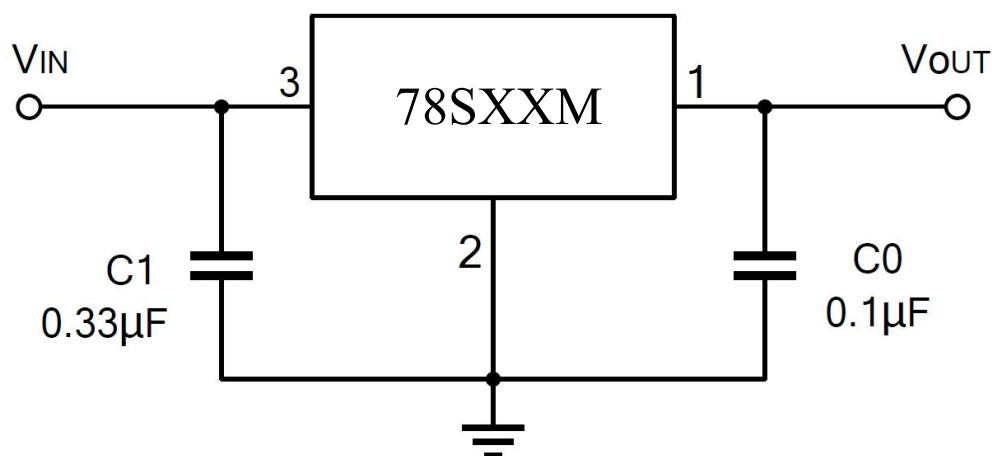
For 78S12M(unless otherwise noted, $V_i=19V, I_o=40mA, C_i=0.33\mu F, C_o=0.1\mu F$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}C$	11.6	12.0	12.4	V
		$14.5V \leq V_i \leq 30V$ $I_o=5mA \sim 150mA$	11.4	12.0	12.6	
Load Regulation	ΔV_o	$T_j=25^{\circ}C, I_o=1mA \sim 200mA$		26	160	mV
		$T_j=25^{\circ}C, I_o=1mA \sim 40mA$		10	120	
Line Regulation	ΔV_o	$14.5V \leq V_i \leq 30V, T_j=25^{\circ}C$		14	250	mV
		$16V \leq V_i \leq 30V, T_j=25^{\circ}C$		10	100	
Quiescent Current	I_q	$T_j=25^{\circ}C$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$14.5V \leq V_i \leq 30V, I_o=40mA$			1.5	mA
		$5mA \leq V_i \leq 150mA$			0.1	
Output noise Voltage	V_N	$10Hz \leq f \leq 100kHz$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}C$		1.7		V
Ripple Rejection	RR	$14.5V \leq V_i \leq 30V, f=120Hz,$ $T_j=25^{\circ}C$	39	46		dB

For 78S15M(unless otherwise noted, $V_i=23V$, $I_o=40mA$, $C_1=0.33\mu F$, $C_o=0.1\mu F$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	V_o	$T_j=25^{\circ}C$	14.5	15.0	15.5	V
		$17.5V \leq V_i \leq 30V$ $I_o=5mA \sim 150mA$	14.3	15.0	15.7	
Load Regulation	ΔV_o	$T_j=25^{\circ}C, I_o=1mA \sim 200mA$		26	160	mV
		$T_j=25^{\circ}C, I_o=1mA \sim 40mA$		10	150	
Line Regulation	ΔV_o	$17.5V \leq V_i \leq 30V, T_j=25^{\circ}C$		11	100	mV
		$20V \leq V_i \leq 30V, T_j=25^{\circ}C$		2	50	
Quiescent Current	I_q	$T_j=25^{\circ}C$		3.8	6	mA
Quiescent Current Charge	ΔI_q	$14.5V \leq V_i \leq 30V, I_o=40mA$			1.5	mA
		$5mA \leq V_i \leq 150mA$			0.1	
Output noise Voltage	V_N	$10Hz \leq f \leq 100kHz$		42	200	μV
Dropout Voltage	V_d	$T_j=25^{\circ}C$		1.7		V
Ripple Rejection	RR	$17.5V \leq V_i \leq 30V, f=120Hz,$ $T_j=25^{\circ}C$	39	46		dB

Application Circuit

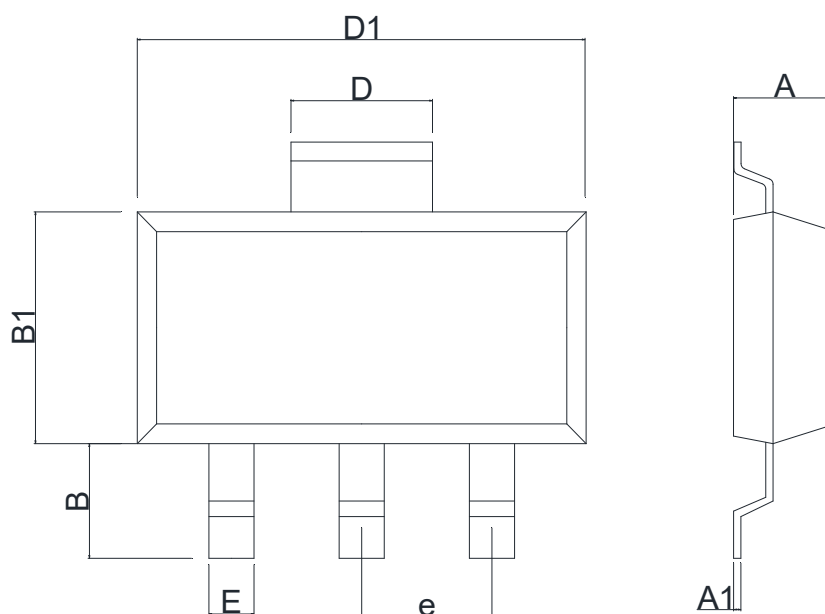


*Bypass capacitors are recommended for optimum stability and transient response and should be located as close as Possible to the regulators.

Outline Dimensions

SOT89-3

Unit: mm



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.550	0.057	0.061
A1	0.390	0.410	0.015	0.016
B	0.950	1.050	0.037	0.041
B1	2.350	2.550	0.092	0.100
E	0.350	0.450	0.013	0.017
D1	4.400	4.600	0.173	0.181
D	1.550 REF		0.061 REF	
e	1.500 (BSC)		0.059 (BSC)	

Statements

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