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Siliup Semiconductor

SP010N15GP8

100V N-Channel Power MOSFET

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
100V	15mΩ@10V	8A
	18mΩ@4.5V	

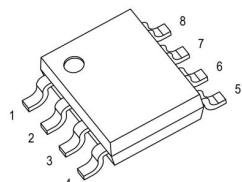
## Feature

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

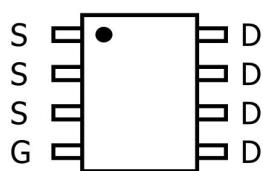
## Applications

- Power switching application
- DC-DC Converter

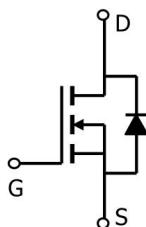
## Package



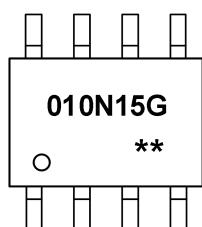
SOP-8L



## Circuit diagram



## Marking



010N15G    =Device Code  
\*\*            =Week Code



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**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current(T <sub>c</sub> =25°C)	I <sub>D</sub>	8	A
Pulsed Drain Current <sup>1</sup>	I <sub>DM</sub>	32	A
Single Pulse Avalanche Energy <sup>2</sup>	E <sub>AS</sub>	81	mJ
Total Power Dissipation	P <sub>D</sub>	2.5	W
Thermal Resistance Junction-Ambient	R <sub>θJA</sub>	50	°C/W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 150	°C

**Electrical characteristics (Ta=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V , ID=250uA	100	---	---	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , TJ=25°C	---	---	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	---	---	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , ID =250uA	1.0	1.8	2.5	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V , ID=8A	---	15	19	mΩ
		V <sub>GS</sub> =4.5V , ID=6A	---	18	24	
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V , V <sub>GS</sub> =0V , f=1MHz	---	2071	---	pF
Output Capacitance	C <sub>oss</sub>		---	241	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	21	---	
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , ID=5A	---	12.5	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	2.1	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	3.3	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =50V , V <sub>GS</sub> =10V , RG=3Ω , ID=5A	---	4.3	---	ns
Rise Time	T <sub>r</sub>		---	5.1	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	16	---	
Fall Time	T <sub>f</sub>		---	7	---	
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V , IS=1A , TJ=25°C	---	---	1.2	V

## Note :

1. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=50V,V<sub>GS</sub>=10V,L=0.5mH,R<sub>g</sub>=25mΩ



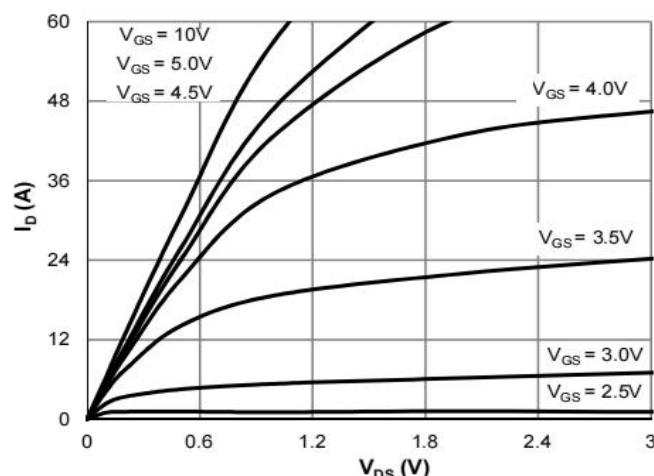
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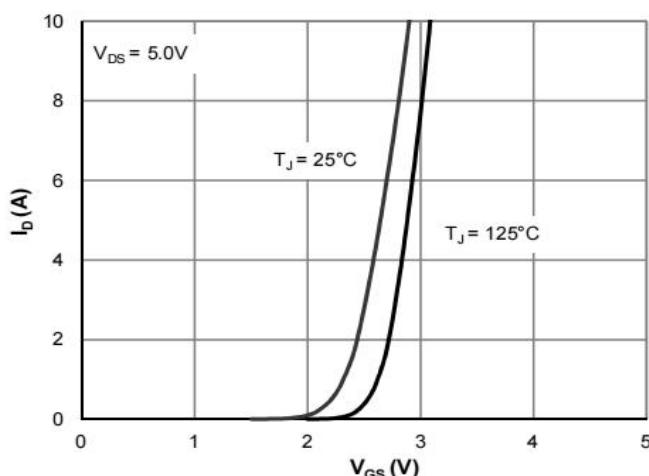
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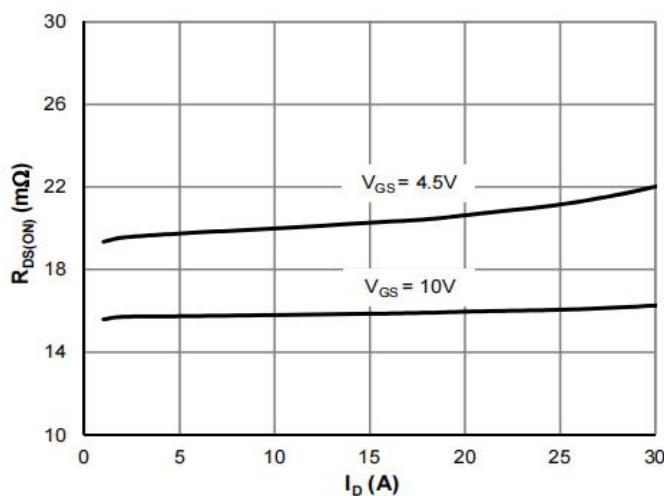
## Typical Characteristics



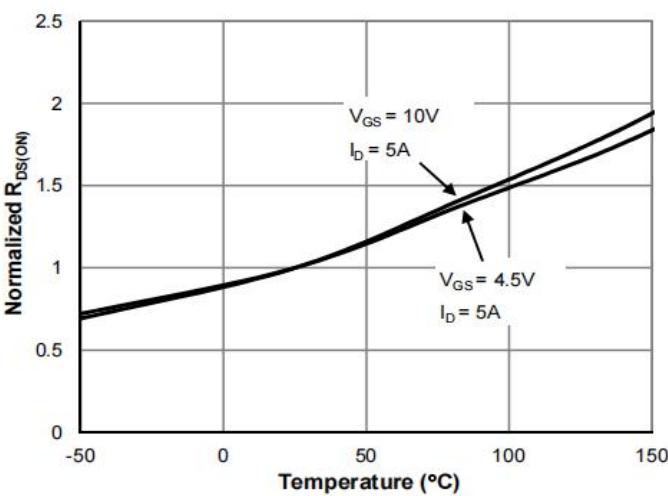
Typical Output Characteristics



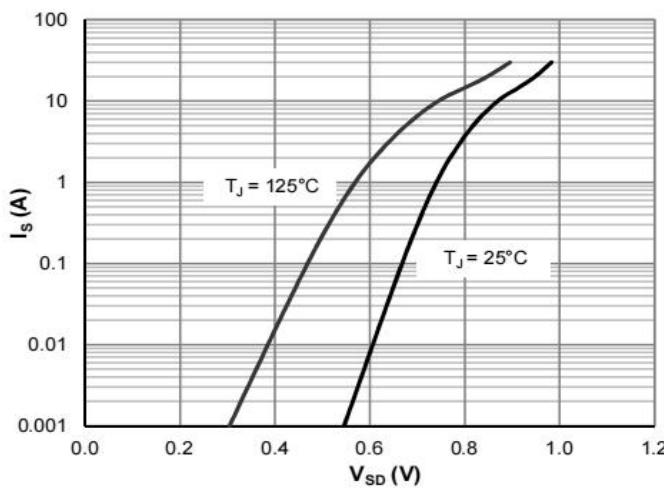
Transfer Characteristics



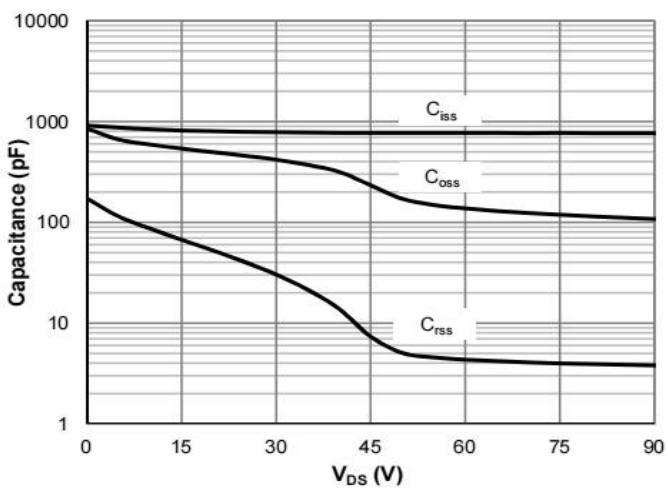
On-Resistance vs. Drain Current



On-Resistance vs. Junction Temperature



Body-Diode Characteristics



Capacitance Characteristics

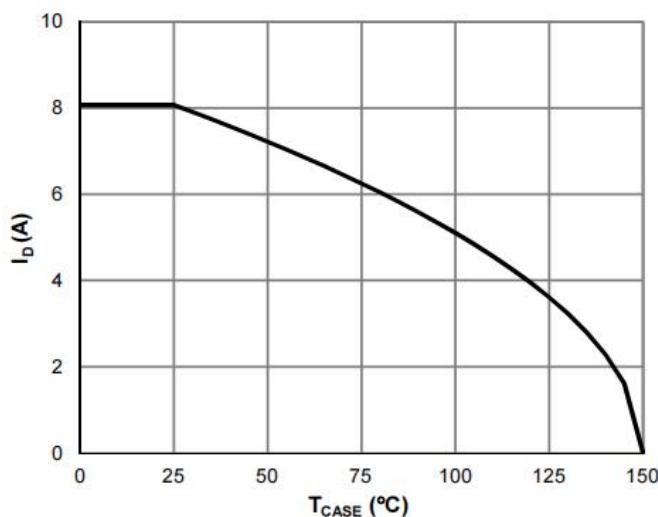


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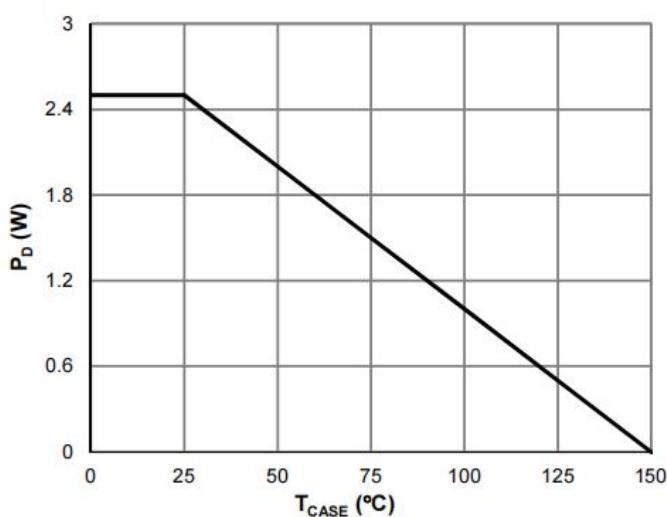
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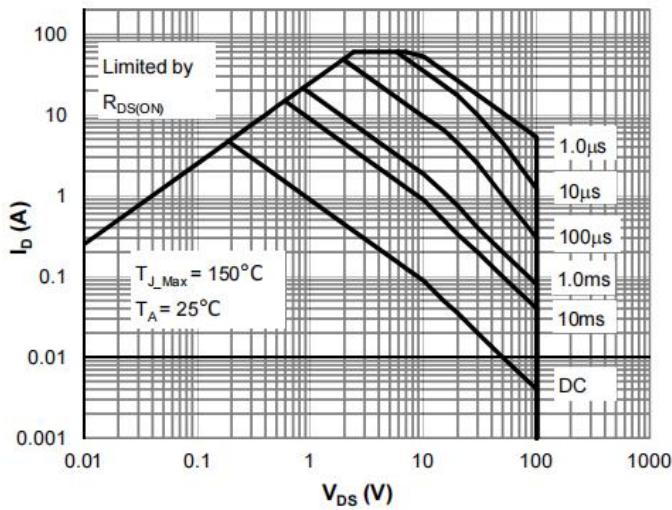
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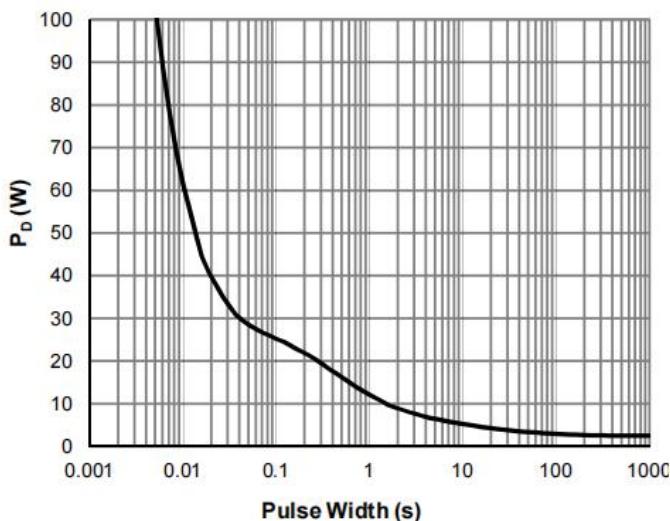
Current De-rating



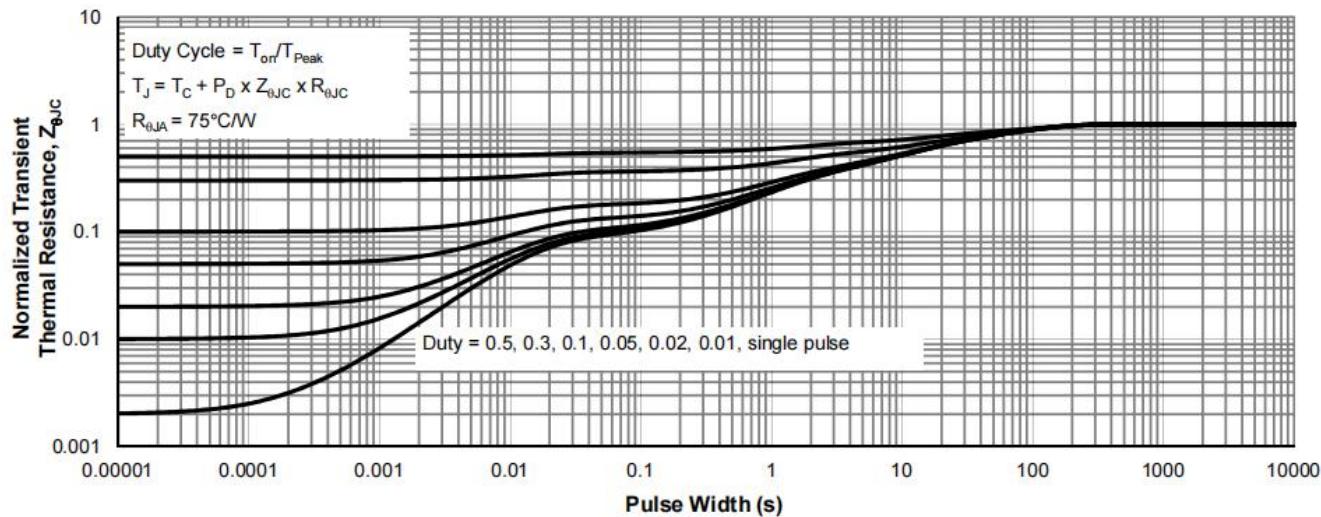
Power De-rating



Maximum Safe Operating Area



Single Pulse Power Rating, Junction-to-Case



Normalized Maximum Transient Thermal Impedance



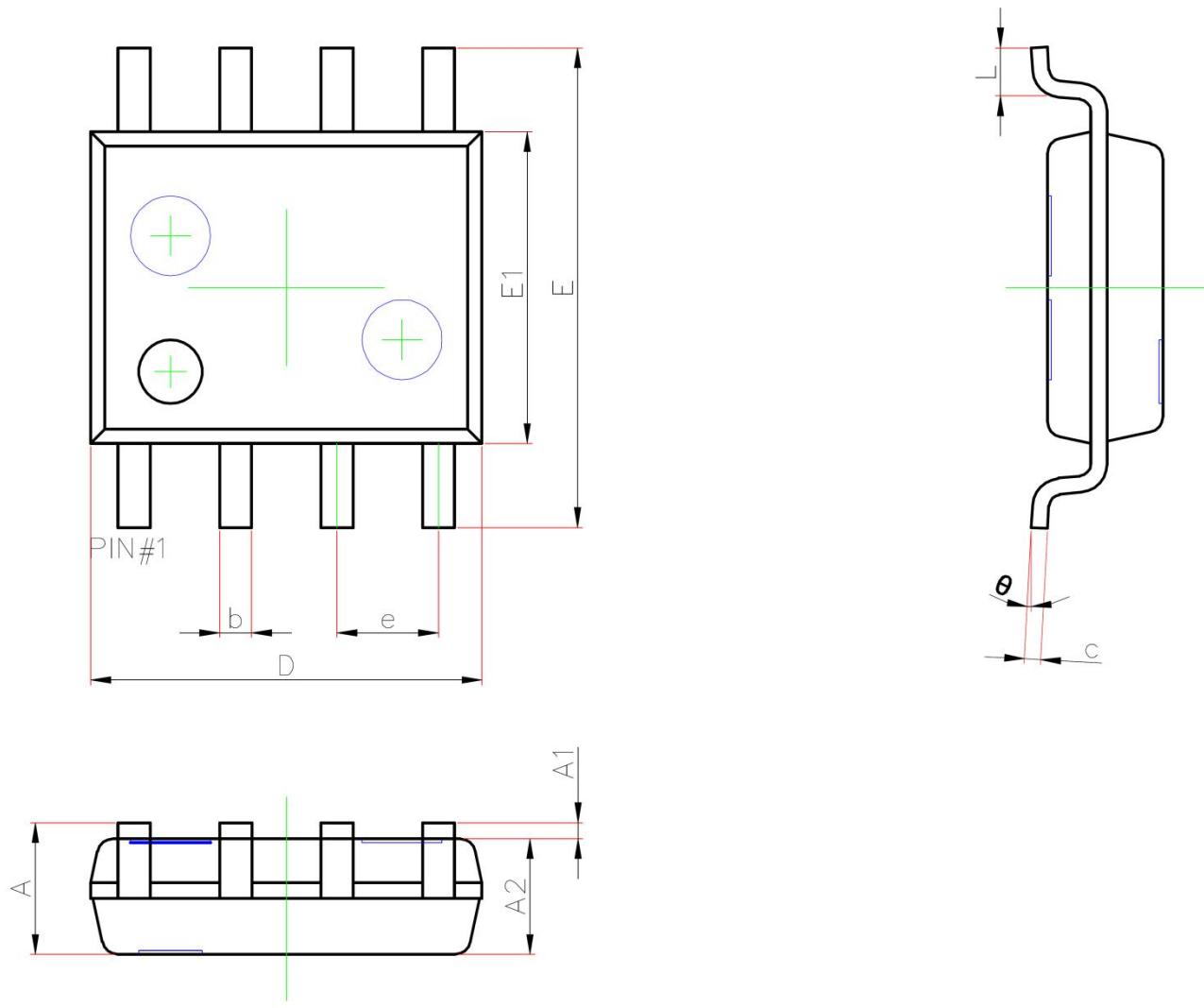
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## SOP-8 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.35	1.75
A1	0.10	0.25
A2	1.35	1.55
b	0.33	0.51
c	0.17	0.25
D	4.80	5.00
e	1.27 REF.	
E	5.80	6.20
E1	3.80	4.00
L	0.40	1.27
$\theta$	0°	8°