

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}TYP$	$I_D$
-100V	190m $\Omega$ @-10V	-3.5A
	210m $\Omega$ @-4.5V	

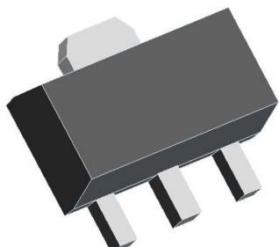


**合肥矽普半导体**  
Siliup Semiconductor Technology Co.,Ltd  
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## Feature

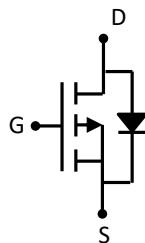
- High density cell design for ultra low  $R_{DS(on)}$
- Fast Switching Speed
- Low Gate Charge

## Package

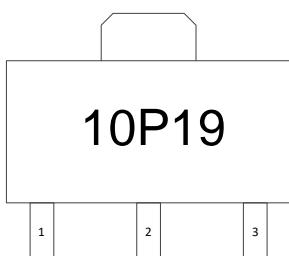


SOT-89(1:G 2:D 3:S)

## Circuit diagram



## Marking



10P19 =Device Code

## Order Information

Device	Package	Unite/Tape
SP010N190T8	SOT-89	1000

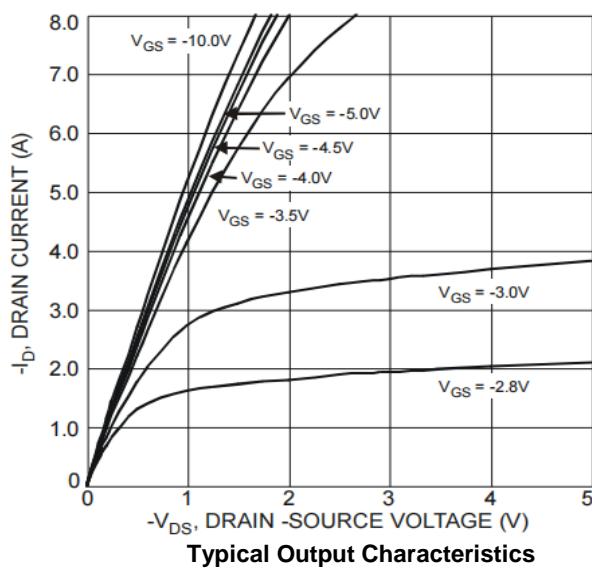
**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	-100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current (T <sub>A</sub> = 25°C)	I <sub>D</sub>	-3.5	A
Pulsed Drain Current	I <sub>DM</sub>	-14	A
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>D</sub>	2.5	W
Thermal Resistance Junction-ambient	R <sub>θJA</sub>	50	°C/W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 150	°C

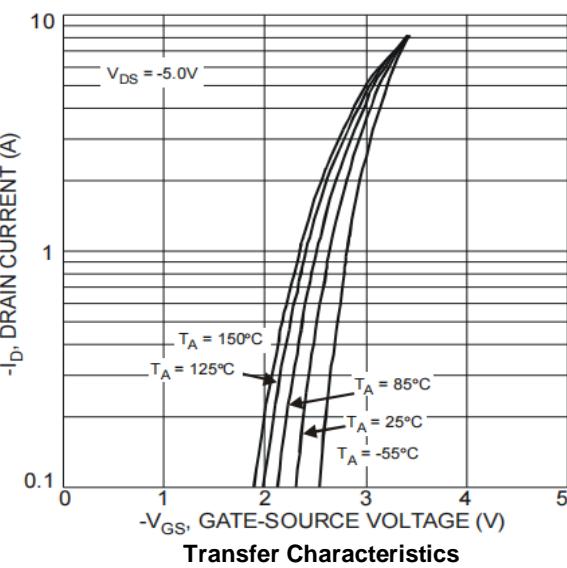
**Electrical characteristics (T<sub>A</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V , ID=-250uA	-100			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V , V <sub>GS</sub> =0V			1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , ID =-250uA	-1	-1.5	-2.5	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V , ID=-0.5A		190	250	mΩ
		V <sub>GS</sub> =-4.5V , ID=-0.4A		210	300	
<b>Dynamic characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-25V , V <sub>GS</sub> =0V , f=1MHz		1239		pF
Output Capacitance	C <sub>oss</sub>			42		
Reverse Transfer Capacitance	C <sub>rss</sub>			38		
<b>Switching Characteristics</b>						
Turn-On Delay Time	T <sub>d(on)</sub>	VDD=-50V , V <sub>GS</sub> =-10V , RG=10Ω, ID=-3A		9.1		ns
Rise Time	T <sub>r</sub>			14.9		
Turn-Off Delay Time	T <sub>d(off)</sub>			57.4		
Fall Time	T <sub>f</sub>			34.4		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-60V , V <sub>GS</sub> =-10V , ID=-3A		17.5		nC
Gate-Source Charge	Q <sub>gs</sub>			2.8		
Gate-Drain Charge	Q <sub>gd</sub>			3.2		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V , IS=-1A			-1.2	V

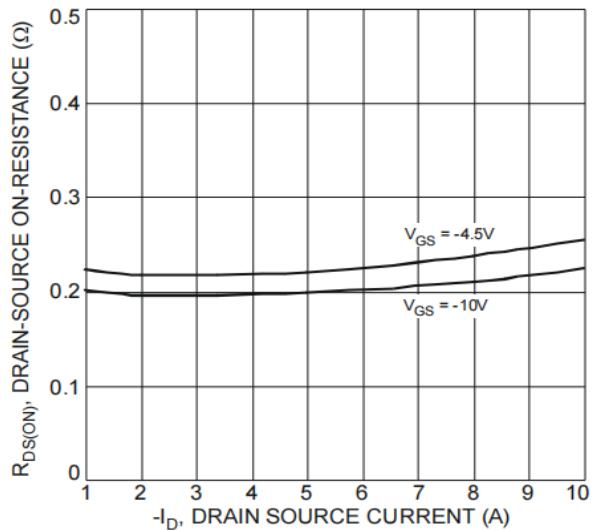
## Typical Characteristics



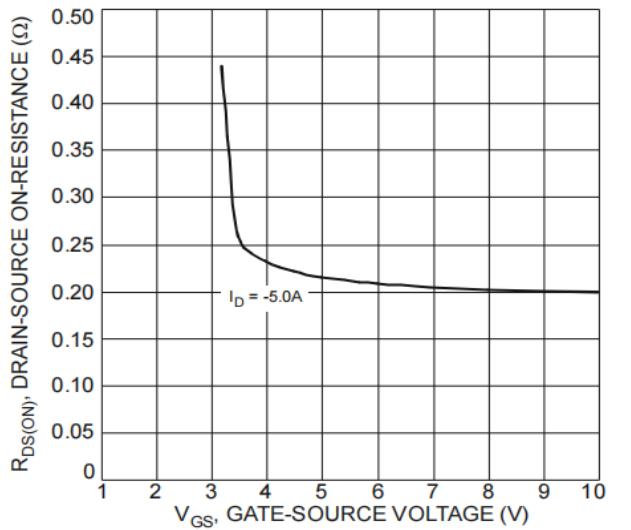
Typical Output Characteristics



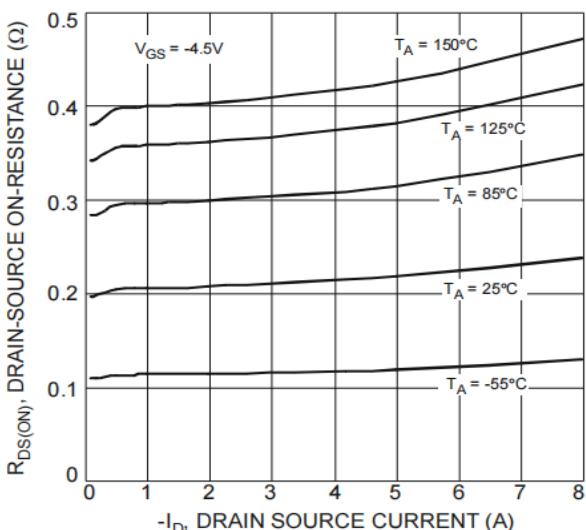
Transfer Characteristics



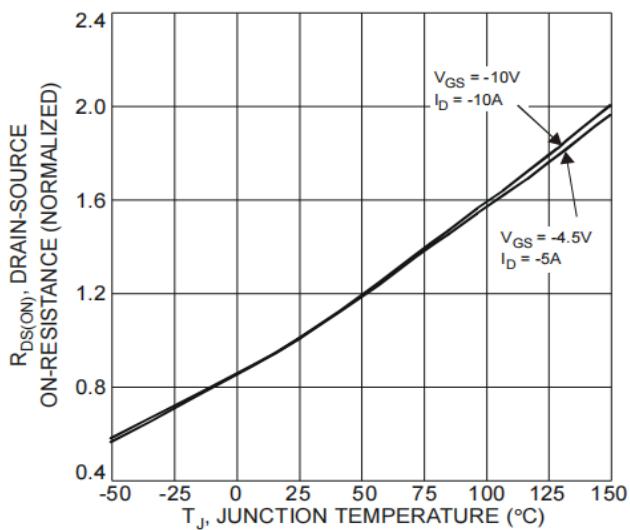
On-Resistance vs. Drain Current and Gate Voltage



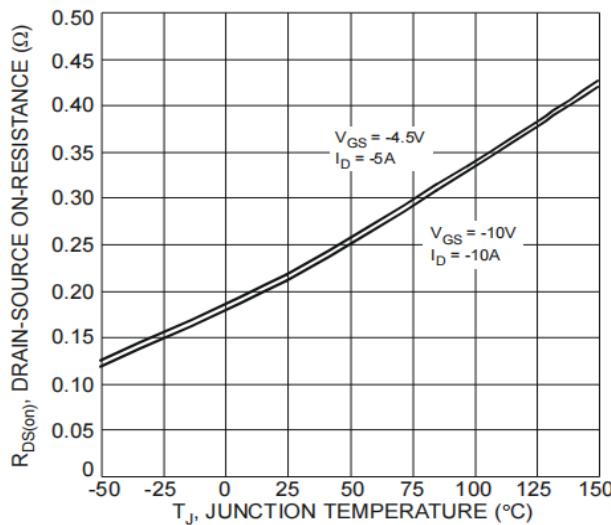
Drain-Source On-Resistance vs. Gate-Source Voltage



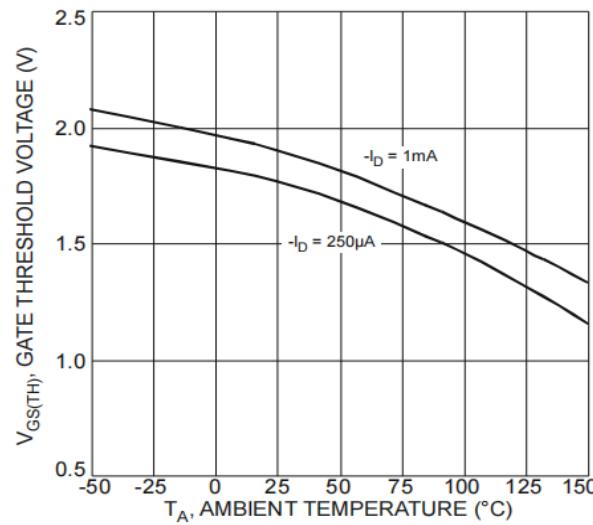
On-Resistance vs. Drain Current and Temperature



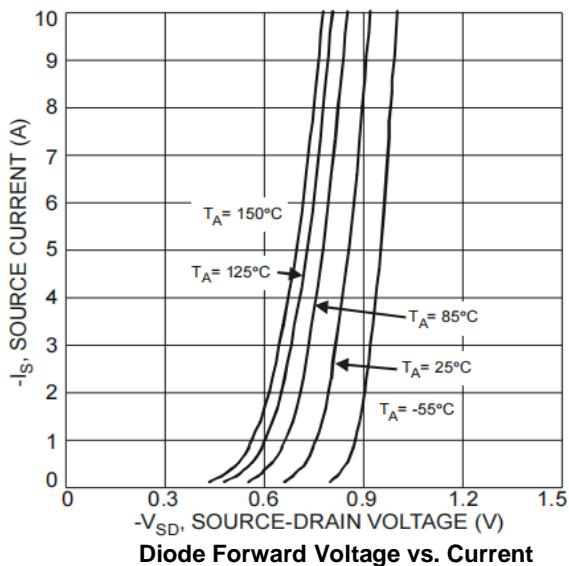
On-Resistance Variation with Temperature



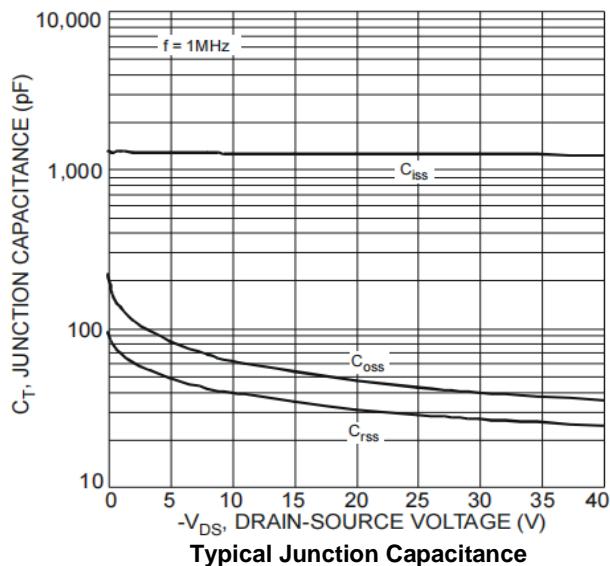
On-Resistance Variation with Temperature



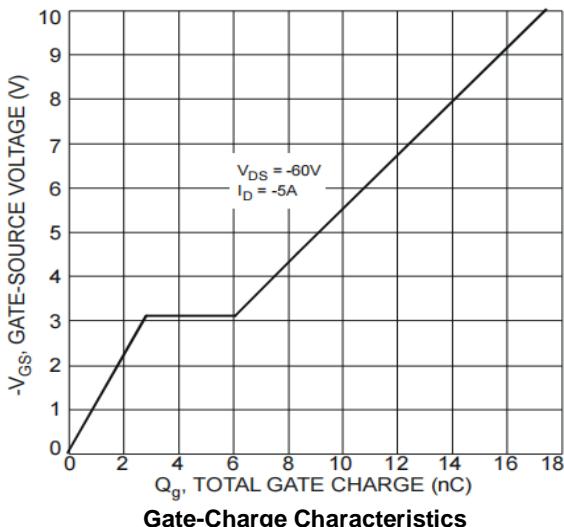
Gate Threshold Variation vs. Ambient Temperature



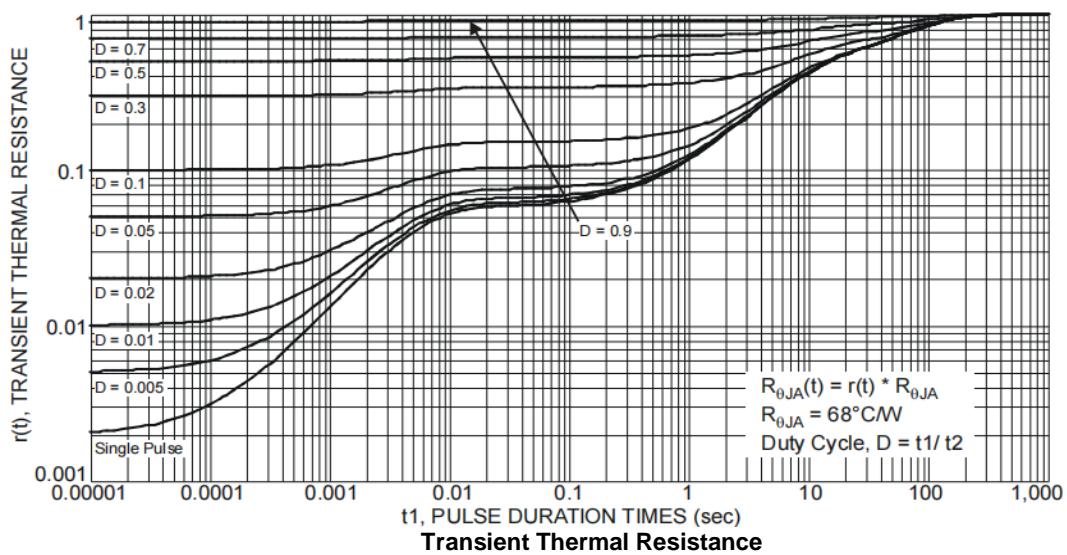
Diode Forward Voltage vs. Current



Typical Junction Capacitance

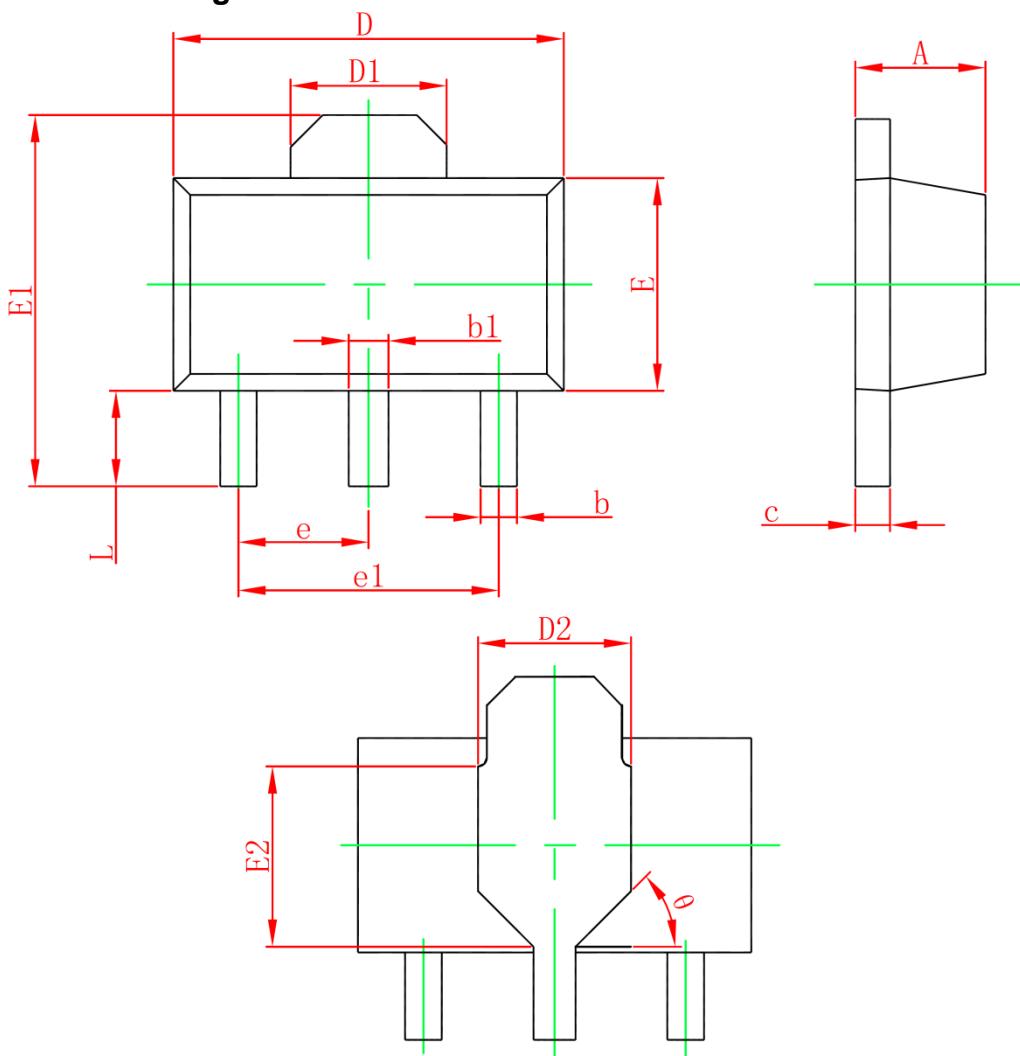


Gate-Charge Characteristics





### SOT-89 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.400	1.600
b	0.320	0.520
b1	0.400	0.580
c	0.350	0.440
D	4.400	4.600
D1		1.550 REF.
D2		1.750 REF.
E	2.300	2.600
E1	3.940	4.250
E2		1.900 REF.
e		1.500 TYP.
e1		3.000 TYP.
L	0.900	1.200
θ		45°