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Siliup Semiconductor

SP010N04BGTQ

100V N-Channel Power MOSFET

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
100V	4.9mΩ@10V	125A
	6.4mΩ@4.5V	

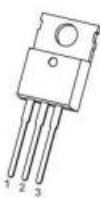
## Feature

- Fast Switching
- Low Gate Charge and Rdson
- Advanced Split Gate Trench Technology
- 100% Single Pulse avalanche energy Test

## Applications

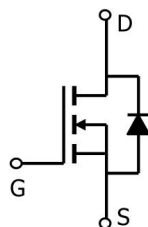
- Power switching application
- PWM Application
- DC-DC Converter

## Package

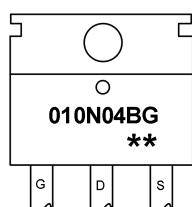


TO-220-3L-C(1:G 2:D 3:S)

## Circuit diagram



## Marking



010N04BG =Device Code  
\*\* =Week Code



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**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current(Tc=25°C)	I <sub>D</sub>	125	A
Pulsed Drain Current <sup>2</sup>	I <sub>DM</sub>	500	A
Single Pulse Avalanche Energy <sup>3</sup>	E <sub>AS</sub>	100	mJ
Avalanche Current	I <sub>AS</sub>	20	A
Total Power Dissipation <sup>4</sup> (Tc=25°C)	P <sub>D</sub>	185	W
Thermal Resistance Junction-Case <sup>1</sup>	R <sub>θJC</sub>	0.67	°C/W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 150	°C

**Electrical characteristics (Ta=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	VGS=0V , ID=250uA	100	---	---	V
Drain-Source Leakage Current	I <sub>DSS</sub>	VDS=80V , VGS=0V , TJ=25°C	---	---	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	VGS=±20V , VDS=0V	---	---	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	VGS=VDS , ID=250uA	1.0	2.0	3.0	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	VGS=10V , ID=30A	---	4.9	6.1	mΩ
		VGS=4.5V , ID=20A	---	6.4	8.5	
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	VDS=50V , VGS=0V , f=1MHz	---	4850	---	pF
Output Capacitance	C <sub>oss</sub>		---	480	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	34	---	
<b>Switching Characteristics</b>						
Total Gate Charge (4.5V)	Q <sub>g</sub>	VDS=50V , VGS=10V , ID=50A	---	97	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	27	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	30	---	
Turn-On Delay Time	T <sub>d(on)</sub>	VDD=50V , VGS=10V , RG=3Ω , ID=50A	---	24	---	ns
Rise Time	T <sub>r</sub>		---	13	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	47	---	
Fall Time	T <sub>f</sub>		---	11	---	
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	VGS=0V , IS=1A , TJ=25°C	---	---	1.2	V

## Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
3. The EAS data shows Max. rating . The test condition is VDD=50V,VGS=10V,L=0.5mH,IAS=20A
4. The power dissipation is limited by 150°C junction temperature



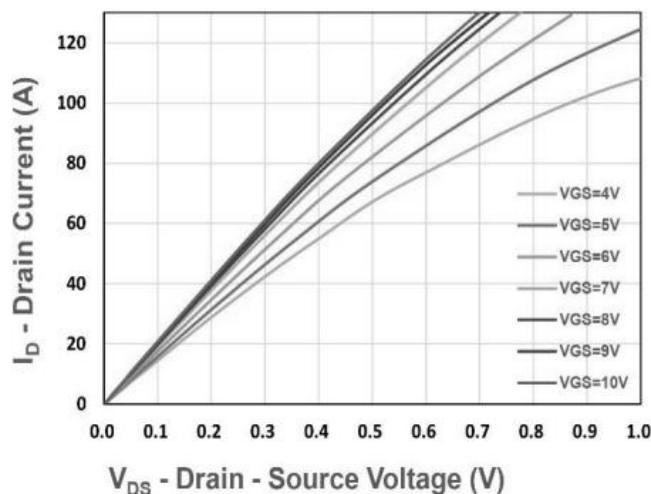
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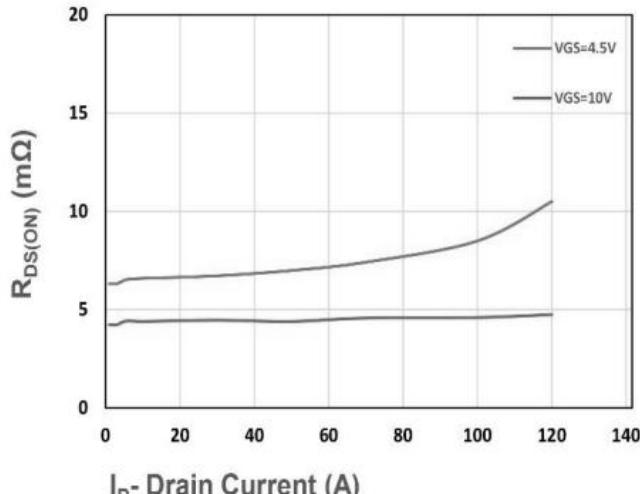
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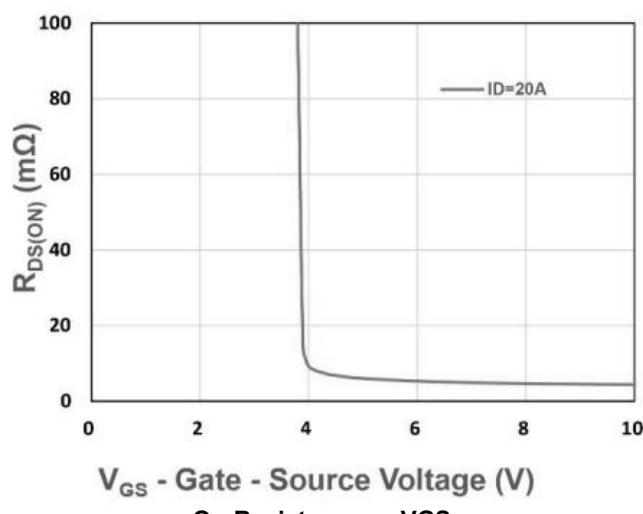
## Typical Characteristics



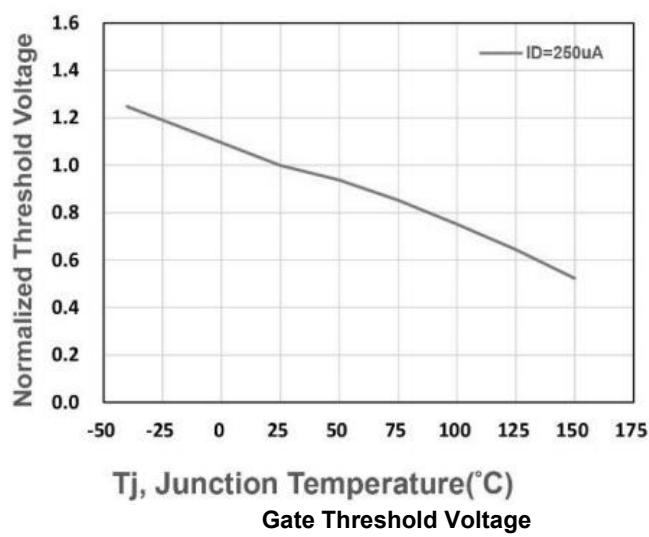
Typical Output Characteristics



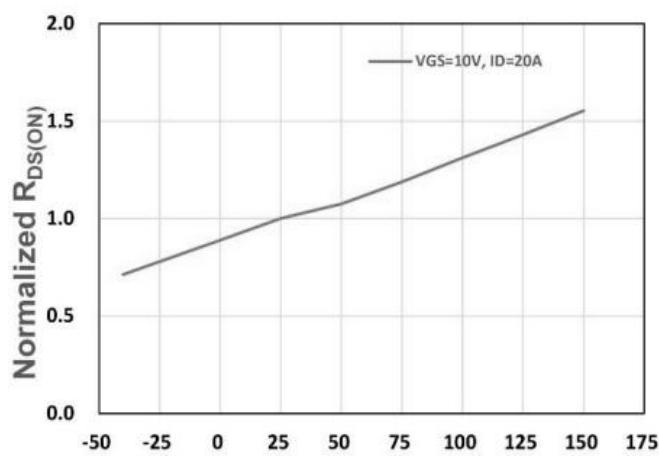
On-Resistance vs.ID



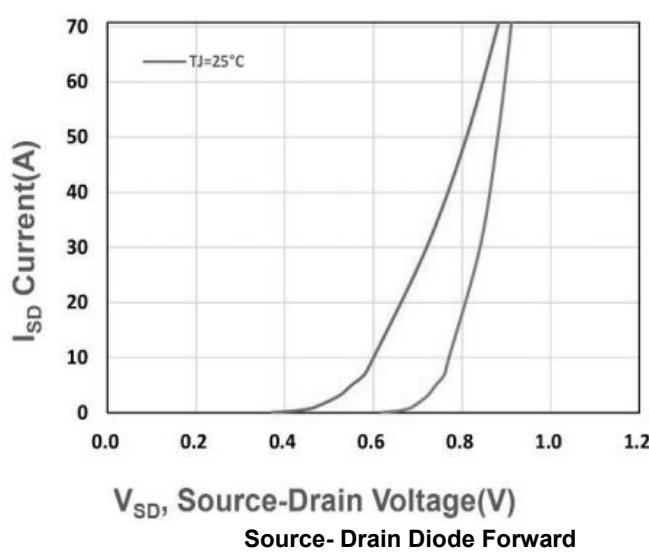
On-Resistance vs.VGS



T<sub>j</sub>, Junction Temperature(°C)  
Gate Threshold Voltage



T<sub>j</sub> , Junction Temperature(°C)  
Drain-Source On-Resistance



V<sub>SD</sub>, Source-Drain Voltage(V)  
Source- Drain Diode Forward

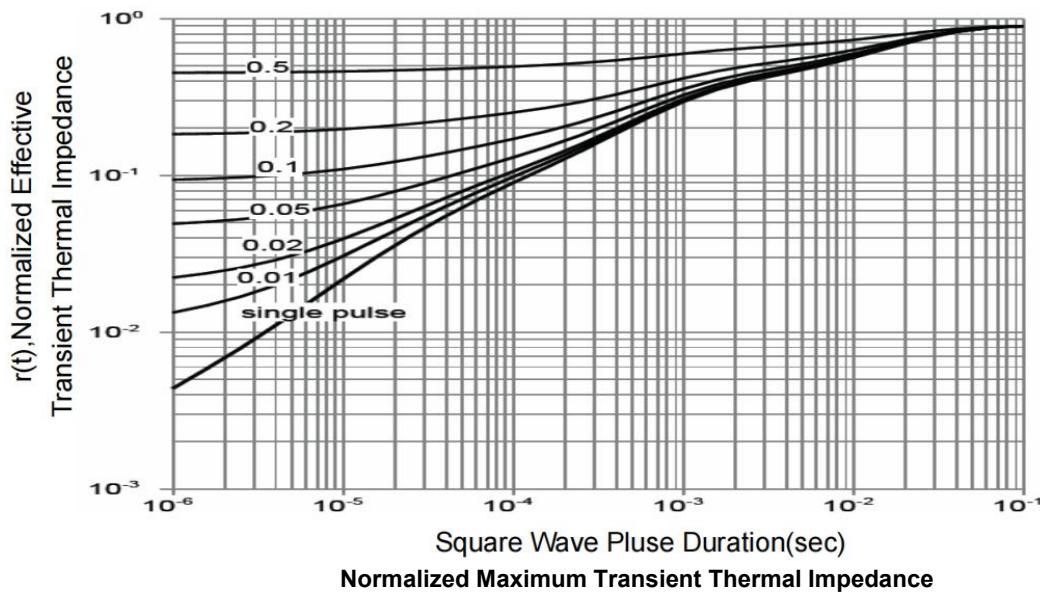
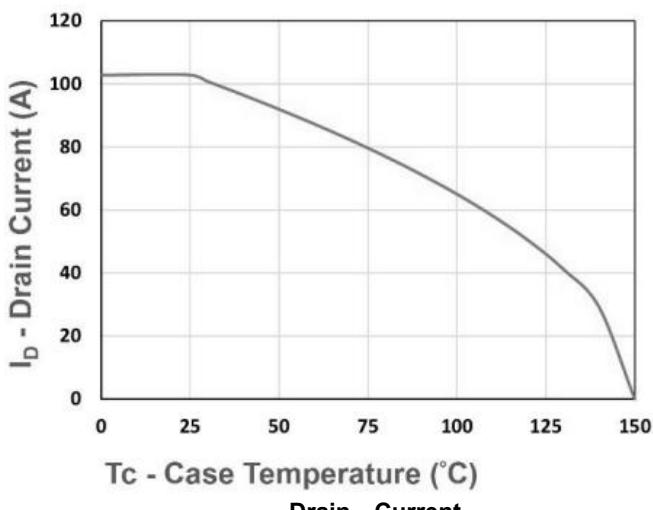
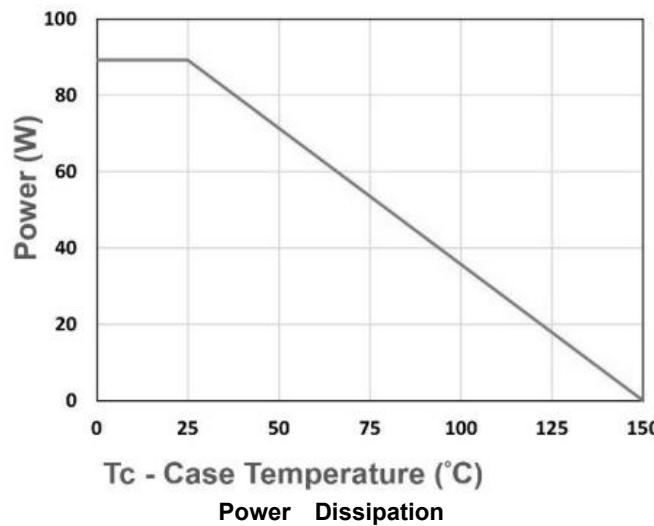
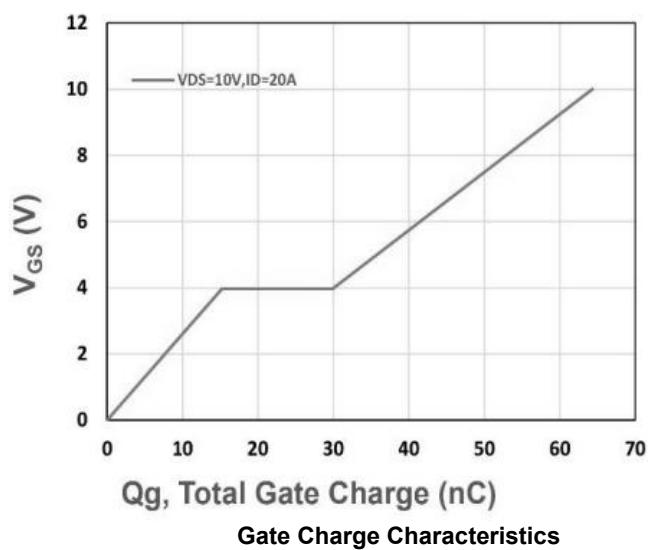
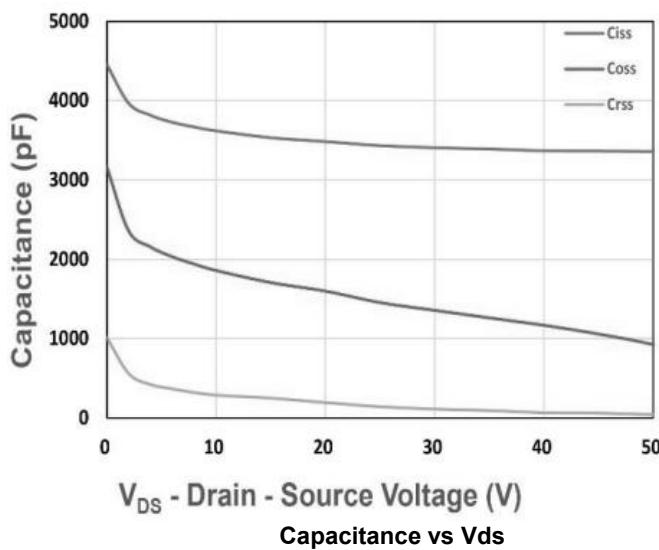


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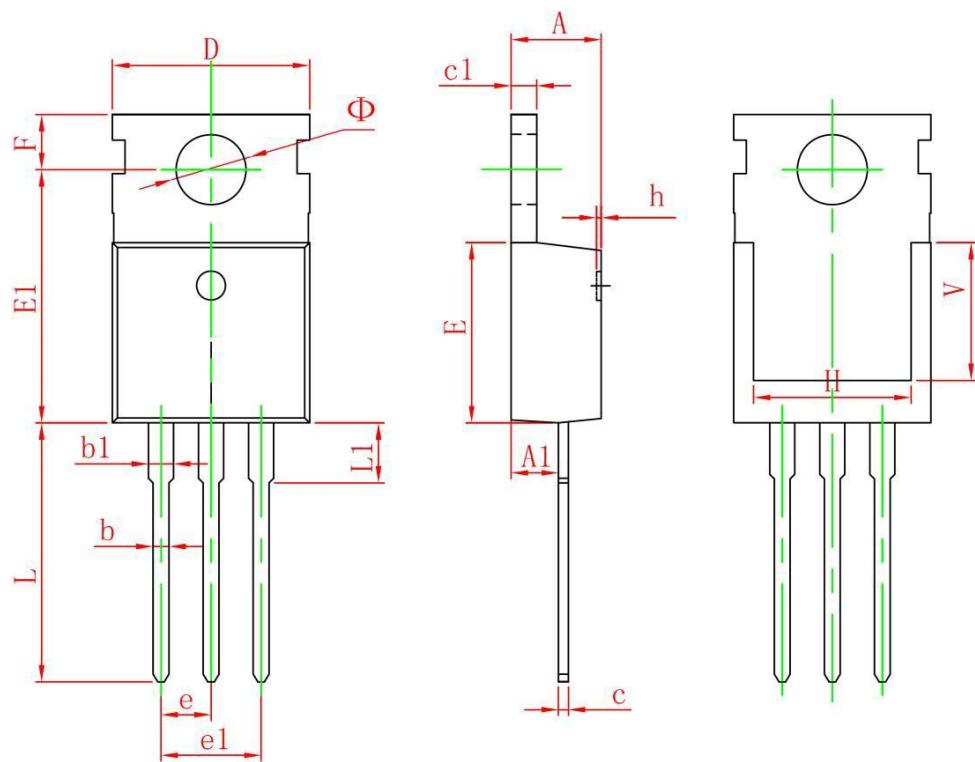


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## TO-220-3L-C Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.950	9.750	0.352	0.384
E1	12.650	13.050	0.498	0.514
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	6.900 REF.		0.276 REF.	
Φ	3.400	3.800	0.134	0.150