



Product Specification

XBLW AO4409

P-Channel Enhancement Mode MOSFET

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Description

The AO4409 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

Feature:

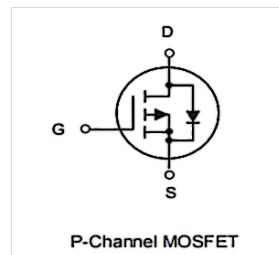
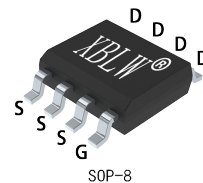
- $V_{DS} = -30V$ $I_D = -15A$
- $R_{DS(ON)} < 8.7m\Omega$ @ $V_{GS} = 10V$

Applications

- Battery protection Load switch
- Uninterruptible power supply

Ordering Information

Product Model	Package Type	Marking	Packing	Packing Qty
XBLW AO4409	SOP-8	AO4409	Tape	3000Pcs/Reel



Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-15	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-11	A
I_{DM}	Pulsed Drain Current ²	-56	A
EAS	Single Pulse Avalanche Energy ³	151	mJ
I_{AS}	Avalanche Current	-55	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation ⁴	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ ($t \leq 10s$)	40	°C/W
	Thermal Resistance Junction-Ambient ¹	75	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	24	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA	---	-0.018	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-12A	---	5.8	8.7	mΩ
		V _{GS} =-4.5V , I _D =-10A	---	8.5	13.5	
V _{GS(th)}	Gate Threshold Voltage		-1.2	---	-2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =-250uA	---	5.04	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-24V , V _{GS} =0V , T _J =55°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V	---	---	± 100	nA
g _{fs}	Forward Transconductance	V _{DS} =-5V , I _D =-12A	---	25	---	S
Q _g	Total Gate Charge (-4.5V)	V _{DS} =-15V V _{GS} =-4.5V I _D =-12A	---	30	---	nC
Q _{gs}	Gate-Source Charge		---	10	---	
Q _{gd}	Gate-Drain Charge		---	10.4	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =-15V V _{GS} =-10V R _G =3.3 I _D =-1A	---	9.4	---	ns
T _r	Rise Time		---	10.2	---	
T _{d(off)}	Turn-Off Delay Time		---	117	---	
T _f	Fall Time		---	24	---	
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	3448	---	pF
C _{oss}	Output Capacitance		---	508	---	
C _{rss}	Reverse Transfer Capacitance		---	421	---	
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	---	---	-14	A
I _{SM}	Pulsed Source Current ^{2,5}		---	---	-56	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-10A , dI/dt=100A/μs ,	---	19.4	---	nS
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	9.1	---	nC

Note :

1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.1mH,I_{AS}=-

55A 4.The power dissipation is limited by 150°C junction temperature

5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

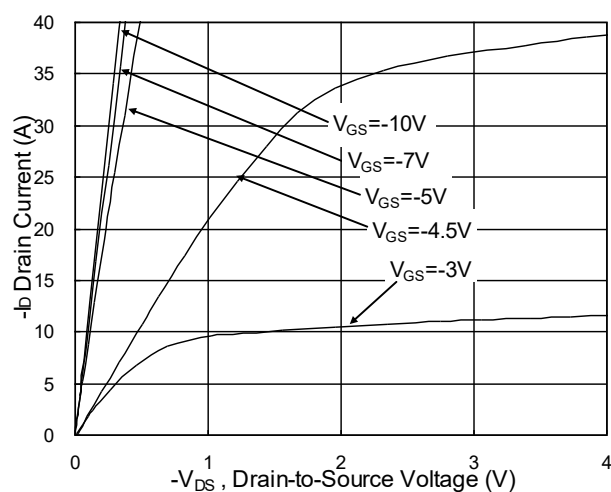


Fig.1 Typical Output Characteristics

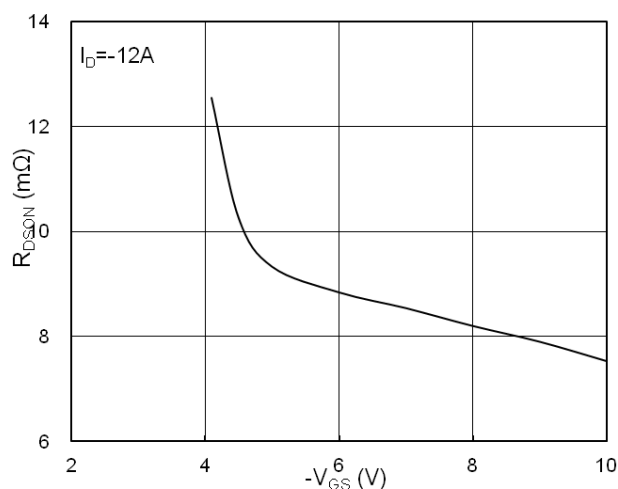


Fig.2 On-Resistance v.s Gate-Source

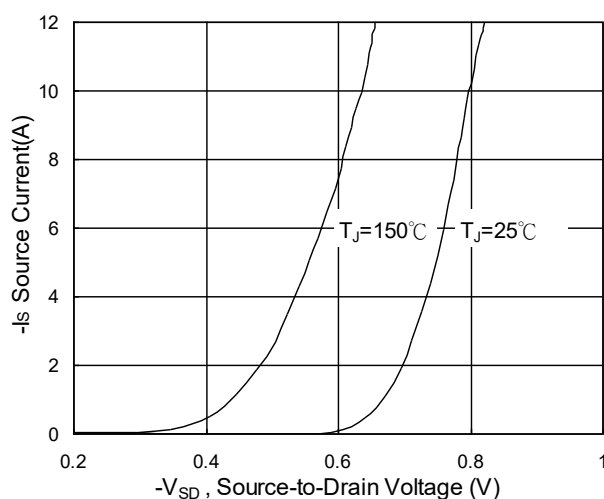


Fig.3 Forward Characteristics Of Reverse

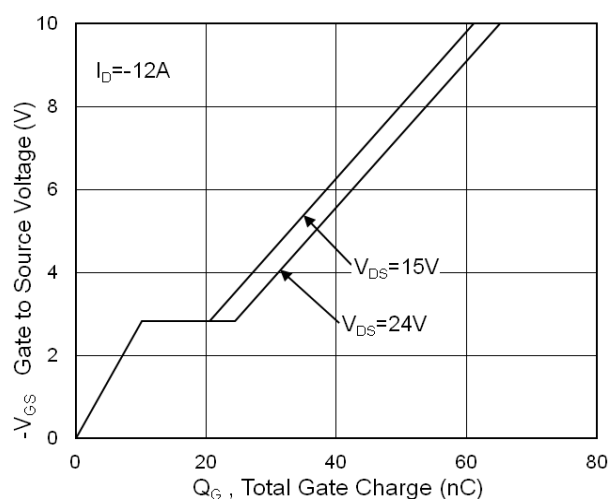


Fig.4 Gate-Charge Characteristics

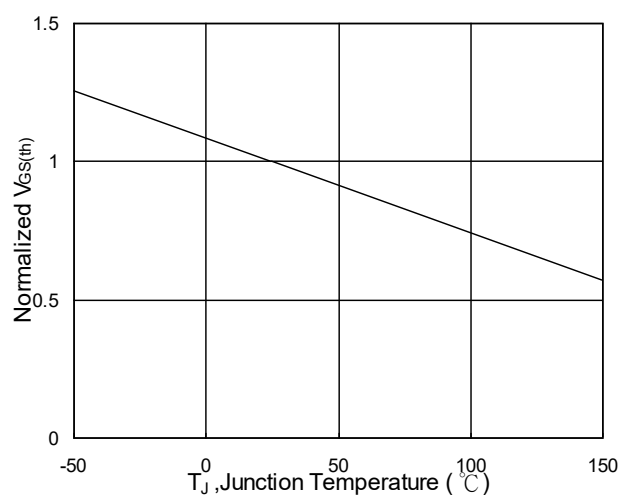


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

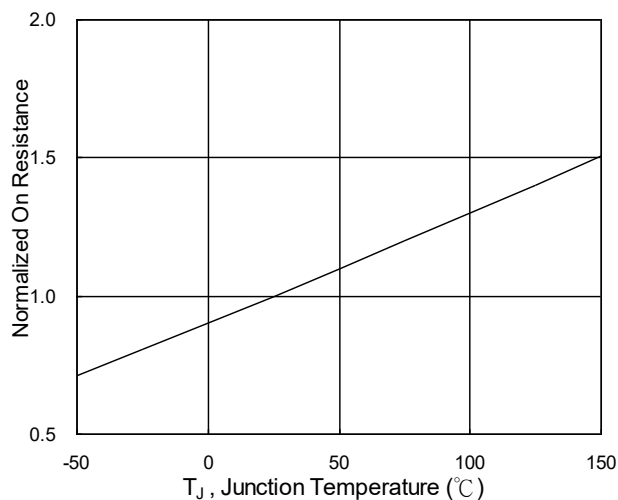


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

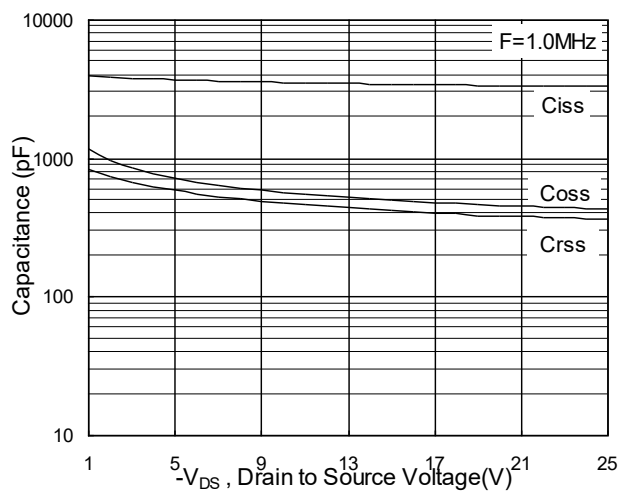


Fig.7 Capacitance

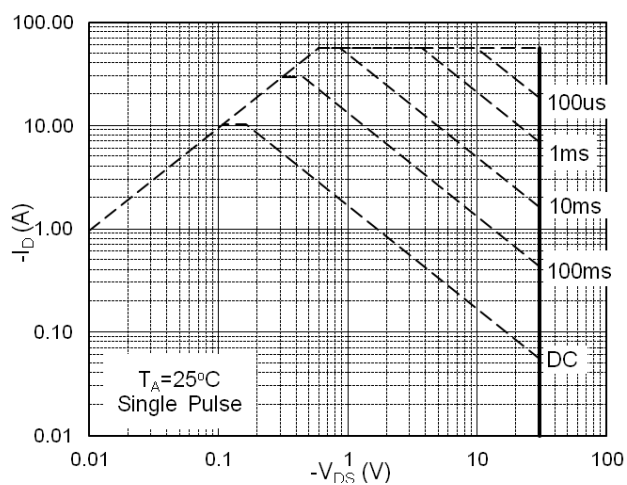


Fig.8 Safe Operating Area

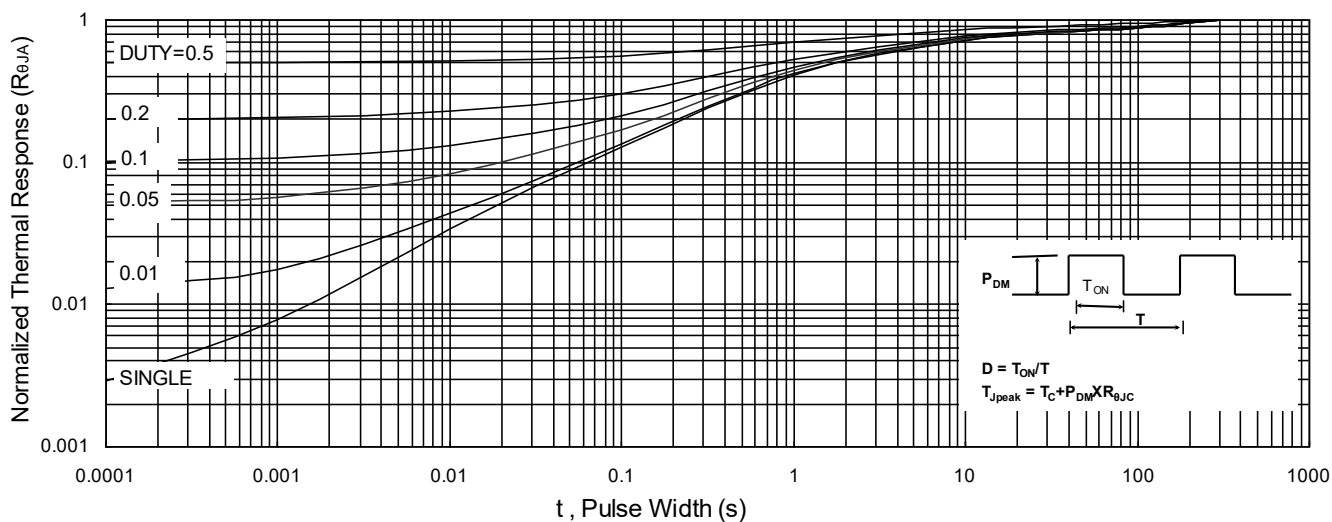


Fig.9 Normalized Maximum Transient Thermal Impedance

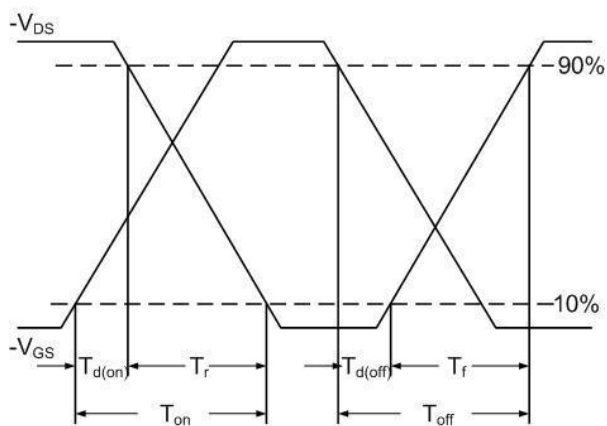


Fig.10 Switching Time Waveform

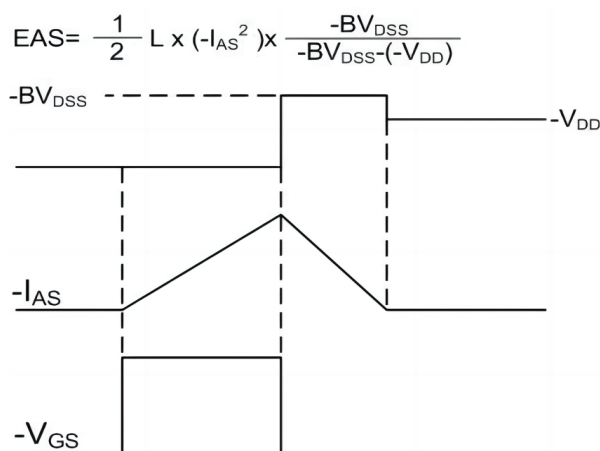
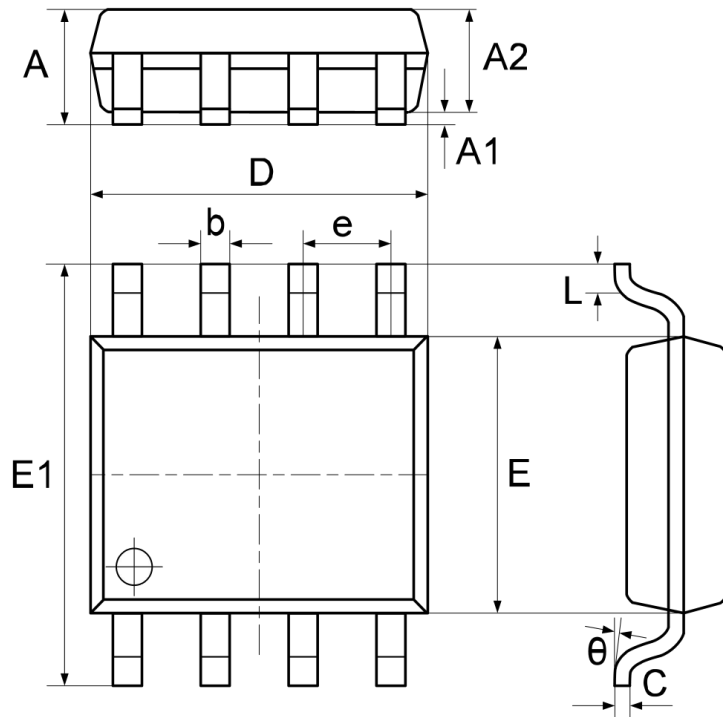


Fig.11 Unclamped Inductive Switching Waveform

Package Information

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.370	1.670	0.056	0.068
A1	0.070	0.170	0.003	0.007
A2	1.300	1.500	0.053	0.061
b	0.306	0.506	0.013	0.021
C	0.203 typ.		0.008 typ.	
D	4.700	5.100	0.192	0.208
E	3.820	4.020	0.156	0.164
E1	5.800	6.200	0.237	0.253
e	1.270 typ.		0.050 typ.	
L	0.450	0.750	0.018	0.306
θ	0°	8°	0°	8°

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