

**N-channel 650V, 5A Power MOSFET****Description**

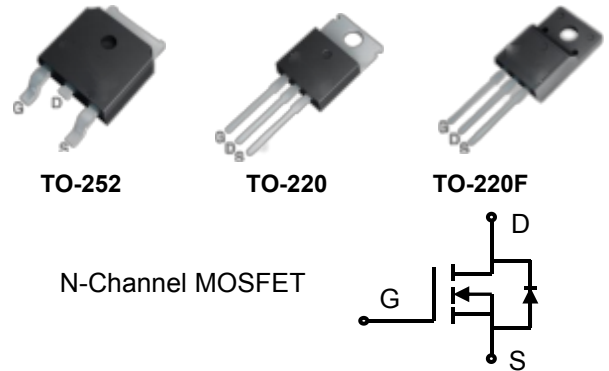
The Power MOSFET is fabricated using the advanced planar VDMOS technology. The resulting device has low conduction resistance, superior switching performance and high avalanche energy.

Features

- ◆ Low $R_{DS(on)}$
- ◆ Low gate charge (typ. $Q_g = 12$ nC)
- ◆ 100% UIS tested
- ◆ RoHS compliant

Applications

- ◆ Power factor correction.
- ◆ Switched mode power supplies.
- ◆ LED driver.

**Product Summary**

V_{DSS}	650V
I_D	5A
$R_{DS(on),max}$	2.70 Ω
$Q_{g,typ}$	12 nC

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	650	V
Continuous drain current ($T_C = 25^\circ\text{C}$)	I_D	5	A
($T_C = 100^\circ\text{C}$)		2.5	A
Pulsed drain current ¹⁾	I_{DM}	16	A
Gate-Source voltage	V_{GSS}	± 30	V
Avalanche energy, single pulse ²⁾	E_{AS}	198	mJ
Peak diode recovery dv/dt ³⁾	dv/dt	5	V/ns
Power Dissipation TO-220F ($T_C = 25^\circ\text{C}$)	P_D	32	W
Derate above 25°C		0.26	W/ $^\circ\text{C}$
Power Dissipation TO-220\ TO-251\ TO-252\ TO-262 ($T_C = 25^\circ\text{C}$)		77	W
Derate above 25°C		0.61	W/ $^\circ\text{C}$
Operating junction and storage temperature range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$
Continuous diode forward current	I_S	5	A
Diode pulse current	$I_{S,pulse}$	16	A

Thermal Characteristics

Parameter	Symbol	Value		Unit
		TO-220F	TO-220 \ TO-252	
Thermal resistance, Junction-to-case	$R_{\theta JC}$	3.8	1.62	$^\circ\text{C/W}$
Thermal resistance, Junction-to-ambient	$R_{\theta JA}$	62.5	110	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Tube	Units/Reel
BCC5N65	TO-220	YTC5N65	50	
BCT5N65	TO-220F	YTD5N65	50	
BCD5N65	TO-252	YTG5N65		2500

Electrical Characteristics

$T_c = 25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25 mA	650	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25 mA	2	-	5	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V, T _j = 25°C T _j = 125°C	- -	-	1 100	μA
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =2 A	-	2.50	2.70	Ω
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	-	600	-	pF
Output capacitance	C _{oss}		-	55	-	
Reverse transfer capacitance	C _{rss}		-	3.2	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 325 V, I _D = 5 A R _G = 10 Ω, V _{GS} =15 V	-	12	-	ns
Rise time	t _r		-	31	-	
Turn-off delay time	t _{d(off)}		-	42	-	
Fall time	t _f		-	15	-	
Gate charge characteristics						
Gate to source charge	Q _{gs}	V _{DD} =520 V, I _D =5 A, V _{GS} =0 to 10 V	-	3.2	-	nC
Gate to drain charge	Q _{gd}		-	5.1	-	
Gate charge total	Q _g		-	12	-	
Gate plateau voltage	V _{plateau}		-	6	-	V
Reverse diode characteristics						
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =5 A	-	-	1.5	V
Reverse recovery time	t _{rr}	V _R =400 V, I _F =5 A, dI _F /dt=100 A/μs	-	282	-	ns
Reverse recovery charge	Q _{rr}		-	1.4	-	μC
Peak reverse recovery current	I _{rrm}		-	10	-	A

Notes:

- Pulse width limited by maximum junction temperature.
- $L=10\text{mH}$, $I_{AS} = 6.3\text{A}$, Starting $T_j = 25^\circ\text{C}$.
- $I_{SD} = 5\text{A}$, $dI/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DS}$, Starting $T_j = 25^\circ\text{C}$.

Electrical Characteristics Diagrams

Figure 1. Typical Output Characteristics

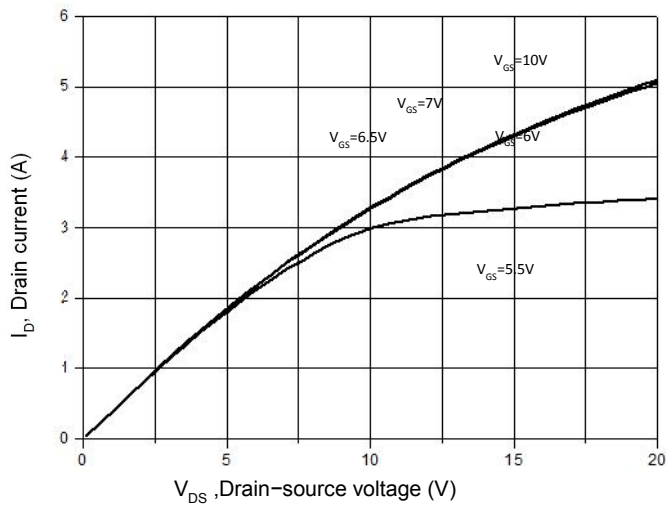


Figure 2. Transfer Characteristics

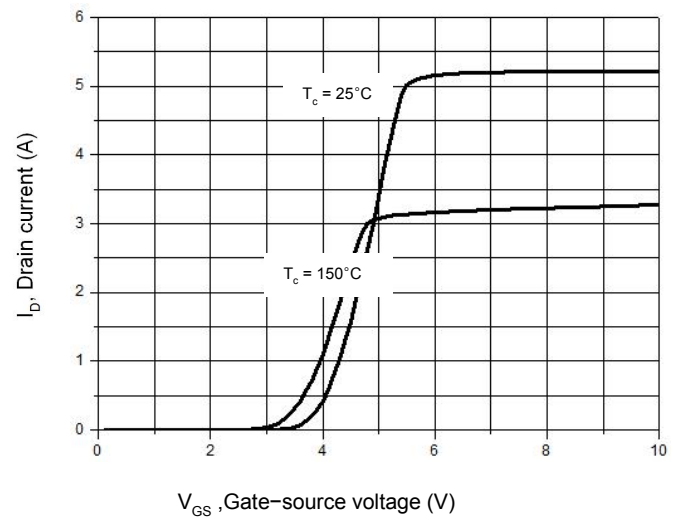


Figure 3. On-Resistance Variation vs. Drain Current

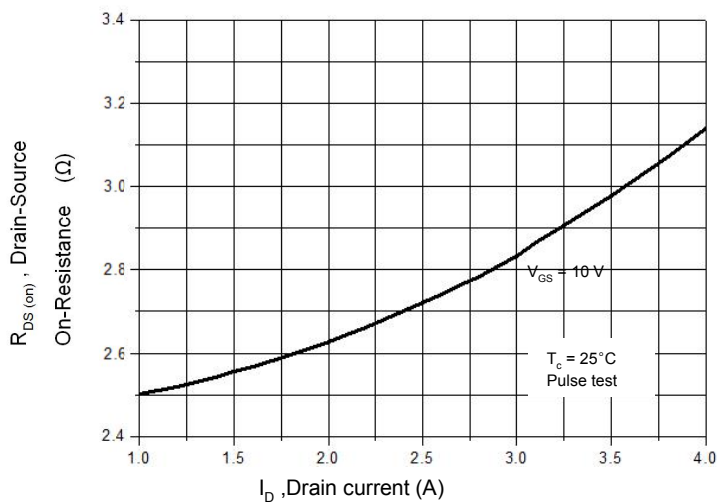


Figure 4. Threshold Voltage vs. Temperature

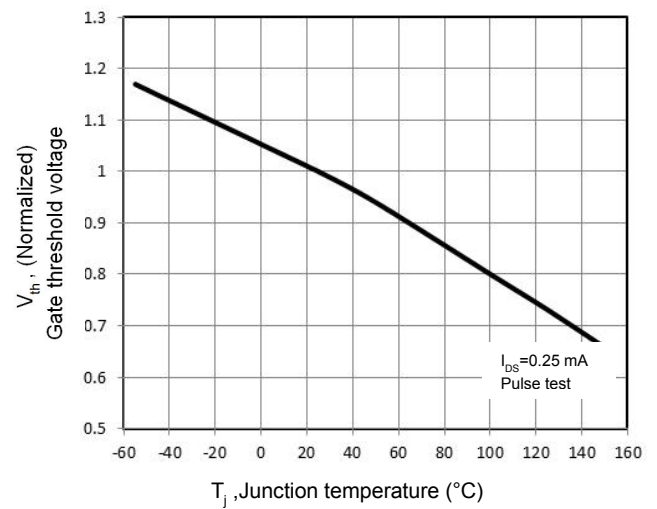


Figure 5. Breakdown Voltage vs. Temperature

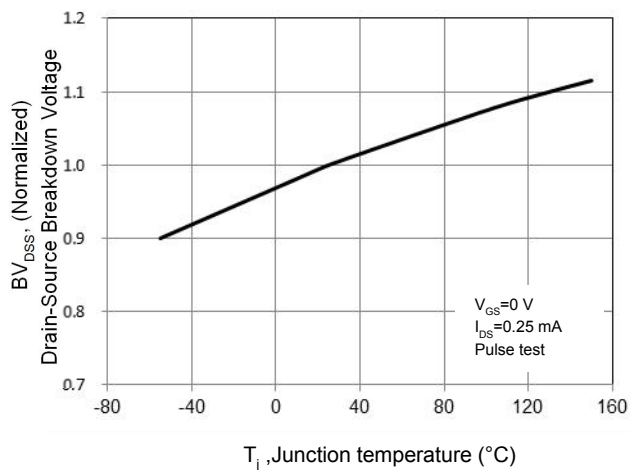


Figure 6. On-Resistance vs. Temperature

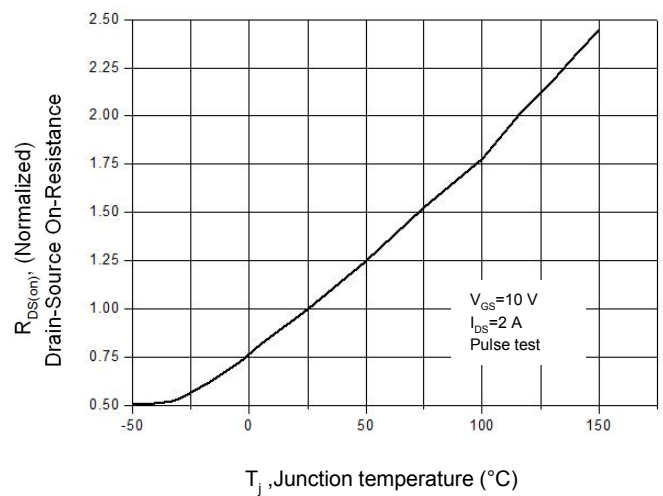


Figure 7. Capacitance Characteristics

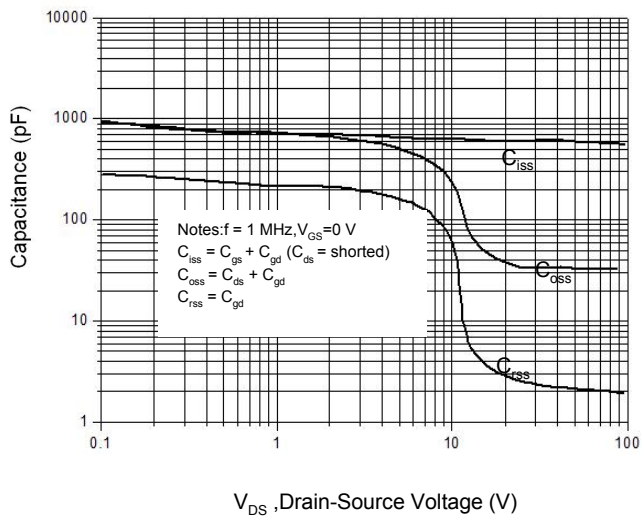


Figure 8. Gate Charge Characteristic

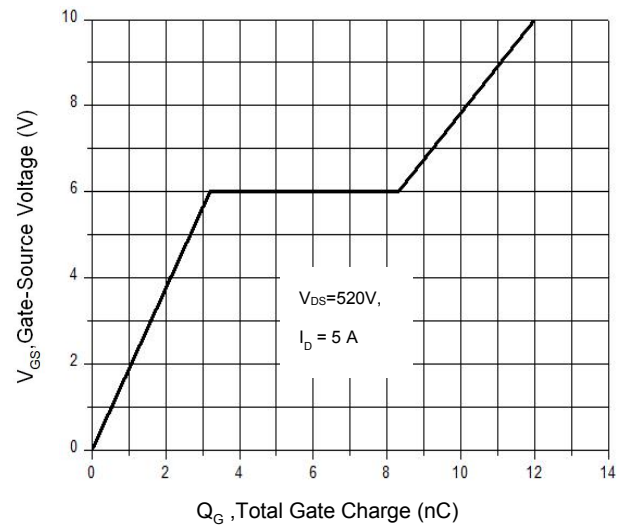


Figure 9. Maximum Safe Operating Area
TO-220F

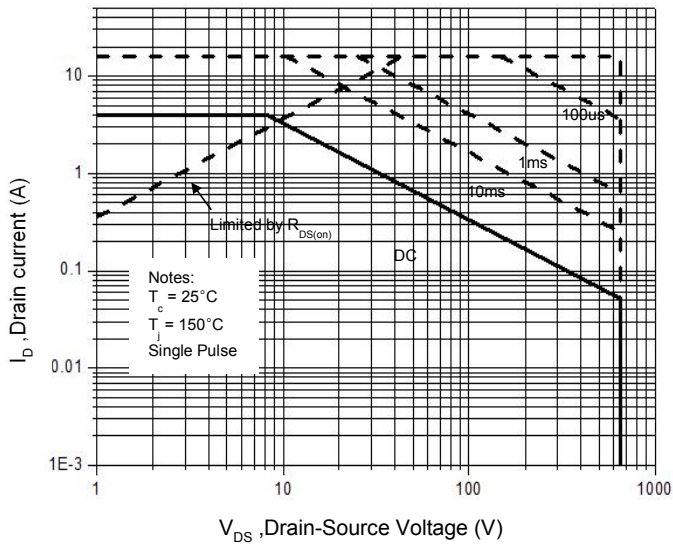


Figure 10. Maximum Safe Operating Area
TO-220/ TO-251/TO-252/TO-262

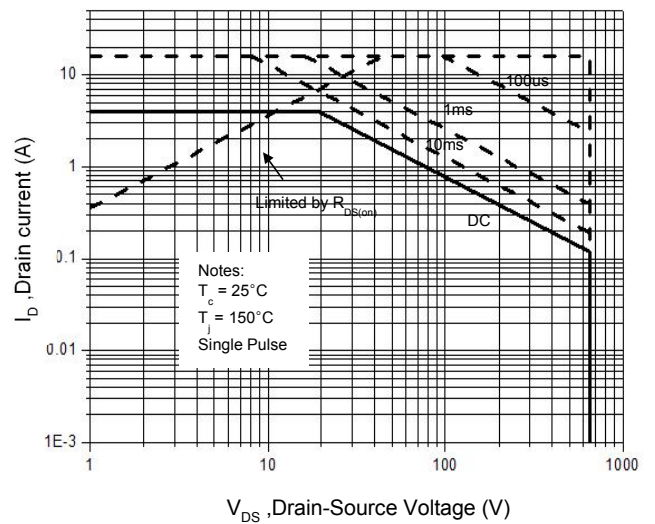


Figure 11. Power Dissipation vs. Temperature
TO-220F

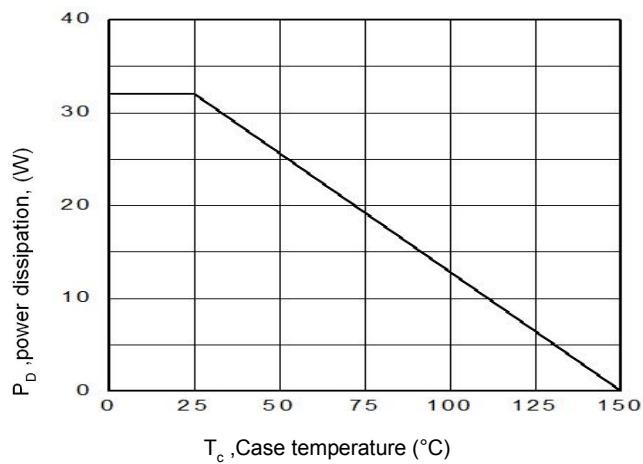


Figure 12. Power Dissipation vs. Temperature
TO-220/ TO-251/TO-252/TO-262

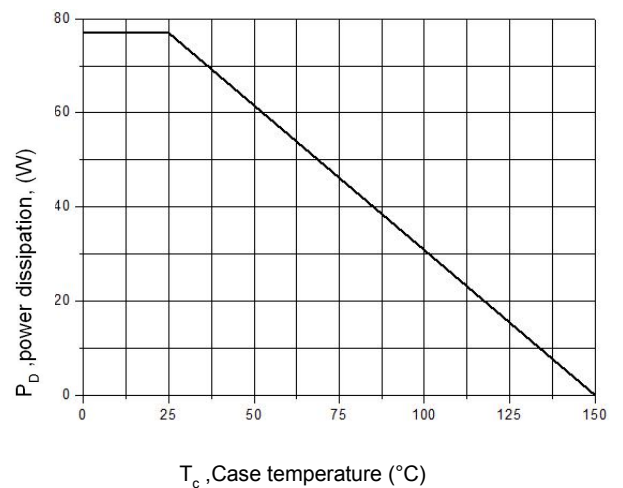


Figure 13. Continuous Drain Current vs. Temperature

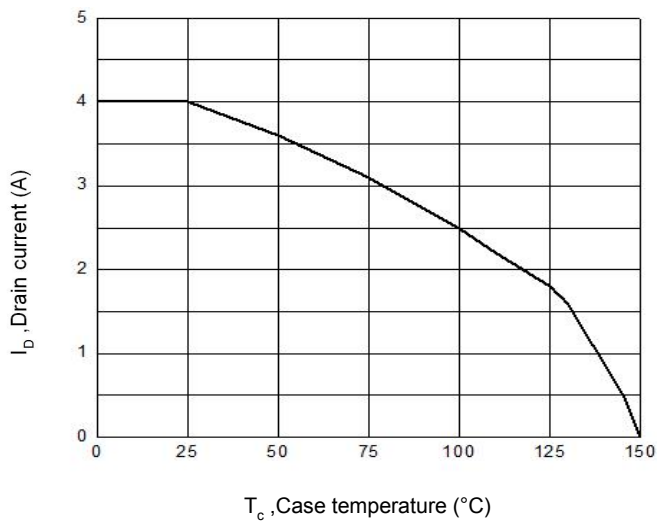


Figure 14. Body Diode Transfer Characteristics

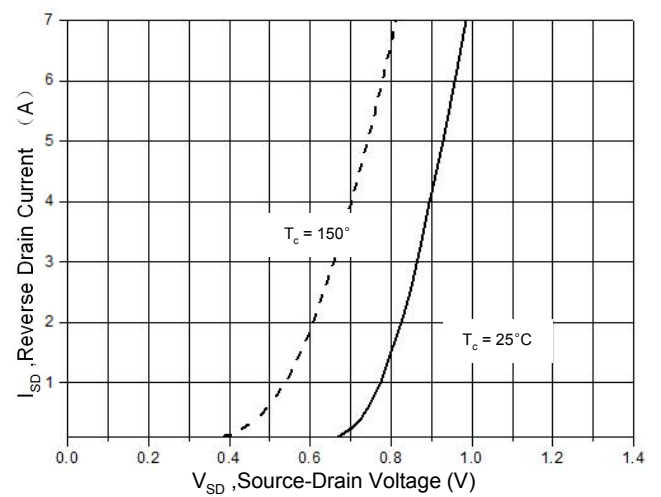


Figure 15 Transient Thermal Impedance, Junction to Case, TO-220F

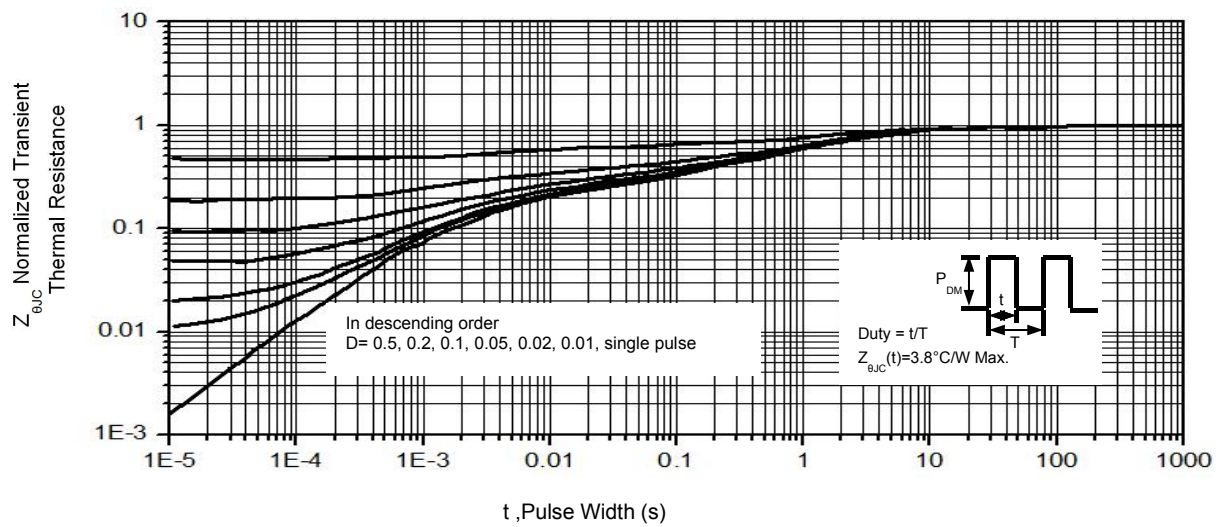
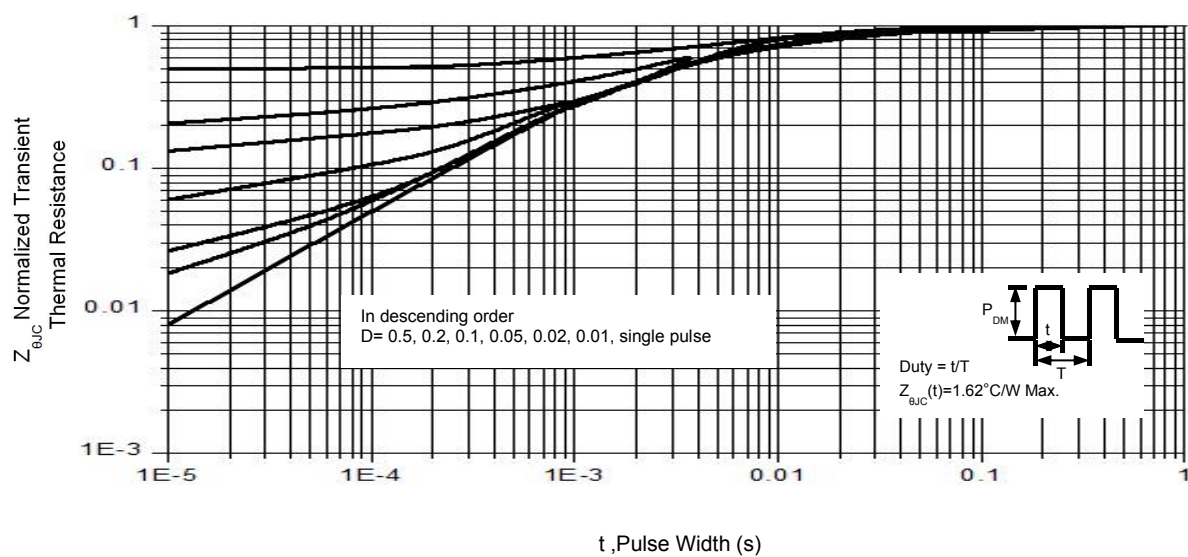
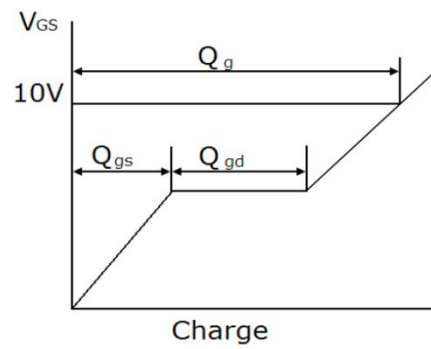
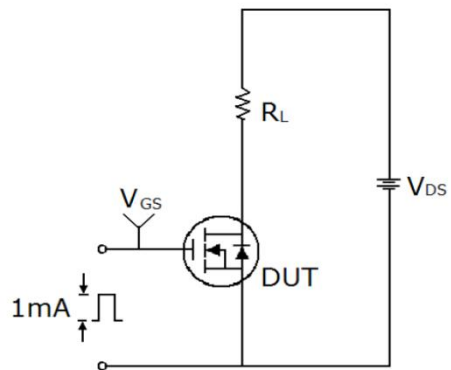


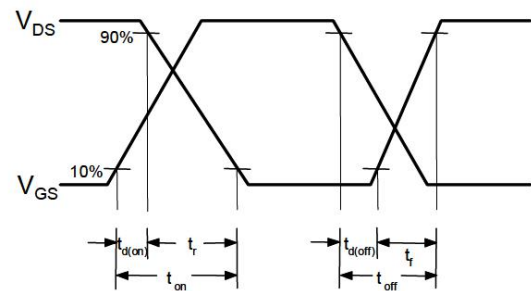
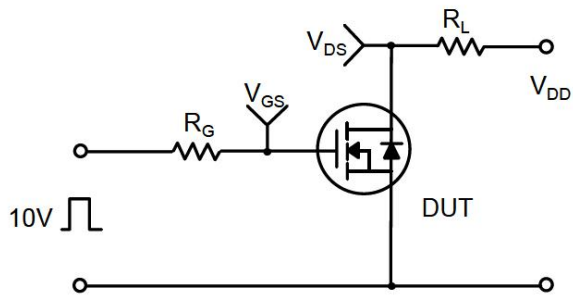
Figure 16. Transient Thermal Impedance, Junction to Case, TO-220/ TO-251/TO-252/TO-262



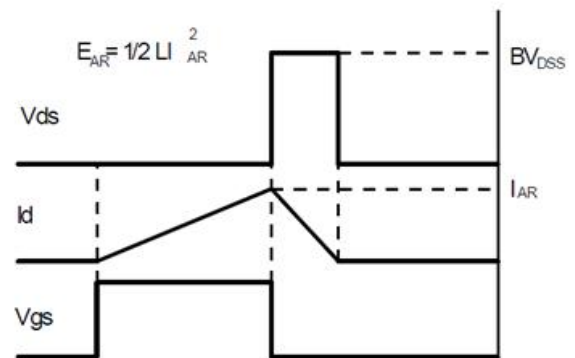
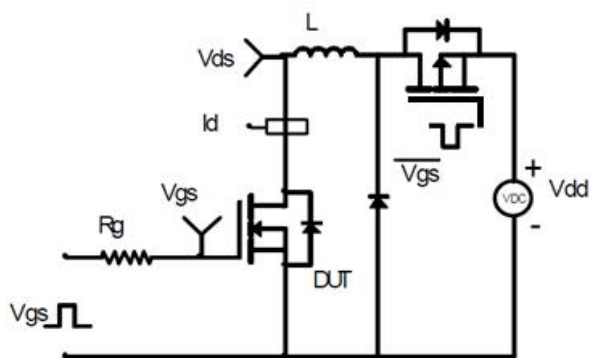
Gate Charge Test Circuit & Waveform



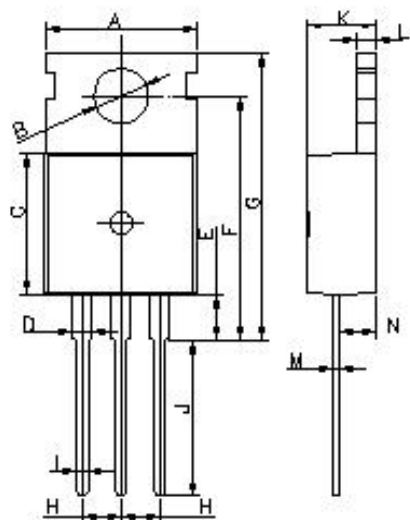
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

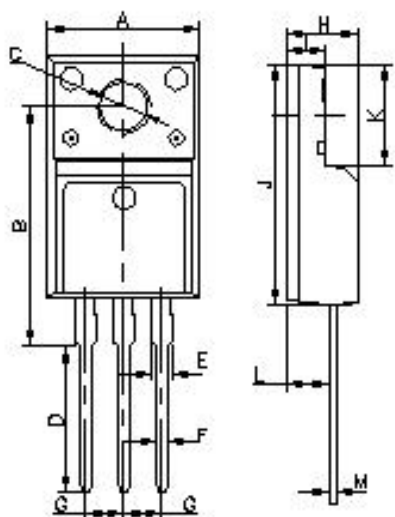


Mechanical Dimensions for TO-220



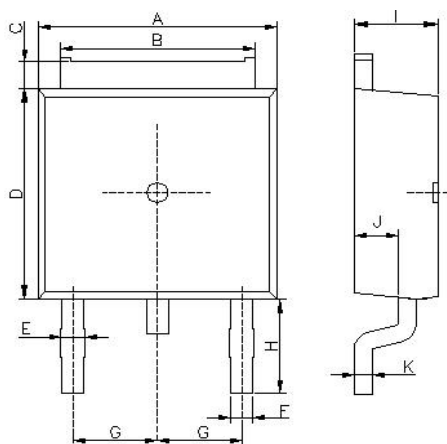
SYMBOL	COMMON DIMENSIONS		
	MIN	NOM	MAX
A	9.70	9.90	10.10
B	3.50	3.60	3.70
C	9.00	9.20	9.40
D	1.17	1.32	1.47
E	2.80	3.00	3.20
F	15.60	15.90	16.20
G			18.950
H	2.44	2.64	2.84
I	0.70	0.80	0.90
J	9.780	10.08	10.380
K	4.30	4.50	4.70
L	1.200	1.30	1.400
M	0.40	0.60	0.80
N	2.25	2.40	2.55

Mechanical Dimensions for TO-220F



SYMBOL	COMMON DIMENSIONS		
	MIN	NOM	MAX
A	9.96	10.16	10.36
B	15.50	15.80	16.10
C	2.98	3.18	3.28
D	9.41	9.71	10.01
E	1.18	1.38	1.58
F	0.70	0.80	0.90
G	2.54BSC		
H	4.50	4.70	4.90
I	2.44	2.54	2.64
J	15.67	15.87	16.07
K	6.50	6.70	6.90
L	2.56	2.76	2.96
M	0.40	0.50	0.60

Mechanical Dimensions for TO-252



SYMBOL	COMMON DIMENSIONS		
	MIN	NOM	MAX
A	6.40	6.60	6.80
B	5.18	5.33	5.48
C	0.72	0.87	1.02
D	5.95	6.15	6.35
E	0.75	0.90	1.05
F	0.70	0.80	0.90
G	2.14	2.29	2.44
H	2.40	2.70	3.00
I	2.10	2.30	2.50
J	0.85	1.00	1.15
K	0.41	0.51	0.61