

N-Channel Enhancement Mode Power MOSFET

● Features

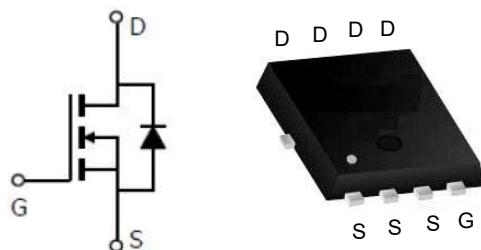
$V_{DS} = 30V$
 $I_D = 28A$
 $R_{DS(ON)} \leq 18m\Omega (V_{GS}=10V)$

● General Description

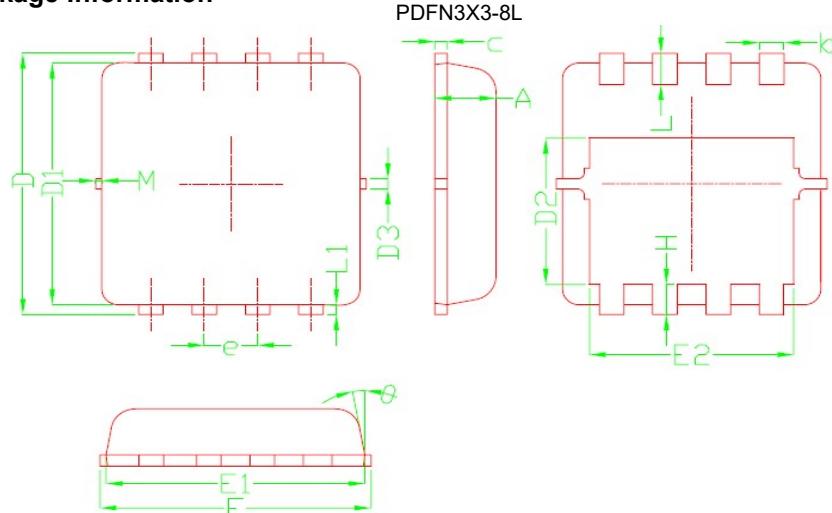
The TNM2030N3X is the high cell density trenched N-ch MOSFETs, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The TNM2030N3X meet the RoHS and Green Product requirement , 100 % EAS guaranteed with full function reliability approved.

● Pin Configurations



● Package Information



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.70	0.85	0.027	0.034
b	0.20	0.40	0.007	0.016
c	0.10	0.25	0.004	0.010
D	3.15	3.45	0.124	0.136
D1	2.90	3.20	0.114	0.126
D2	1.54	1.98	0.060	0.080
D3	0.10	0.30	0.004	0.012
E	3.15	3.45	0.124	0.136
E1	3.00	3.25	0.118	0.128
E2	2.29	2.65	0.090	0.104
e	0.65 BSC		0.025 BSC	
H	0.28	0.65	0.011	0.026
Θ	0°	14°	0°	14°
L	0.30	0.50	0.012	0.020
L1	0.13		0.005	
M	---	0.15	---	0.006



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● Absolute Maximum Ratings (@TA=25°C unless otherwise noted)

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage		V _{DSS}	30	V
Gate Source Voltage		V _{GSS}	±20	V
Drain Current (Continuous) *AC	TA=25°C	I _D	28	A
	TA=100°C		18	
Drain Current (Pulse) *B		I _{DM}	55	A
Power Dissipation	TA=25°C	P _D	20	W
Operating Temperature/ Storage Temperature		T _{J/T_{STG}}	-55~150	°C
Single Pulse Avalanche Energy		E _{AS}	22.1	mJ
Avalanche Current		I _{AS}	21	A

● Thermal Characteristics

Parameter	Symbol	Ratings	Unit
Thermal Resistance ,Junction-to-Ambient	R _{θJA}	75	°C/W
Thermal resistance, junction-case max	R _{θJC}	6	°C/W

● Electrical Characteristics (@TA=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	30	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V	--	--	1	uA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250uA	1.0	--	2.5	V
Gate Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
Drain-Source On-state Resistance	R _{DS(on)}	V _{GS} =10V, I _D =10A	--	--	18	mΩ
		V _{GS} =4.5V, I _D =5A	--	--	30	mΩ
Forward Transconductance	g _{fs}	I _{SD} =1A, V _{DS} =5V	--	4.5	--	S
Total Gate Charge	Q _g	V _{GS} =4.5V, V _{DS} =20V, I _D =10A	--	7.2	--	nC
Gate- Source Charge	Q _{gs}		--	1.4	--	nC
Gate- Drain Charge	Q _{gd}		--	2.2	--	nC
Tum-on Delay Time	t _{d(on)}	V _{GS} =10V, V _{DD} =12V, I _D =5A, R _{GEN} =3.3Ω	--	4.1	--	ns
Turn-on Rise Time	t _r		--	9.8	--	ns
Turn-off Delay Time	t _{d(off)}		--	15.5	--	ns
Turn-off Fall Time	t _f		--	6.0	--	ns
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, f=1MHZ	--	572	--	pF
Output Capacitance	C _{oss}		--	81	--	pF
Reverse Transfer Capacitance	C _{rss}		--	65	--	pF



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● Reverse Diode Characteristics (@TA=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Continuous Diode Forward Current	I _S	V _G =V _D =0V , Force Current	--	--	28	A
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	--	--	1.2	V

A: The value of R_{θJA} is measured with the device mounted on 1in2 FR- 4 board with 2oz. Copper, in a still air environment with TA=25C. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature .

C: The current rating is based on the t< 10s junction to ambient thermal resistance rating.



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● TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

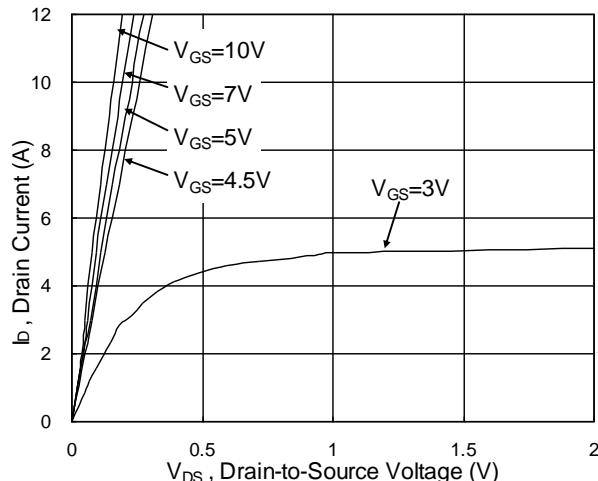


Fig.1 Typical Output Characteristics

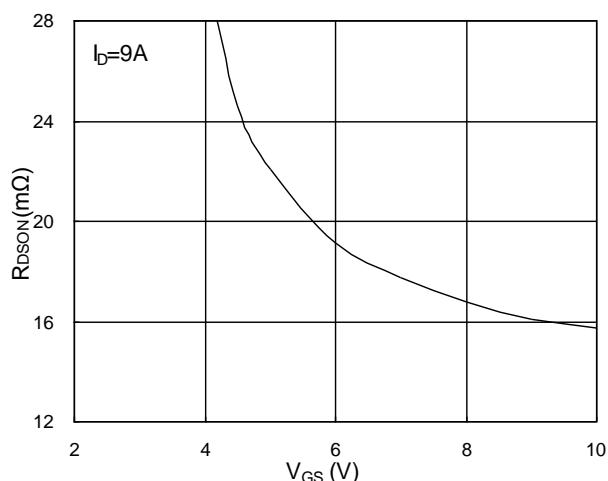


Fig.2 On-Resistance vs. Gate-Source

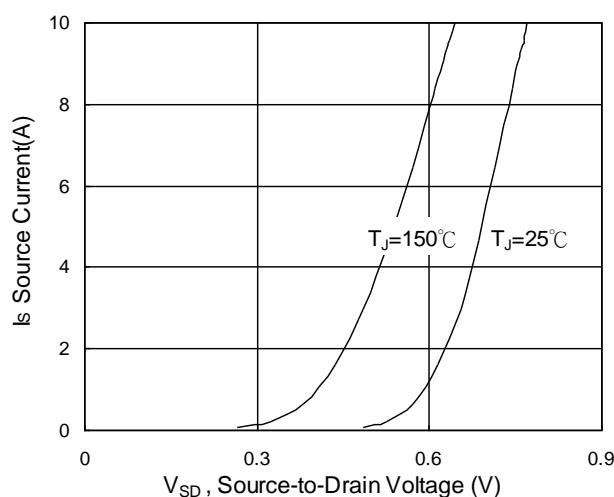


Fig.3 Forward Characteristics Of Reverse

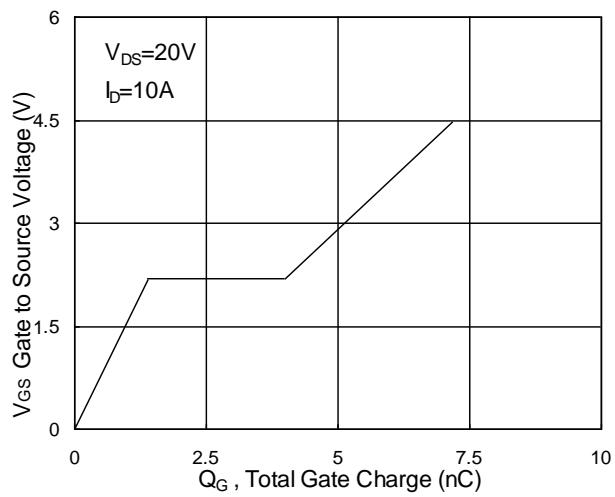
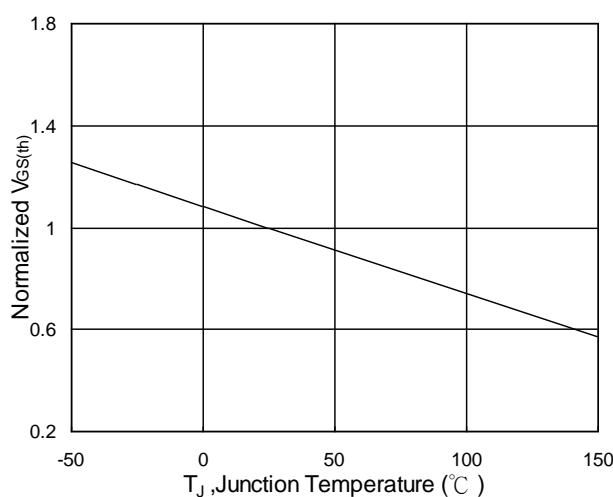
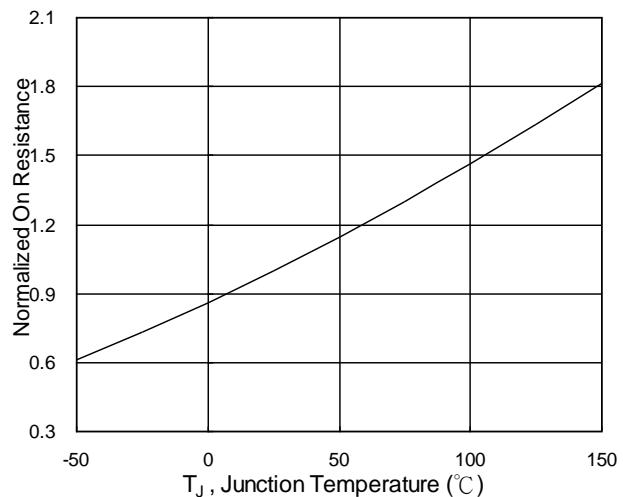


Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ vs. T_J Fig.6 Normalized R_{DSON} vs. T_J



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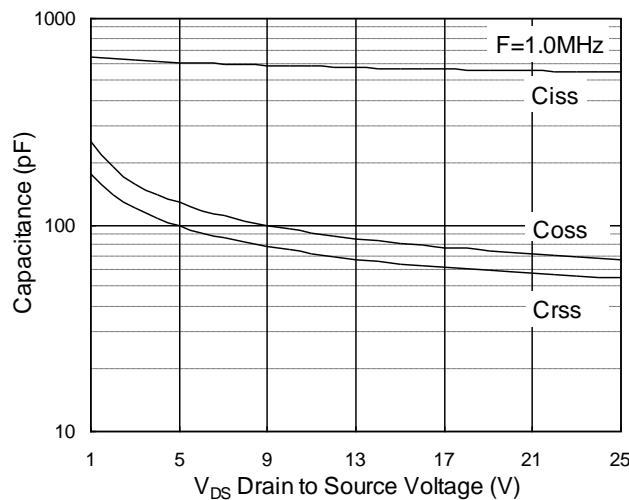


Fig.7 Capacitance

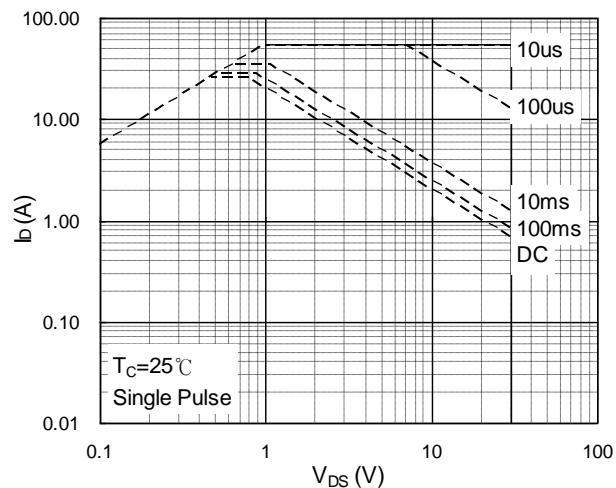


Fig.8 Safe Operating Area

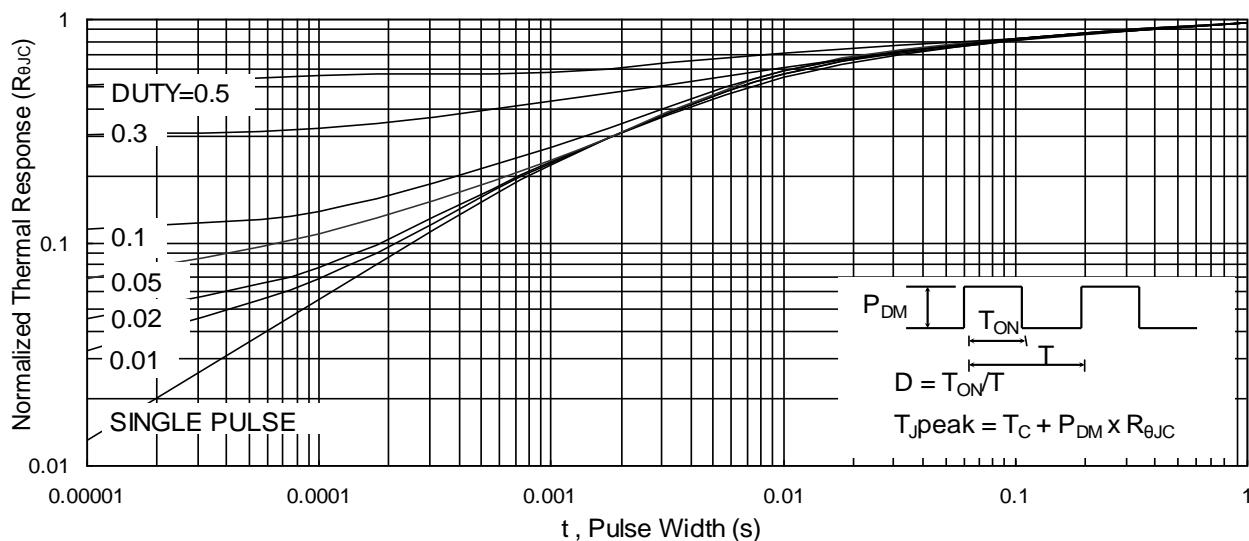


Fig.9 Normalized Maximum Transient Thermal Impedance

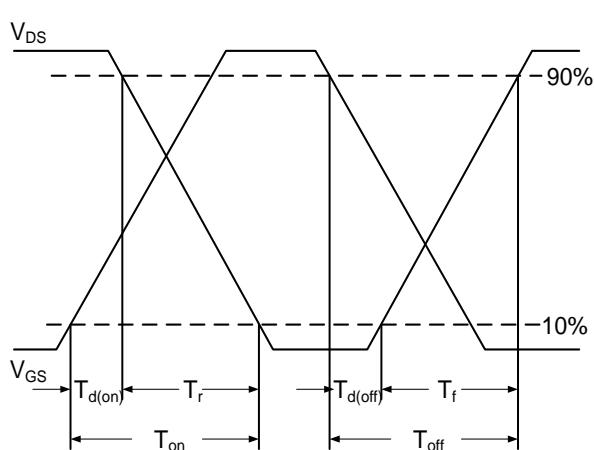


Fig.10 Switching Time Waveform

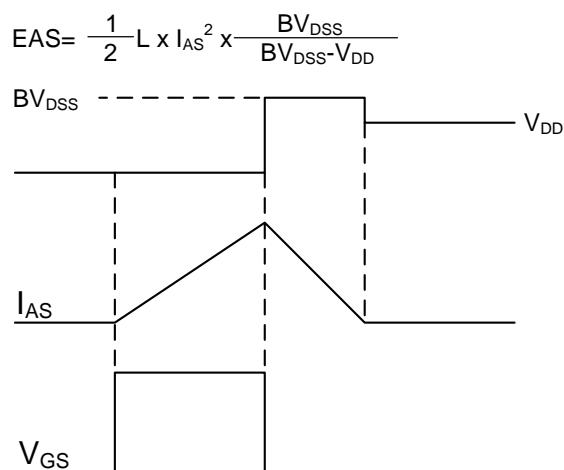


Fig.11 Unclamped Inductive Waveform