

SOT-23 Plastic-Encapsulate MOSFETS

Features

- $V_{DS}=30V$
- $I_D=5.6A$
- $R_{DS(on)}@V_{GS}=10V < 27m\Omega$
- $R_{DS(on)}@V_{GS}=4.5V < 33m\Omega$
- Trench Power LV MOSFET technology
- Voltage controlled small signal switch
- Fast Switching Speed

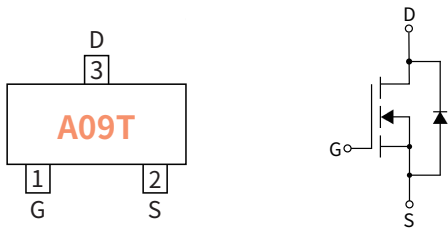
Applications

- Battery protection
- Load switch
- Power management

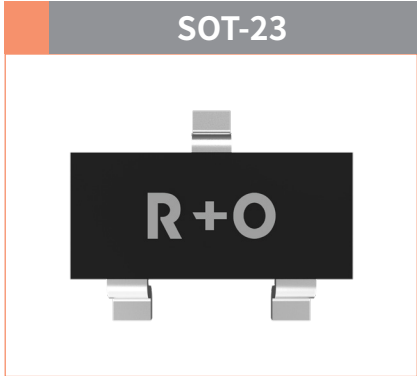
Mechanical Data

- Case: SOT-23
Molding compound meets UL 94V-0 flammability rating, RoHS-compliant,halogen-free
- Terminals: Solder plated, solderable per MIL-STD-750,Method 2026

Function Diagram



Drain-source Voltage
30 V
Drain Current
5.6 Ampere



Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE
Drain-source Voltage	V_{DS}	V	30
Gate-source Voltage	V_{GS}	V	± 12
Drain Current	I_D	A	5.6
Pulsed Drain Current ⁽¹⁾	I_{DM}	A	23
Total Power Dissipation ⁽²⁾	P_D	W	1.2
Junction temperature	T_J	°C	-55 ~+150
Storage temperature	T_{stg}	°C	-55 ~+150
Thermal Resistance Junction-to-Ambient @ Steady State ⁽²⁾	$R_{\theta JA}$	°C / W	104

Ordering Information

PACKAGE	PACKAGE CODE	UNIT WEIGHT(g)	REEL(pcs)	BOX(pcs)	CARTON(pcs)	DELIVERY MODE
SOT-23	R1	0.008	3000	45000	180000	7"

● Static Parameter Characteristics (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	V	30	—	—
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	μA	—	—	1.0
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$	nA	—	—	± 100
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	V	0.65	0.9	1.5
Static Drain-Source On-Resistance ⁽³⁾	$R_{DS(on)}$	$V_{GS}=10V, I_D=5.6A$	m Ω	—	21	27
		$V_{GS}=4.5V, I_D=5.0A$		—	25	33
		$V_{GS}=2.5V, I_D=3.0A$		—	33	51
Gate resistance	R_g	$V_{DS}=0V, V_{GS}=0, f=1MHz$	Ω	—	—	3.6
Forward Transconductance	g_{FS}	$V_{DS}=5.0V, I_D=5.0A$	S	8.0	—	—
Diode Forward Voltage	V_{SD}	$I_S=5.6A, V_{GS}=0V$	V	—	—	1.2

● Dynamic Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Input Capacitance	C_{iss}	$V_{DS}=15V$ $V_{GS}=0V$ $f=1MHz$	pF	—	630	—
Output Capacitance	C_{oss}			—	90	—
Reverse Transfer Capacitance	C_{rss}			—	71	—

● Switching Parameters (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	Condition	UNIT	Min	Typ	Max
Total Gate Charge	Q_g	$V_{GS}=10V$ $V_{DS}=15V$ $I_D=5.6$	nC	—	17	—
Gate-Source Charge	Q_{gs}			—	2.0	—
Gate-Drain Charge	Q_{gd}			—	2.0	—
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=10V$ $V_{DS}=15V$ $I_D=5.6A$ $R_{GEN}=3.0\Omega$	ns	—	5.0	—
Turn-on Rise Time	t_r			—	28	—
Turn-off Delay Time	$t_{D(off)}$			—	16	—
Turn-off fall Time	t_f			—	26	—

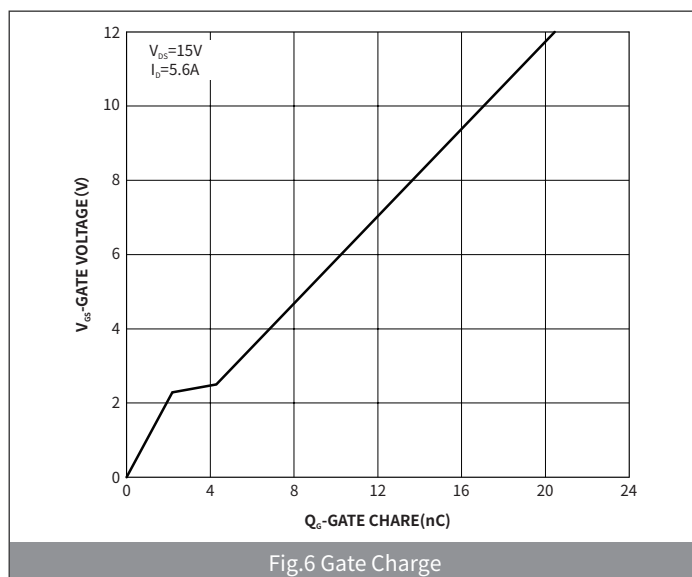
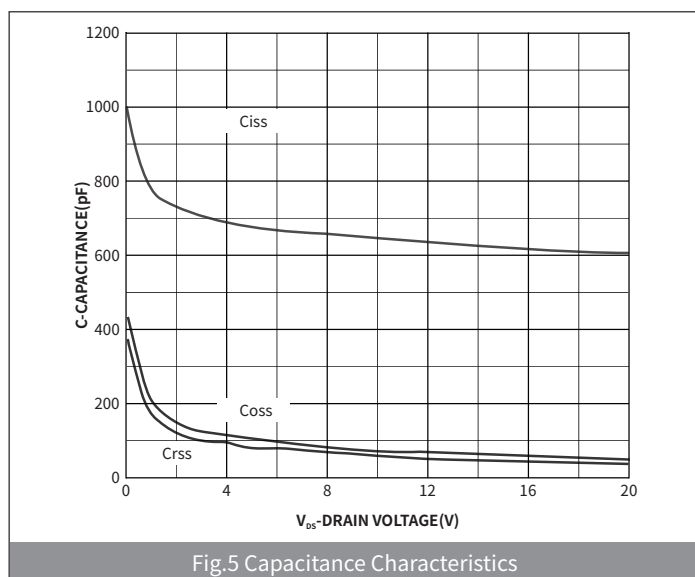
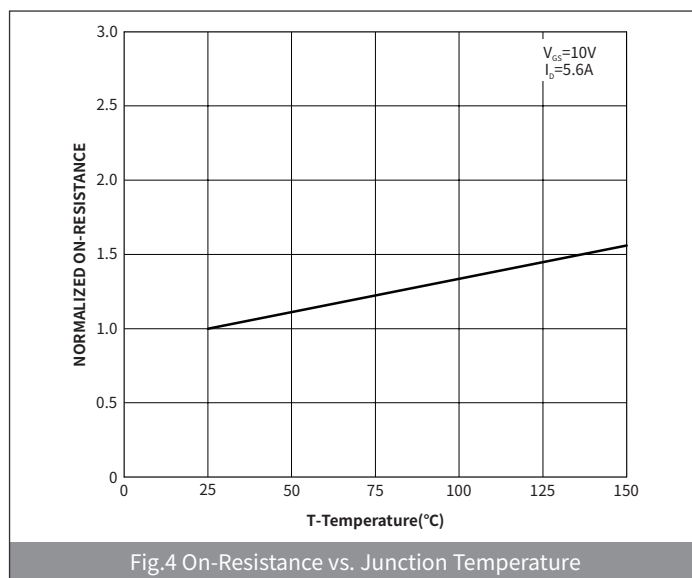
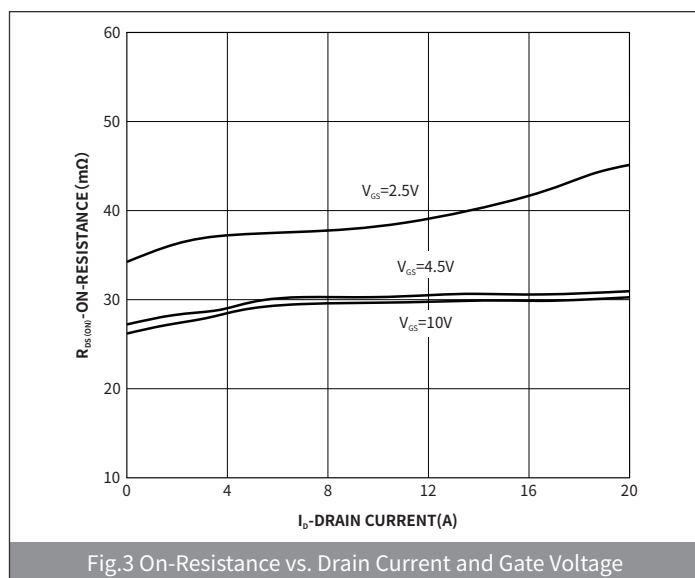
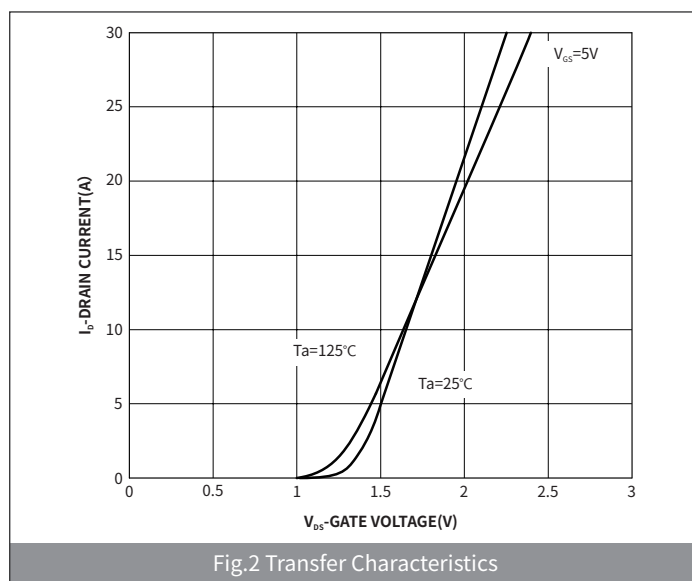
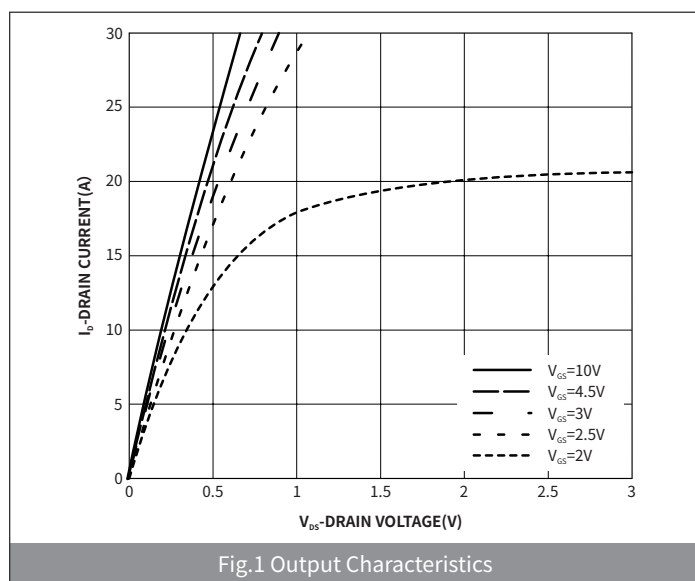
Note :

(1) Repetitive rating, pulse width limited by junction temperature $T_{J(max)}=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ C$.

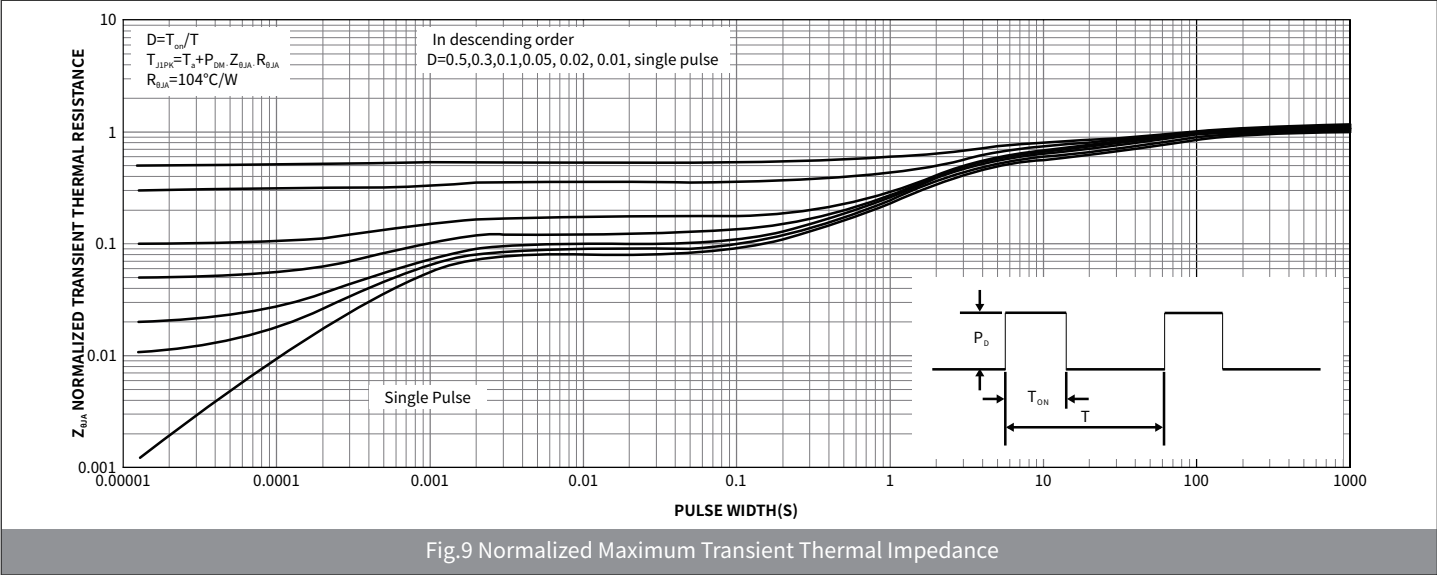
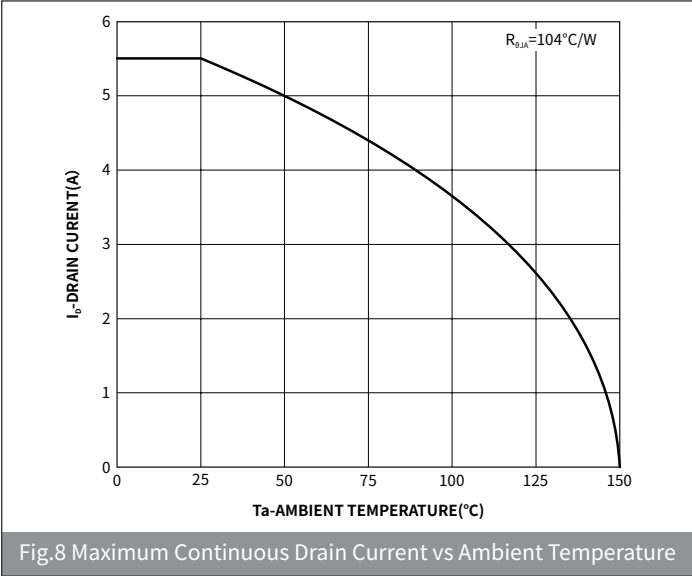
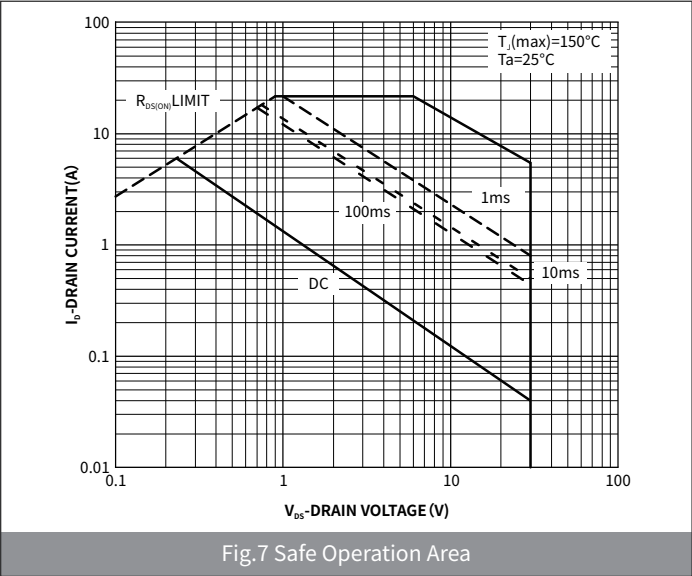
(2) The value of P_D and $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ C$.

(3) Pulse test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

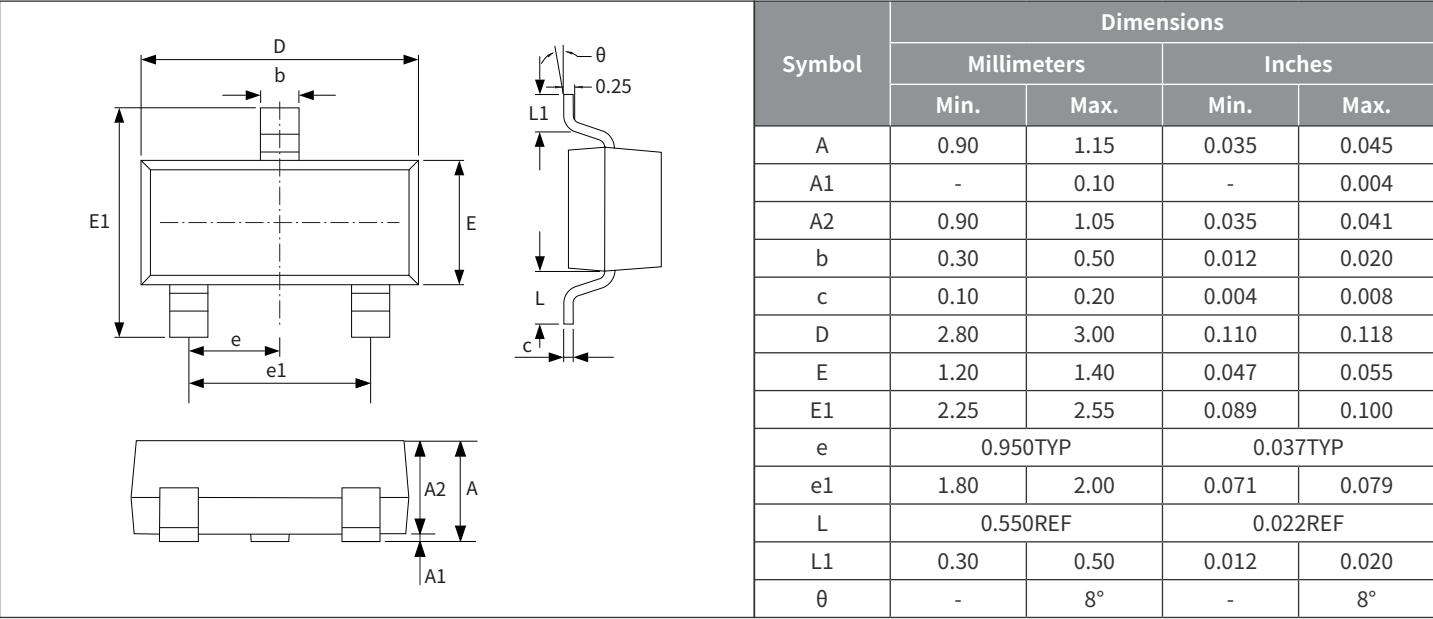
● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



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● Package Outline Dimensions (SOT-23)



● Suggested Pad Layout

