

## SMC Surface Mount Schottky Barrier Rectifier

### Features

- For surface mounted applications
- High forward surge current capability
- Guardring for overvoltage protection
- Low power losses, high efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260°C

**Reverse Voltage**

20-200 V

**Forward Current**

3 Ampere

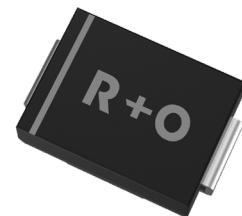
### Applications

For use in low voltage high frequency inverters, free wheeling, DC/DC converters, and polarity protection applications.

### Mechanical Data

- Case: DO-214AB(SMC)  
Molding compound meets UL 94V-0 flammability rating, RoHS-compliant, halogen-free
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity: Cathode line denotes the cathode end

**DO-214AB(SMC)**



### Function Diagram



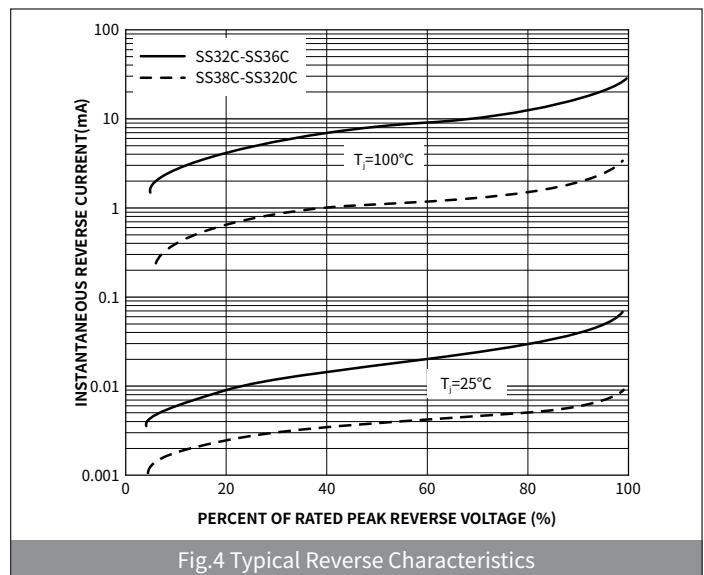
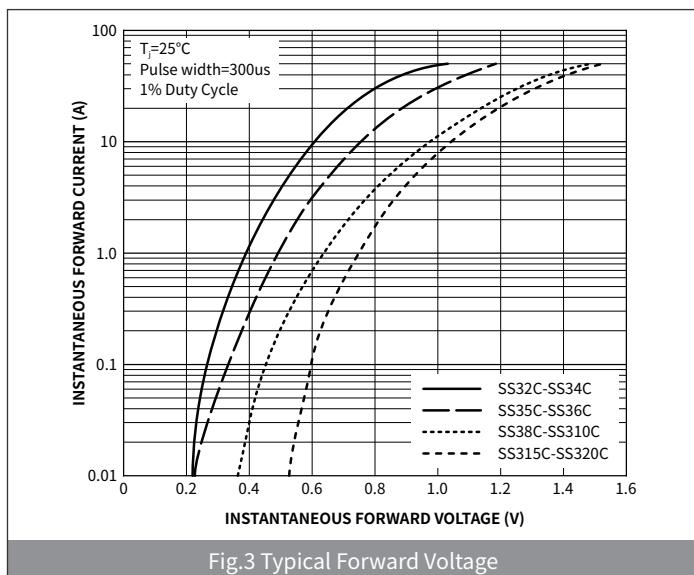
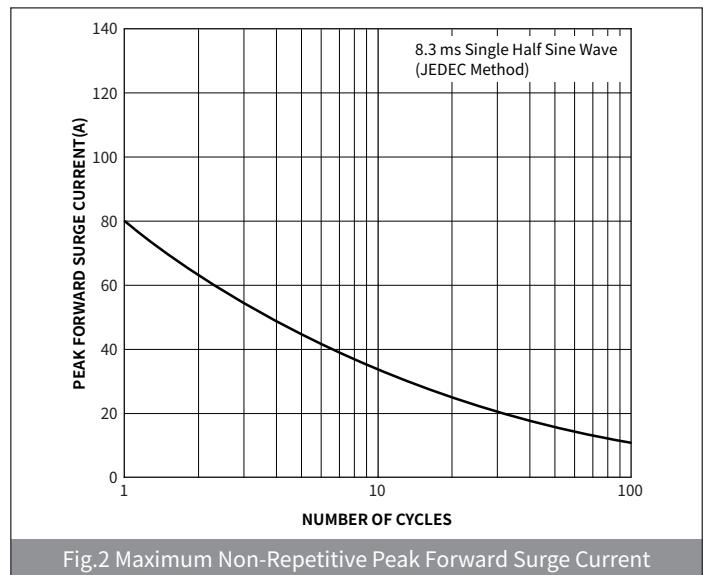
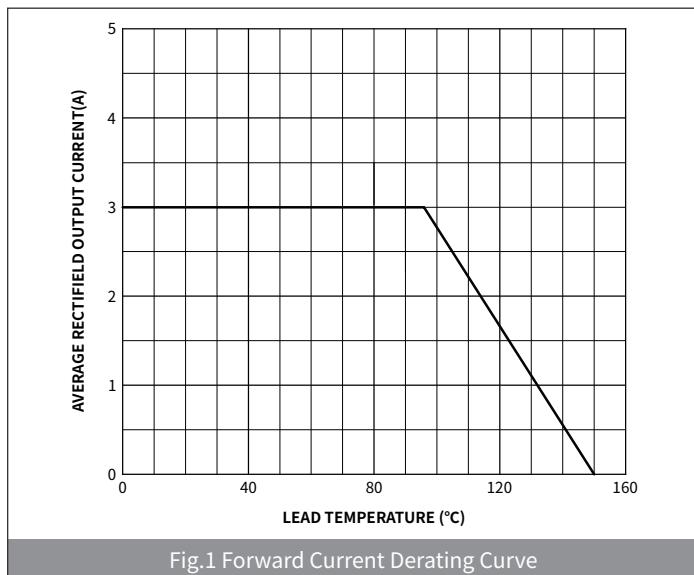
### Maximum Ratings (Ta=25°C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	SS32C	SS33C	SS34C	SS35C	SS36C	SS38C	SS310C	SS315C	SS320C
Device marking code			SS32C	SS33C	SS34C	SS35C	SS36C	SS38C	SS310C	SS315C	SS320C
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	V	20	30	40	50	60	80	100	150	200
Maximum RMS Voltage	V <sub>RMS</sub>	V	14	21	28	35	42	56	70	105	140
Maximum DC blocking Voltage	V <sub>DC</sub>	V	20	30	40	50	60	80	100	150	200
Maximum Average Forward Rectified Current @ 60Hz sinewave, Resistance load,TL (Fig.1)	I <sub>F(AV)</sub>	A						3.0			
Non-repetitive Peak Forward Surge Current @ t=8.3ms Half-sine wave	I <sub>FSM</sub>	A						80			
Storage temperature	T <sub>stg</sub>	°C						-55 ~ +150			
Junction temperature	T <sub>j</sub>	°C		-55 ~ +125					-55 ~ +150		
Typical Thermal Resistance	R <sub>θJ-A</sub>	°C /W						47			
	R <sub>θJ-L</sub>	°C /W						17			

## ● Electrical Characteristics (Ta=25°C Unless otherwise noted)

PARAMETER	TEST CONDITIONS	SYMBOL	UNIT	SS32C	SS33C	SS34C	SS35C	SS36C	SS38C	SS310C	SS315C	SS320C
Maximum instantaneous forward voltage	$I_F=3.0A$	$V_F$	V		0.55		0.70		0.85		0.95	
Maximum DC reverse current at rated DC blocking voltage	$V_R=V_{DC}, T_A=25^\circ C$	$I_{R1}$	mA			0.5				0.05		
	$V_R=V_{DC}, T_A=100^\circ C$	$I_{R2}$				50				10		
Typical junction capacitance	4.0V DC,1MHz	$C_J$	pF		450					350		

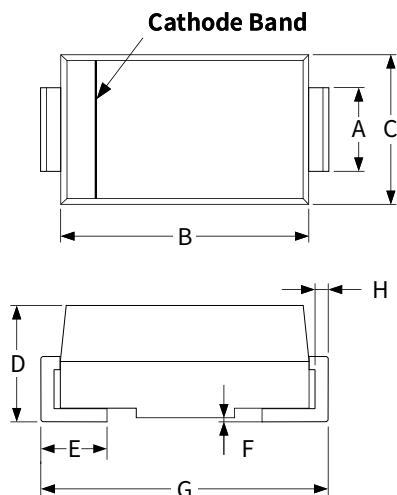
## ● Ratings And Characteristics Curves (Ta=25°C Unless otherwise specified)



## ● Ordering Information

PACKAGE	PACKAGE CODE	UNIT WEIGHT(g)	REEL(pcs)	BOX(pcs)	CARTON(pcs)	DELIVERY MODE
SMC	R3	0.257	3000	6000	48000	13"

## ● Package Outline Dimensions (SMC/DO-214AB)

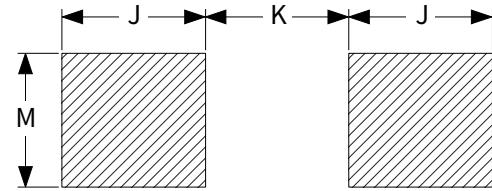


The diagram illustrates the Cathode Band package outline with the following dimensions:

- A:** Total height from the bottom surface to the top edge of the cathode band.
- B:** Total width of the package.
- C:** Width of the cathode band.
- D:** Total height of the package including the lead height.
- E:** Lead height.
- F:** Lead thickness.
- G:** Total width of the leads.
- H:** Lead pitch.

Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.85	3.27	0.112	0.129
B	6.60	7.11	0.261	0.281
C	5.59	6.22	0.221	0.246
D	1.99	2.61	0.078	0.103
E	0.76	1.52	0.030	0.060
F	-	0.20	-	0.008
G	7.75	8.13	0.306	0.321
H	0.15	0.31	0.006	0.012

## ● Suggested Pad Layout



The diagram illustrates the suggested pad layout with the following dimensions:

- J:** Width of each pad.
- K:** Distance between the centers of the two pads.
- M:** Total height of the pads.

Symbol	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
M	3.82	-	0.151	-
J	3.03	-	0.120	-
K	-	3.84	-	0.152