

- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ 100% EAS Guaranteed
- ★ Advanced high cell density Trench technology

### Product Summary

**RoHS**

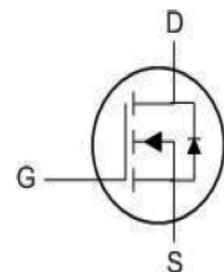
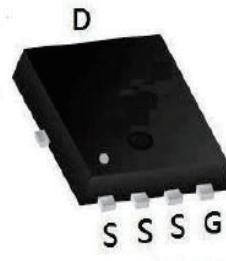
| BVDSS | RDS(ON) | ID  |
|-------|---------|-----|
| 40V   | 11mΩ    | 45A |

### Description

The 50N04F is the high cell density trenched N ch MOSFETs, which provide excellent RDS(ON) and gate charge for most of the synchronous buck converter applications.

The 50N04F meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### PDFN5X6 Pin Configuration



### Absolute Maximum Ratings

| Symbol                                | Parameter   | Rating     | Units |
|---------------------------------------|---|------------|-------|
| V <sub>DS</sub>                       | Drain-Source Voltage  | 40         | V     |
| V <sub>G</sub>                        | Gate-Source Voltage   | ±20        | V     |
| I <sub>D</sub> @T <sub>c</sub> =25°C  | Continuous Drain Current, V <sub>G</sub> @ 10V <sup>1</sup> | 45         | A     |
| I <sub>D</sub> @T <sub>c</sub> =100°C | Continuous Drain Current, V <sub>G</sub> @ 10V <sup>1</sup> | 25         | A     |
| I <sub>DM</sub>                       | Pulsed Drain Current <sup>2</sup>                           | 80         | A     |
| EAS                                   | Single Pulse Avalanche Energy <sup>3</sup>                  | 19         | mJ    |
| I <sub>AS</sub>                       | Avalanche Current   | 30         | A     |
| P <sub>D</sub> @T <sub>c</sub> =25°C  | Total Power Dissipation <sup>4</sup>                        | 20         | W     |
| T <sub>STG</sub>                      | Storage Temperature Range                                   | -55 to 150 | °C    |
| T <sub>J</sub>                        | Operating Junction Temperature Range                        | -55 to 150 | °C    |

### Thermal Data

| Symbol           | Parameter   | Typ. | Max. | Unit |
|------------------|---|------|------|------|
| R <sub>θJA</sub> | Thermal Resistance Junction-ambient (Steady State) <sub>1</sub> | ---  | 55   | °C/W |
| R <sub>θJC</sub> | Thermal Resistance Junction-Case <sub>1</sub>                   | ---  | 4.32 | °C/W |

Electrical Characteristics ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

| Symbol   | Parameter                                     | Test condition   | Min. | Typ. | Max.      | Units            |
|--|---|--|------|------|-----------|------------------|
| <b>Off Characteristics</b>                               |   |  |      |      |           |                  |
| $\text{BV}_{\text{DSS}}$                                 | Drain-Source Breakdown Voltage                | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$                       | 40   | -    | -         | V                |
| $I_{\text{DS}(\text{SS})}$                               | Zero Gate Voltage Drain Current               | $V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}$                          | -    | -    | 1         | $\mu\text{A}$    |
| $I_{\text{GSS}}$   | Gate-Body Leakage Current                     | $V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$                      | -    | -    | $\pm 100$ | nA               |
| <b>On Characteristics</b> <small>(Note 3)</small>        |   |  |      |      |           |                  |
| $V_{\text{GS}(\text{th})}$                               | Gate Threshold Voltage                        | $V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$                   | 1    | 1.5  | 2         | V                |
| $R_{\text{DS}(\text{ON})}$                               | Drain-Source On-State Resistance              | $V_{\text{GS}}=10\text{V}, I_{\text{D}}=8\text{A}$                           | -    | 11   | 16        | $\text{m}\Omega$ |
|  |   | $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=4\text{A}$                          | -    | 18.9 | 24        | $\text{m}\Omega$ |
| $g_{\text{FS}}$  | Forward Transconductance                      | $V_{\text{DS}}=5\text{V}, I_{\text{D}}=8\text{A}$                            | 33   | -    | -         | S                |
| <b>Dynamic Characteristics</b> <small>(Note 4)</small>   |   |  |      |      |           |                  |
| $C_{\text{iss}}$   | Input Capacitance                             | $V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$         | -    | 964  | -         | PF               |
| $C_{\text{oss}}$   | Output Capacitance                            |  | -    | 109  | -         | PF               |
| $C_{\text{rss}}$   | Reverse Transfer Capacitance                  |  | -    | 96   | -         | PF               |
| <b>Switching Characteristics</b> <small>(Note 4)</small> |   |  |      |      |           |                  |
| $t_{\text{d(on)}}$                                       | Turn-on Delay Time                            | $V_{\text{DD}}=20\text{V}, R_{\text{L}}=2.5\Omega$                           | -    | 5.5  | -         | nS               |
| $t_r$  | Turn-on Rise Time                             |  | -    | 14   | -         | nS               |
| $t_{\text{d(off)}}$                                      | Turn-Off Delay Time                           |  | -    | 24   | -         | nS               |
| $t_f$  | Turn-Off Fall Time                            |  | -    | 12   | -         | nS               |
| $Q_g$  | Total Gate Charge                             | $V_{\text{DS}}=20\text{V}, I_{\text{D}}=8\text{A}, V_{\text{GS}}=10\text{V}$ | -    | 22.9 | -         | nC               |
| $Q_{\text{gs}}$  | Gate-Source Charge                            |  | -    | 3.5  | -         | nC               |
| $Q_{\text{gd}}$  | Gate-Drain Charge                             |  | -    | 5.3  | -         | nC               |
| <b>Drain-Source Diode Characteristics</b>                |   |  |      |      |           |                  |
| $V_{\text{SD}}$  | Diode Forward Voltage <small>(Note 3)</small> | $V_{\text{GS}}=0\text{V}, I_{\text{S}}=9\text{A}$                            | -    | 0.8  | 1.2       | V                |

## Notes:

- 1.Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

## N-Channel Typical Electrical and Thermal Characteristics (Curves)

Figure 1: Switching Test Circuit

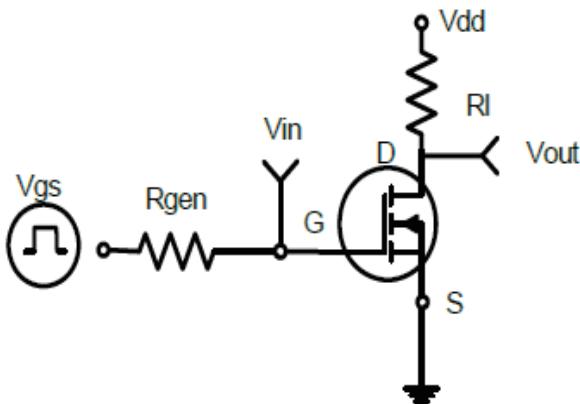


Figure 2: Switching Waveforms

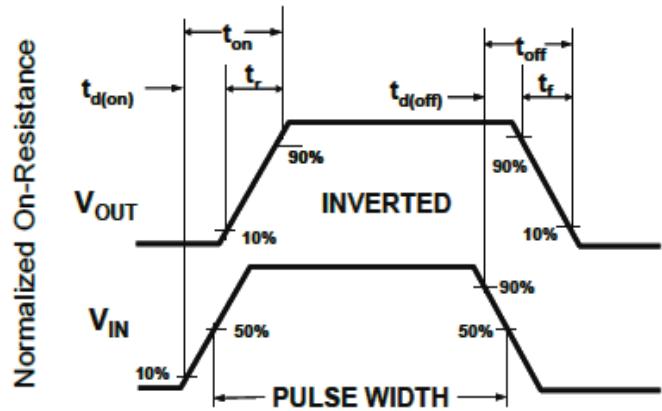


Figure 3: Output Characteristics

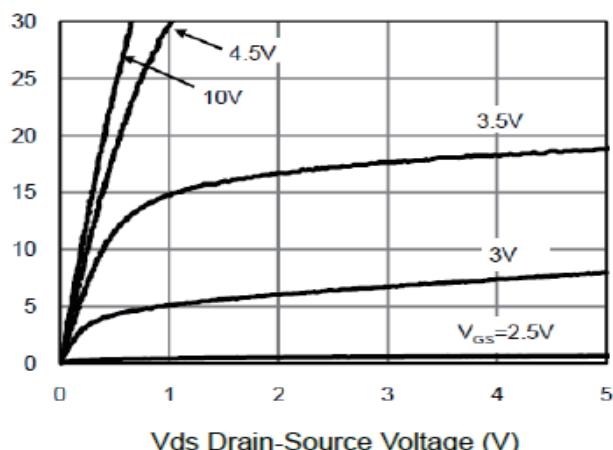


Figure 5: Drain-Source On-Resistance

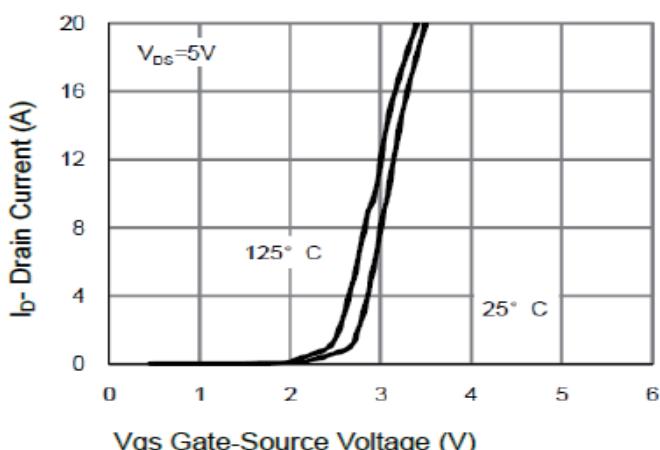
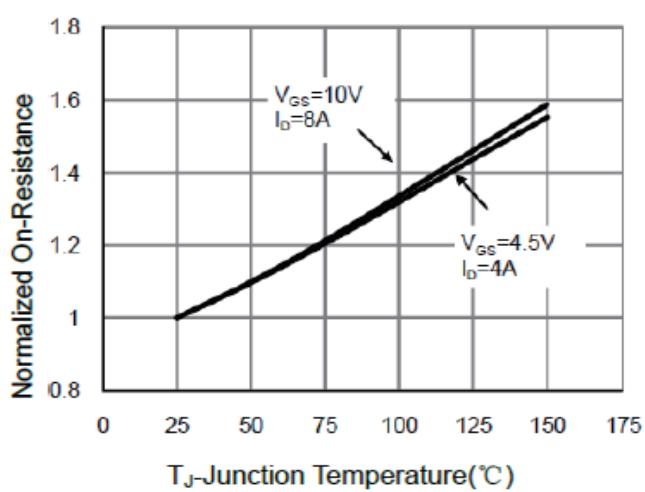
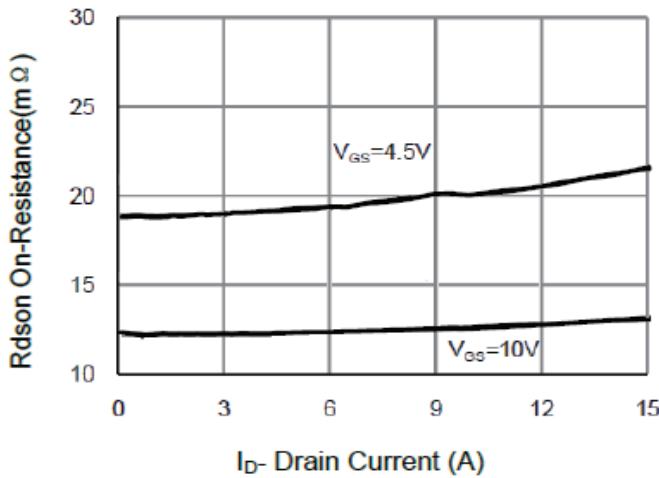


Figure 6: Drain-Source On-Resistance



## Typical Performance Characteristics

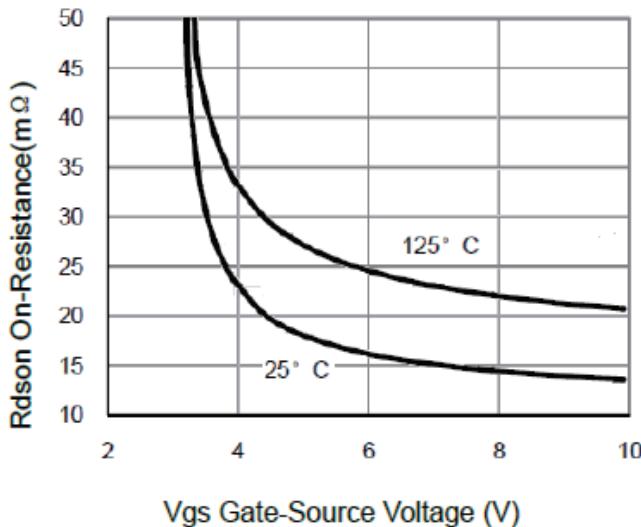
Figure 7:R<sub>dson</sub> vs V<sub>gs</sub>


Figure 8: Power Dissipation

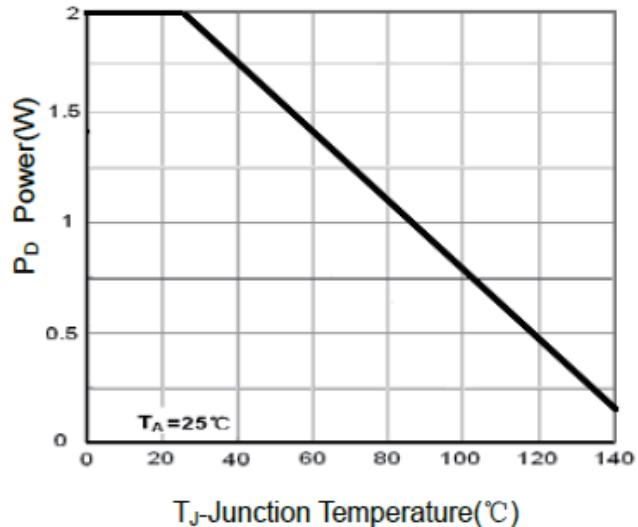


Figure 9: Gate Charge

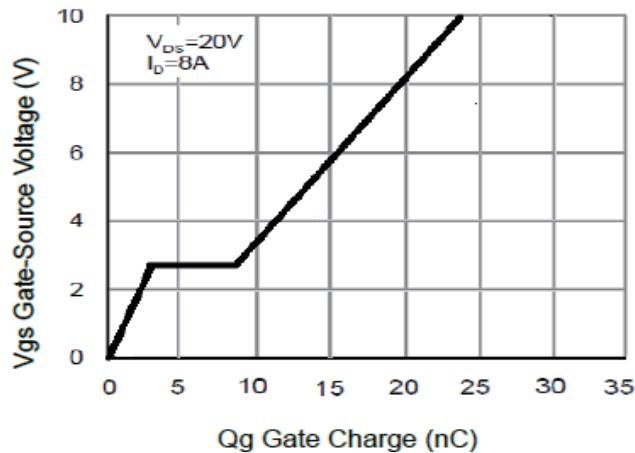


Figure 10: Source- Drain Diode Forward

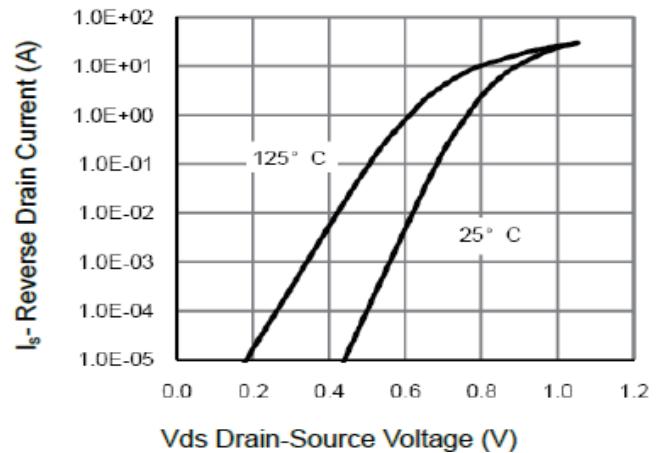


Figure 11: Capacitance vs Vds

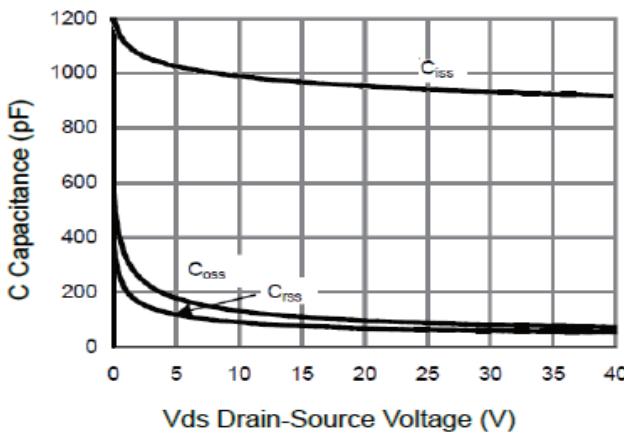
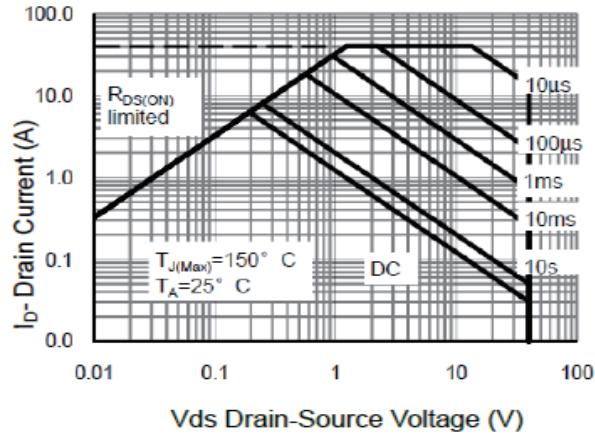
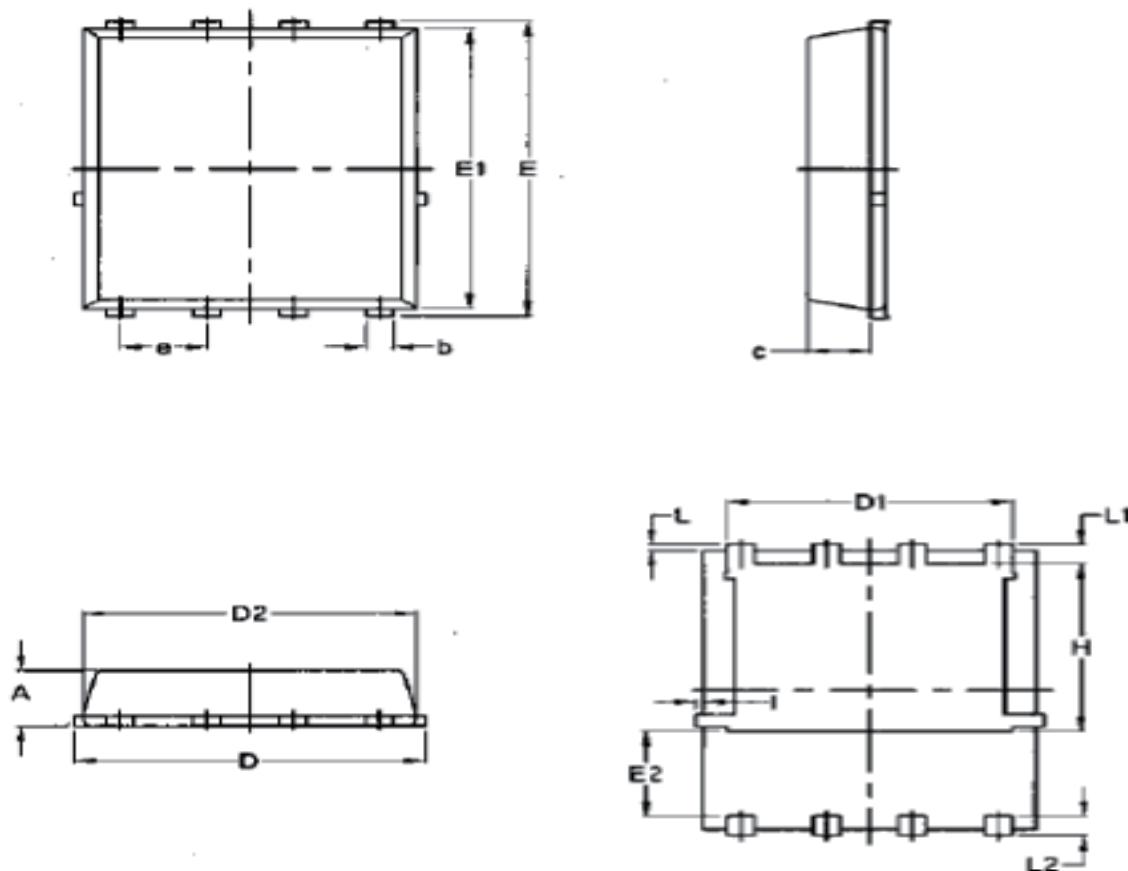


Figure 12: Safe Operation Area



## Package Mechanical Data-DFN5\*6-8L-JQ Single



| Symbol | Common   |        |          |        |
|--------|----------|--------|----------|--------|
|        | mm       |        | Inch     |        |
|        | Mim      | Max    | Min      | Max    |
| A      | 1.03     | 1.17   | 0.0406   | 0.0461 |
| b      | 0.34     | 0.48   | 0.0134   | 0.0189 |
| c      | 0.824    | 0.0970 | 0.0324   | 0.082  |
| D      | 4.80     | 5.40   | 0.1890   | 0.2126 |
| D1     | 4.11     | 4.31   | 0.1618   | 0.1697 |
| D2     | 4.80     | 5.00   | 0.1890   | 0.1969 |
| E      | 5.95     | 6.15   | 0.2343   | 0.2421 |
| E1     | 5.65     | 5.85   | 0.2224   | 0.2303 |
| E2     | 1.60     | /      | 0.0630   | /      |
| e      | 1.27 BSC |        | 0.05 BSC |        |
| L      | 0.05     | 0.25   | 0.0020   | 0.0098 |
| L1     | 0.38     | 0.50   | 0.0150   | 0.0197 |
| L2     | 0.38     | 0.50   | 0.0150   | 0.0197 |
| H      | 3.30     | 3.50   | 0.1299   | 0.1378 |
| I      | /        | 0.18   | /        | 0.0070 |