

Features ➤ 100% EAS Guaranteed ➤ Super Low Gate Charge ➤ Green Device Available ➤ Excellent Cdv/dt effect decline ➤ Advanced high cell density Trench technology	Bvdss	Rdson	ID
	70V	5.5mΩ	80A
	Application ➤ PWM applications ➤ Load Switch ➤ Power management		

Package
Marking and pin assignment TO-252 top view Schematic diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Quantity
80N07	80N07	TO-252	2500

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V _{DS}	70	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current, V _{GS} @10V ¹	T _C = 25°C	I _D	80	A
	T _C = 100°C		60	A
Pulsed Drain Current ²		I _{DM}	364	A
Avalanche Current		I _{AS}	--	A
'Single Pulsed Avalanche Energy ³		E _{AS}	400	mJ
Power Dissipation ⁴	T _C = 25°C	P _D	136	W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C



Thermal Resistance Ratings

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Ambient ¹	$R_{\theta JA}$	--	$^{\circ}\text{C}/\text{W}$
Thermal Resistance, Junction to Case ¹	$R_{\theta JC}$	1.1	$^{\circ}\text{C}/\text{W}$

Ordering Information

Ordering Number	Package	Pin Assignment			Packing
Halogen Free		G	D	S	
HL80N07	TO-252	1	2	3	Tape Reel

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$B_{V_{DS}}$	$V_{GS}=0\text{V}, I_D = 250\mu\text{A}$	70	-	-	V
Gate Leakage Current	I_{GSS}	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	± 100	nA
Drain Cut-off Current	I_{DSS}	$V_{DS}=70\text{V}, V_{GS}=0\text{V}, T_J=25^{\circ}\text{C}$	-	-	1	μA
		$V_{DS}=70\text{V}, V_{GS}=0\text{V}, T_J=100^{\circ}\text{C}$	-	-	100	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	2	3	4	V
Drain-Source On-State Resistance ²	$R_{DS(on)}$	$V_{GS}=10\text{V}, I_D=40\text{A}$	-	5.5	7	$\text{m}\Omega$
Forward Trans conductance	g_{fs}	$V_{DS}=10\text{V}, I_D=20\text{A}$	-	33	-	S
Gate Resistance	R_g	$V_{DS}=0\text{V}, V_{GS}=0\text{V}, F=1\text{MHz}$	-	0.7	-	Ω
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=34\text{V},$ $f=1\text{MHz}$	-	4723	-	pF
Output Capacitance	C_{oss}		-	225	-	
Reverse Transfer Capacitance	C_{rss}		-	207	-	
Total Gate Charge	Q_g	$V_{GS}=10\text{V}, V_{DS}=34\text{V},$ $I_D=20\text{A}$	-	76	-	nC
Gate-Source Charge	Q_{gs}		-	16	-	
Gate-Drain Charge	Q_{gd}		-	20	-	
Turn-On Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DD}=34\text{V},$ $R_G=6\Omega$	-	14.8	-	ns
Rise Time	t_r		-	33.2	-	
Turn-Off Time	$t_{d(off)}$		-	59.2	-	
Fall Time	t_f		-	12	-	
Body Diode Voltage ²	V_{SD}	$V_{GS}=0\text{V}, I_S=1\text{A}, T_J=25^{\circ}\text{C}$	-	-	1.2	V



Continuous Source Current ^{1,4}	I _s	V _G =V _D =0V, Force Current	-	-	80	A
Body Diode Reverse Recovery Time	t _{rr}	I _F =20A, dI/dt=100A/us, T _J =25°C	-	29	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	35	-	nC

Notes:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is T_J = 25°C , V_{DD}=40V, V_G=10V, R_g=25Ω, L=0.5mH.
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

Figure 1. Output Characteristics

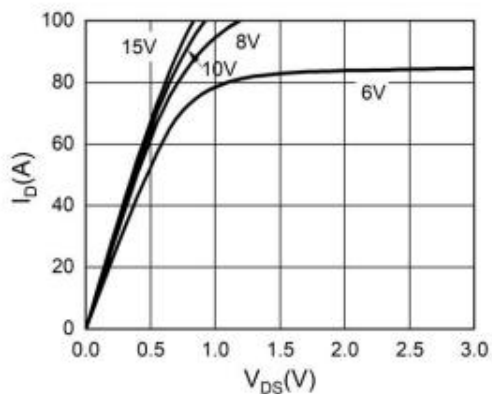


Figure 2. Transfer Characteristics

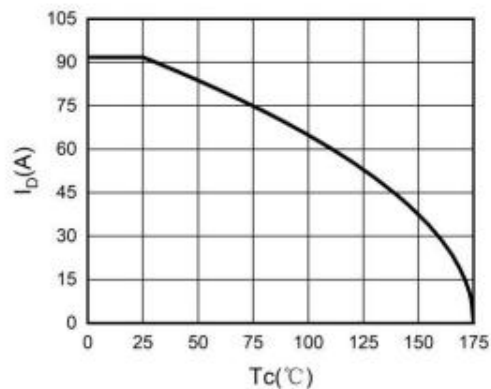
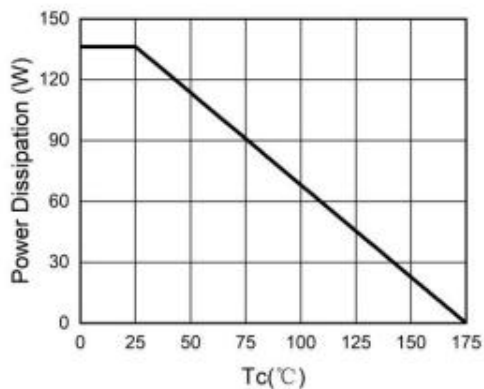
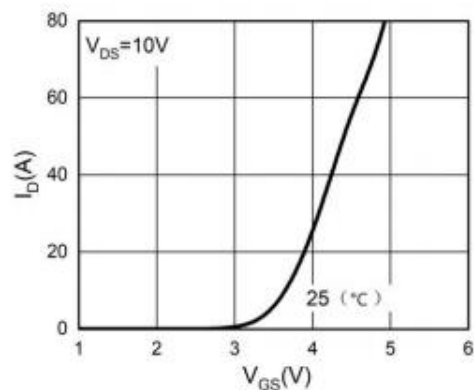


Figure 5. BV_{DS} vs Junction Temperature

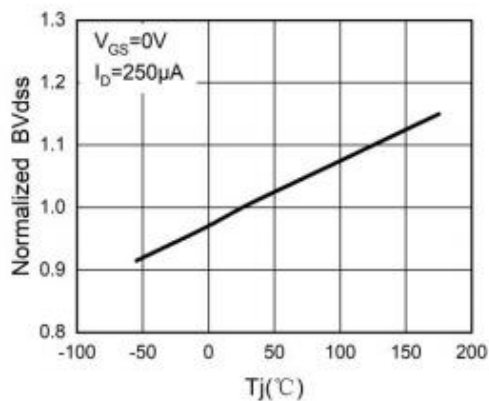


Figure 6. $R_{DS(ON)}$ vs Junction Temperature

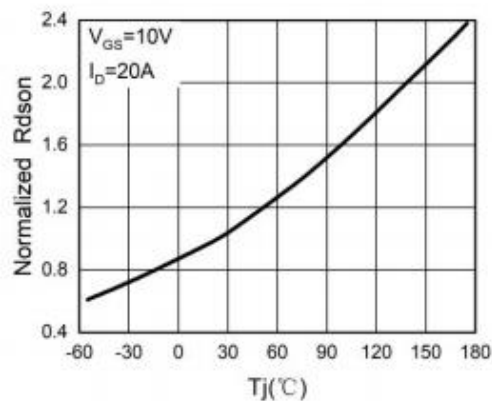


Figure 7. Gate Charge Waveforms

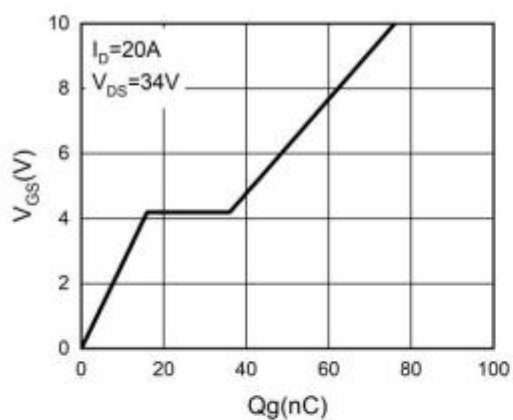


Figure 8. Capacitance

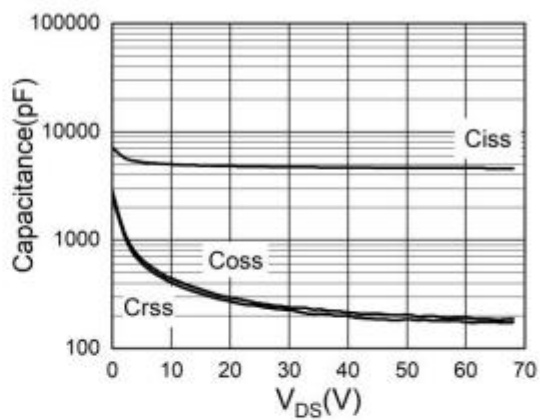


Figure 9. Body-Diode Characteristics

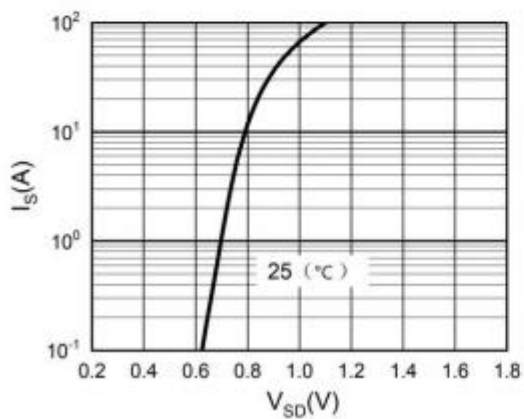
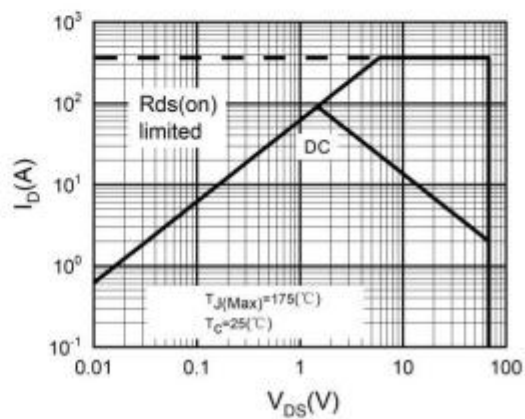
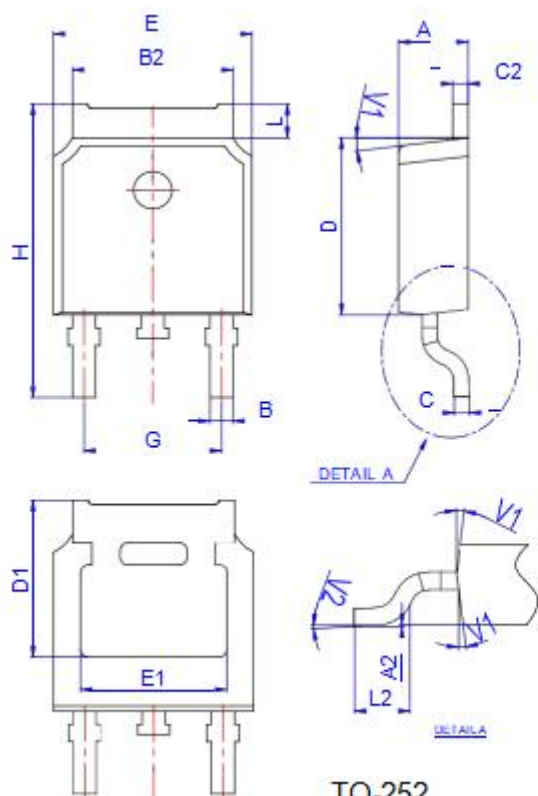


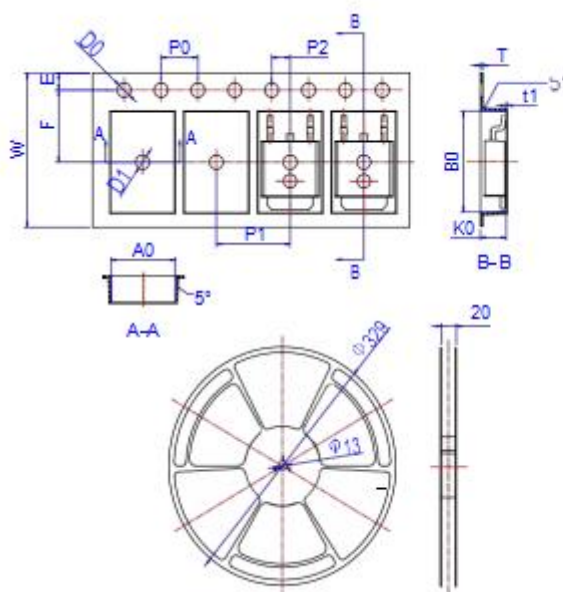
Figure 10. Maximum Safe Operating Area



Package Dimensions TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
MAX						
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583



Important Notice and Disclaimer

HL Microelectronics reserves the right to make changes to this document and its products and specifications at any time without notice.

Customers should obtain and confirm the latest product information and specifications before final design, purchase or use.

HL Microelectronics makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, not does HL Microelectronics assume any liability for application assistance or customer product design.

HL Microelectronics does not warrant or accept any liability with products which are purchased or used for any unintended or unauthorized application.

No license is granted by implication or otherwise under any intellectual property rights of HL Microelectronics.

HL Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of HL Microelectronics.