

• General Description

The AGM01T08LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

• Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

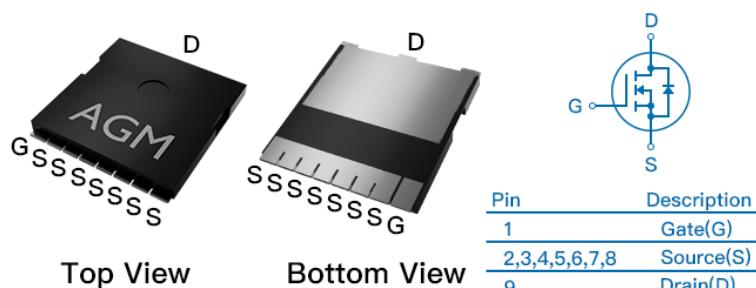
• Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
85V	1.2mΩ	385A

TOLL Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM01T08LL	AGM01T08LL	TOLL	330mm	25mm	2000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	85	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	385	A
	Drain Current-Continuous(Tc=100°C)	340	A
IDM (pulse)	Drain Current-Pulsed (Note 2)	1540	A
PD	Maximum Power Dissipation(Tc=25°C)	500	W
	Maximum Power Dissipation(Tc=100°C)	200	W
EAS	Avalanche energy (Note 3)	3136	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady State) ¹	---	40	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	0.25	°C/W

Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	85	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=80V, VGS=0V	--	--	1	µA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250µA	2.0	--	4.0	V
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=100A	--	1.2	1.5	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=40V, VGS=0V, F=1MHZ	--	13855	--	pF
Coss	Output Capacitance		--	2212	--	pF
Crss	Reverse Transfer Capacitance		--	752	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	1.9	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V, VDS=42V, ID=50A, RGEN=4.5Ω	--	40	--	nS
tr	Turn-on Rise Time		--	122	--	nS
td(off)	Turn-Off Delay Time		--	111	--	nS
tf	Turn-Off Fall Time		--	137	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=42V, ID=50A	--	240	--	nC
Qgs	Gate-Source Charge		--	56	--	nC
Qgd	Gate-Drain Charge		--	58	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	385	A
VSD	Forward on Voltage	VGS=0V, IS=50A	--	--	1.2	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/µs , TJ=25°C	--	137	--	ns
Qrr	Reverse Recovery Charge		--	347	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C, VDD=40V, Vgs=10V , ID=112A, L=0.5mH, RG=25ohm

Figure 1. Maximum Continuous Drain Current vs Case Temperature

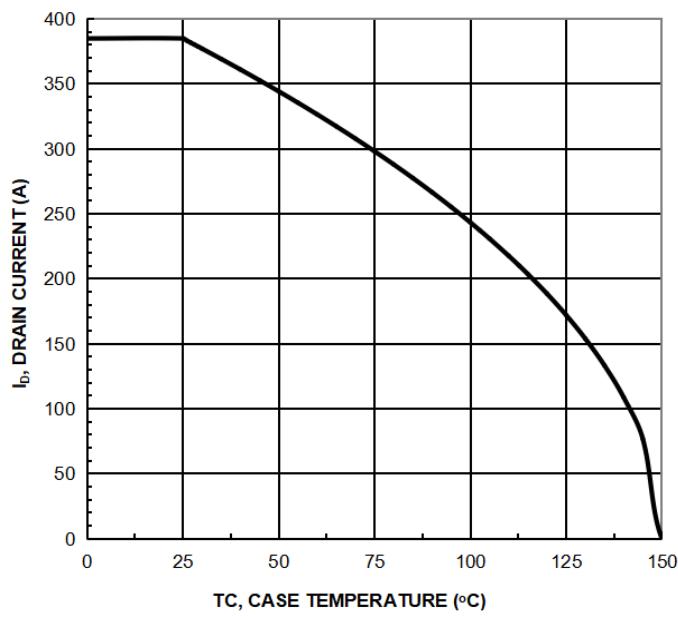


Figure 2. Typical Output Characteristics

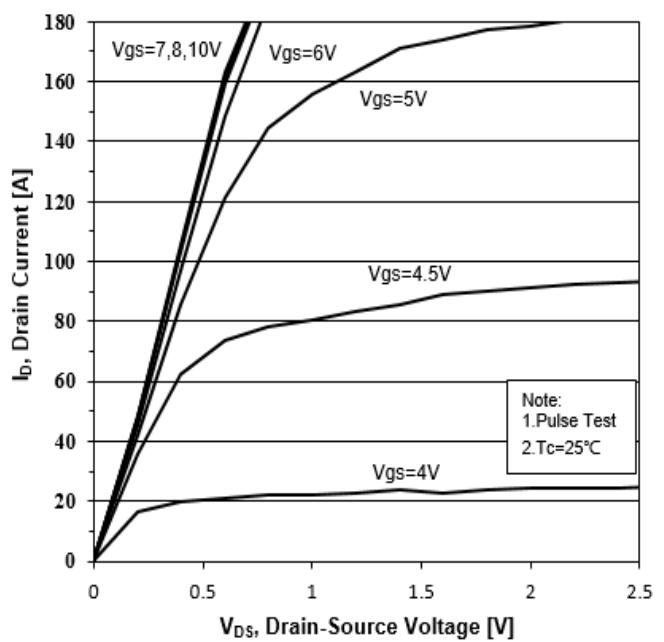


Figure 3. Typical Transfer Characteristics

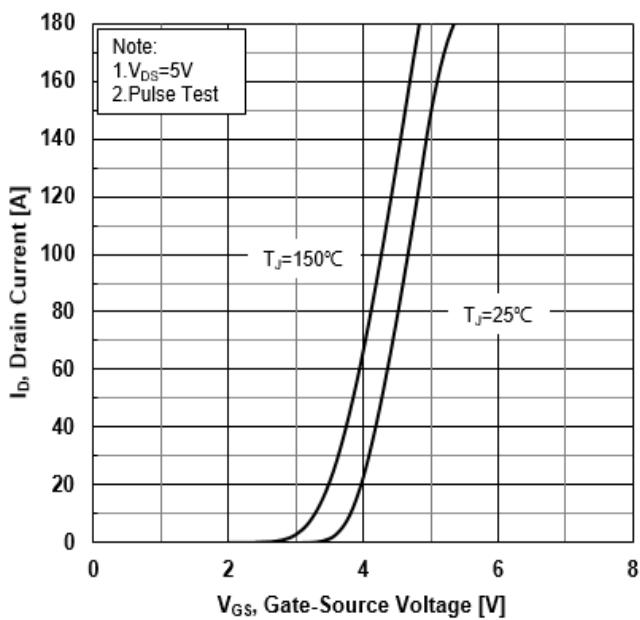


Figure 4. Source-Drain Diode Forward Characteristics

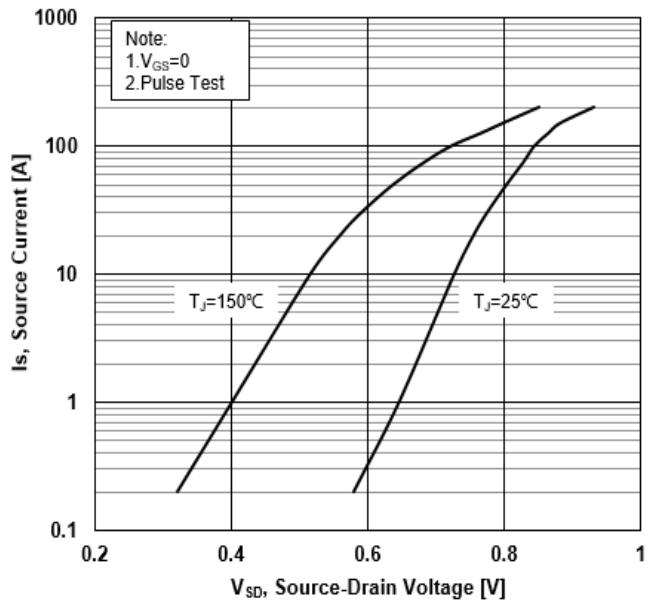


Figure 5. Drain-Source On-Resistance vs Drain Current

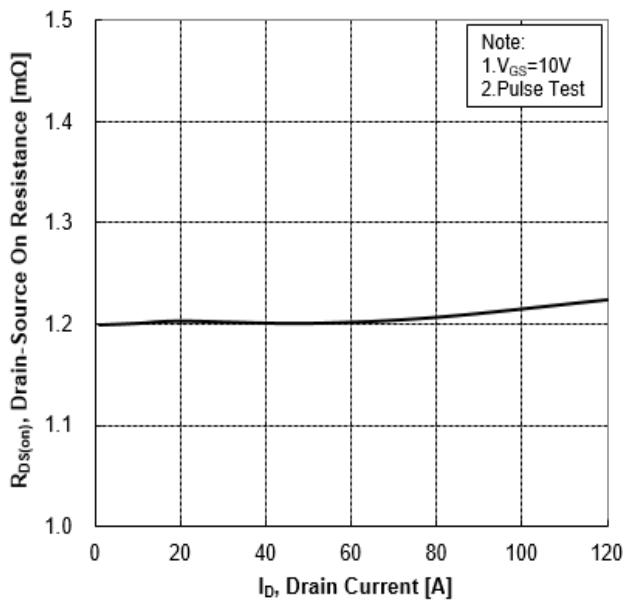


Figure 6. Normalized On-Resistance vs Junction Temperature

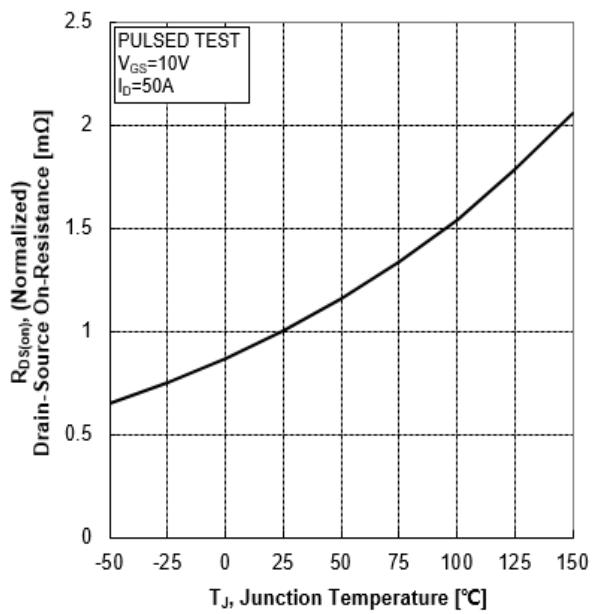


Figure 7. Normalized Threshold Voltage vs Junction Temperature

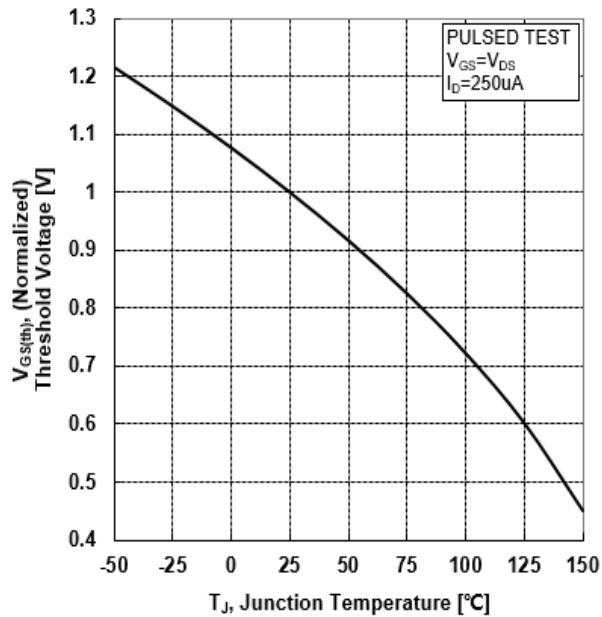


Figure 8. Normalized Breakdown Voltage vs Junction Temperature

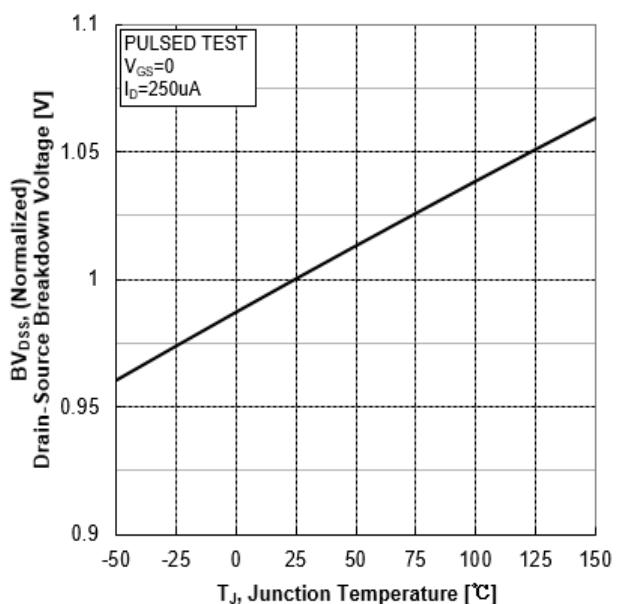


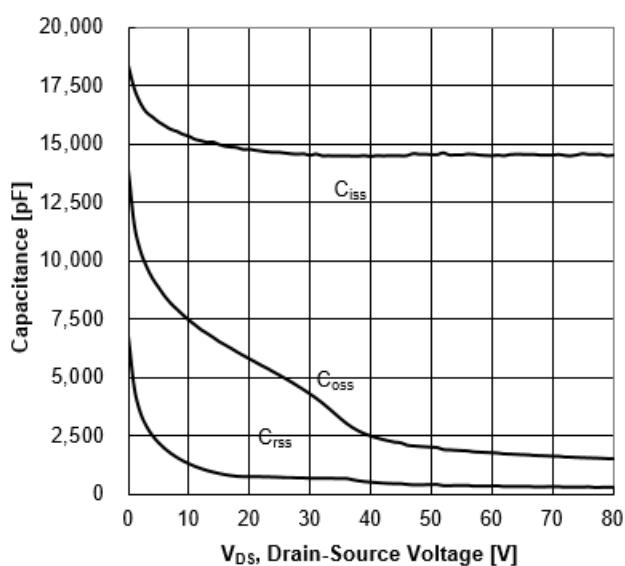
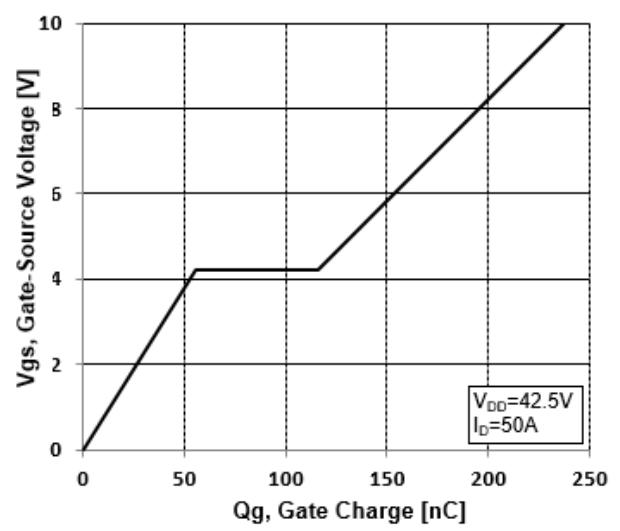
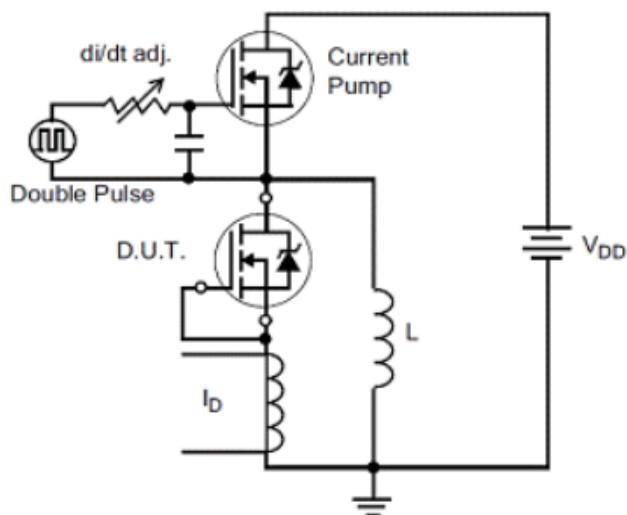
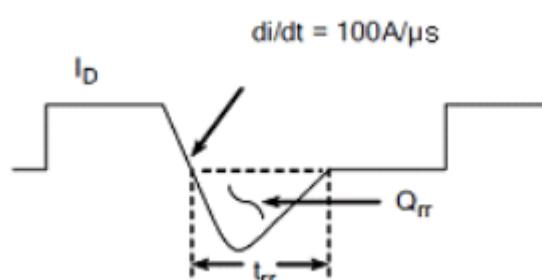
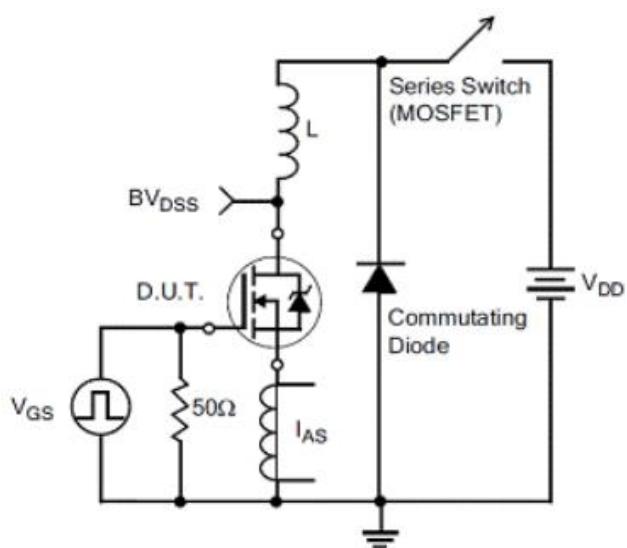
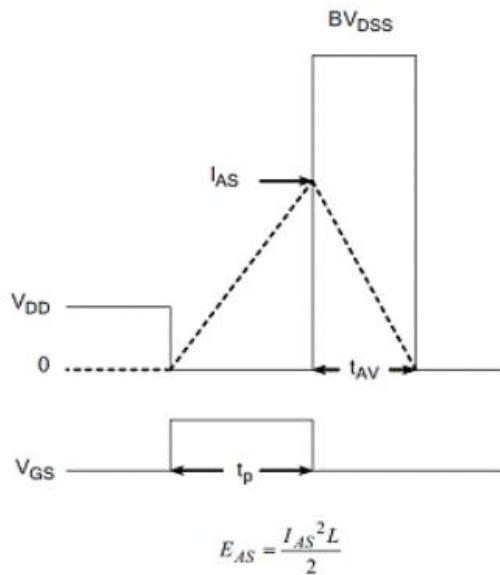
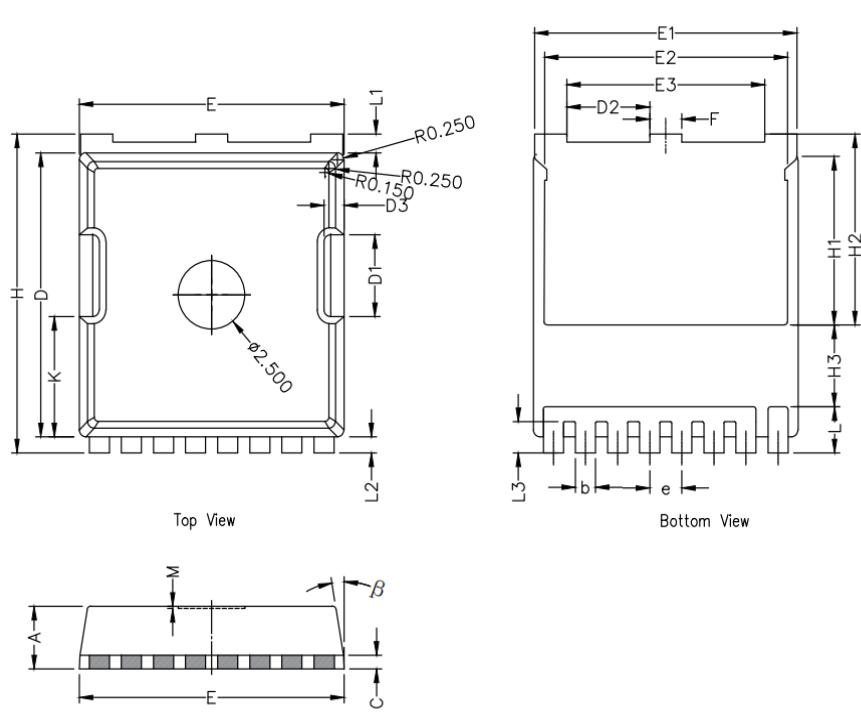
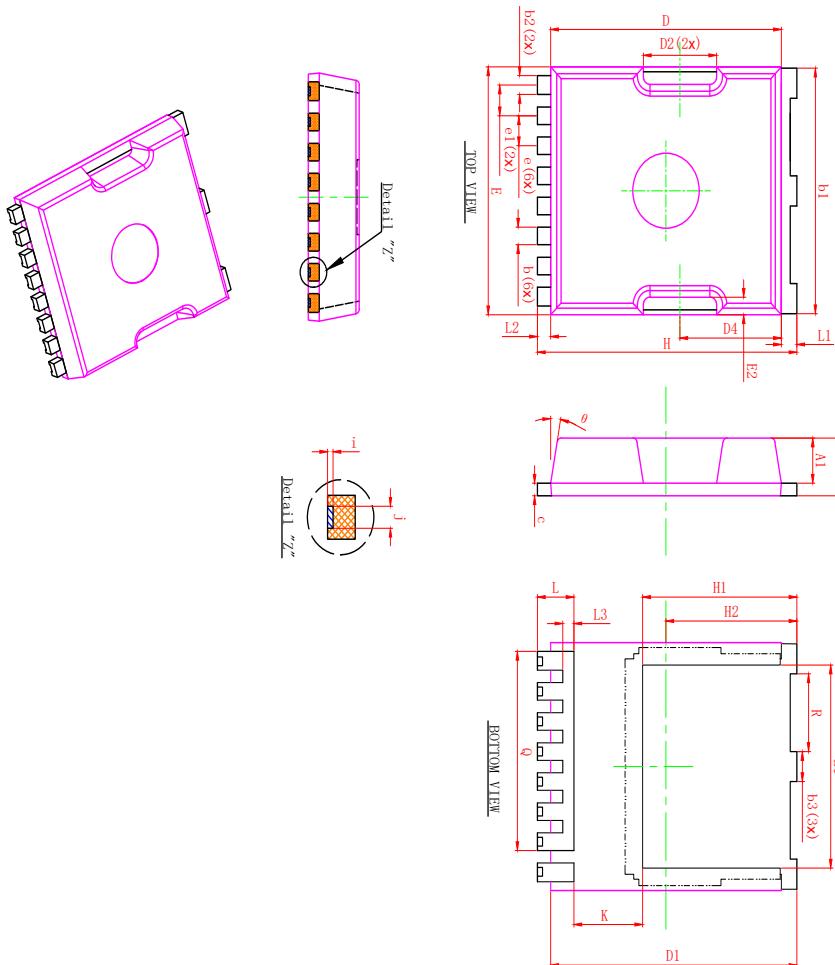
Figure 9. Capacitance Characteristics**Figure 10. Typical Gate Charge vs Gate-Source Voltage**

Figure 18. Diode Reverse Recovery Test Circuit

Figure 19. Diode Reverse Recovery Waveform

Figure 20. Unclamped Inductive Switching Test Circuit

Figure 21. Unclamped Inductive Switching Waveform


TOLL Package



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