

# Motor/Actuator Drivers for DC Brush Motor series

# Automotive 2ch 60V Max, H-bridge Drivers



# BD16925EFV-M

### Description

The acceptable reversal motor driver for output 1.0A automotive 2 motors is a motor driver that can set to four modes (normal rotation, reverse rotation, the stop (idling), and the brake) according to two input logic.

It is possible to contribute to high reliability, energy-saving, and the lowering the cost of the set because a high resisting pressure (maximum ratings 60V) and the low ON resistance and small packages have been achieved.

### ● Feature

- 1 Built-in 1.0A DMOS H bridge output 2 circuit
- 2 input control (stand by, normal rotation, reverse rotation, brake)
- Low stand by current
- Built-in output reversely electromotive pressure absorption diode
- Built-in overcurrent protection circuit (detection and timer)
- Built-in overpower-supply voltage output OFF function
- Built-in thermal shutdown (TSD)
- Built-in protection state output (PO) terminal

# Key Specifications

Power supply voltage 8 to 16V
Operating temperature range -40 to 110°C
Output current 1.0A
Output ON resistance 1

■ Output ON resistance1 2.25Ω(TYP)

# ●Package(s)

HTSSOP-B24 7.8mm×7.6mm×1.2mm



# Applications

For automotive (Body equipment, air conditioner, and door mirror, etc)

### Typical Application Circuits

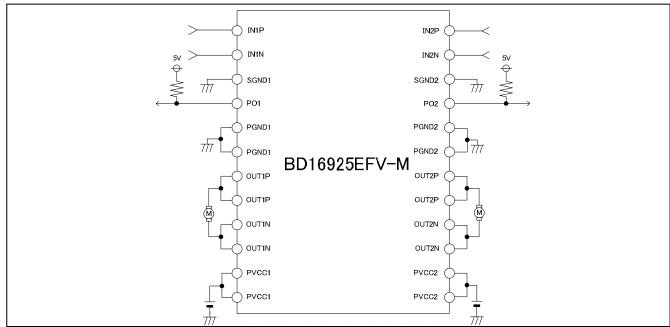


Figure 1: Typical Application Circuit

OProduct structure : Silicon monolithic integrated circuit

OThis product is not designed protection against radioactive rays.

# Pin Configuration

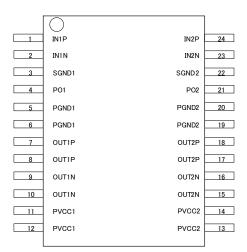


Figure2: Pin Configuration

### Pin description

Trill descrip	Juon		
HTSSOP	0 1 1	D	
-B24	Symbol	Description	
1	IN1P	Output state control input terminal	
2	IN1N	Output state control input terminal	
3	SGND1	Small signal GND terminal	
4	PO1	Output state output terminal (Open drain)	
5	PGND1	Output part GND	
6	PGND1	Output part GND	
7	OUT1P	Motor output terminal	
8	OUT1P	Motor output terminal	
9	OUT1N	Power supply terminal	
10	OUT1N	Motor output terminal	
11	PVCC1	Power supply terminal	
12	PVCC1	Power supply terminal	
13	PVCC2	Power supply terminal	
14	PVCC2	Power supply terminal	
15	OUT2N	Motor output terminal	
16	OUT2N	Power supply terminal	
17	OUT2P	Motor output terminal	
18	OUT2P	Motor output terminal	
19	PGND2	Output part GND	
20	PGND2	Output part GND	
21	PO2	Output state output terminal (Open drain)	
22	SGND2	Small signal GND terminal	
23	IN2N	Output state control input terminal	
24	IN2P	Output state control input terminal	

# ●Block Diagram(s)

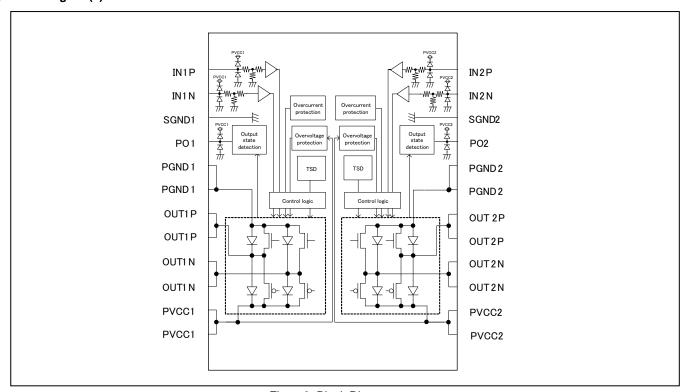


Figure3: Block Diagram

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	PVCC1、PVCC2	60	V
Outrut valta ra	OUT1P/2P,		V
Output voltage	OUT1N/2N	60	
Input voltage	VIN1P/2PVIN1N/2N	-0.3 to 20 *1	V
Output current	lo	1.0 *2	А
Power dissipation	Pd	3.99 *3	W
Operating temperature range	Topr	-40 to 110	°C
Storage temperature range	Tstg	-55 to 150	°C
Junction temperature	Tjmax	150	οຶ

# **Operating conditions** (Ta=25°C)

Parameter	Parameter Symbol		Unit
Range of Power-supply	PVCC1、PVCC2	8 to 16	V
voltage operation	1 1001( 1 1002	0 10 10	v

<sup>\*1</sup> However, PVCC1,2>IN1P/2P,IN1N/2N

<sup>\*2</sup> However, exceed neither Pd nor ASO.
\*3 IC mounted on ROHM standard board(70mm×70mm×1.6mm, glass epoxy 4 layers board) Reduce power by 31.9mW for each degree above 25°C.

●Electrical characteristics (Unless otherwise noted, PVCC1,2 = 8V to 16V, Ta = -40°C to 110°C)

Doromotor	Current ed	Standard value					
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Condition	
Circuit current 1	lcc1	-	0	10	μΑ	CH1, CH2 : Stand-by	
Circuit current 2	lcc2	-	8	16	mA	CH1, CH2 : forward or reverse rotation	
Circuit current 3	lcc3	-	8	16	mΑ	CH1, CH2 : Brake	
Input H voltage	VIH	3.0	-	-	<b>V</b>		
Input L voltage	VIL	-	-	1.0	V		
Input H current	lін	-	50	100	μΑ	IN1P/2P,IN1N/2N = 5.0V, Inflow current	
Input L current	lı∟	-	0	10	μΑ	IN1P/2P,IN1N/2N = 0.0V, Outflow current	
Output ON resistance1	Ron1	-	2.25	3.50	Ω	PVCC1,2 = 12V to 16V, Io = $0.1A \cdot 0.8A$ Ta = $-40$ °C to 25°C, Upper and lower total	
Output ON resistance 2	Ron2	-	3.00	4.13	Ω	PVCC1,2 = 8V to 12V, Io = 0.1A • 0.8A  Ta = -40°C to 25°C,  Upper and lower total	
Output ON resistance 3	Ron3	-	3.50	4.50	Ω	PVCC1,2 = 12V to 16V, lo = 0.1A · 0.8A  Ta = 25°C to 110°C,  Upper and lower total	
Output ON resistance 4	Ron4	-	4.75	5.75	Ω	PVCC1,2 = 8V to 12V, Io = 0.1A • 0.8A  Ta = 25°C to 110°C,  Upper and lower total	
Output leak H	ILH	-	0	10	μΑ	OUT1P/2P, OUT1N/2N = 0V	
Output leak L	ILL	-	0	10	μΑ	OUT1P/2P, OUT1N/2N = PVCC1,2	
Output diode voltage H	VFH	0.2	0.8	1.4	V	IF = 0.6A	
Output diode voltage L	VFL	0.2	0.8	1.4	V	IF = 0.6A	
Protection output voltage	VLPO	-	0.3	0.6	٧	I = 3mA	
Protection output leak current	ILPO	-	0	10	μA	PO1,2 = PVCC1,2	
Overcurrent detect current	locp	1.1	1.9	2.8	Α		
TSD operation temperature	TTSD	150	175	-	°C		
Overvoltage detect voltage	Vovp	25	30	35	٧		

OA radiation is not designed.

# ●Truth table

Input		Ou	tput	0 " 1
IN1P/2P	IN1P/2N	OUT1P/2P	OUT1P/2N	Operation mode
Н	Н	L	L	Brake
Н	L	Н	L	Forward rotation
L	Н	L	Н	Reverse rotation
L	L	Open	Open	Stand-by

# ●Timing chart

PO1,2 timing chart (INP=H, INN=L At Normal rotation, Ta=25°C )

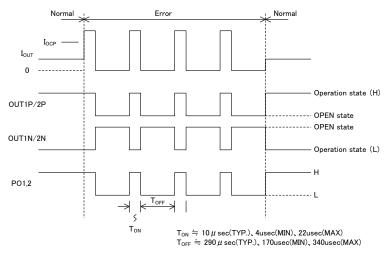


Figure4: PO1,2 timing chart (INP=H, INN=L at normal rotation)

# Overvoltage protection timing chart

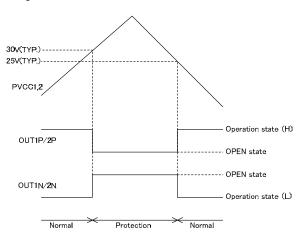


Figure5: Overvoltage protection timing chart

# TSD timing chart

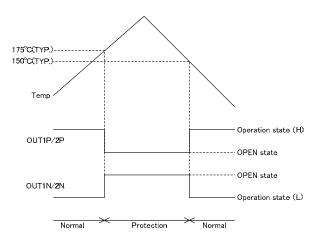


Figure6: TSD timing chart

# ●Example of recommended circuit

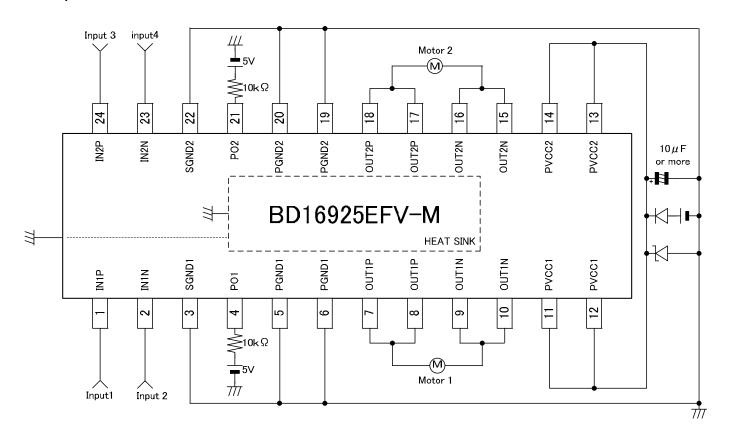


Figure7: BD16925EFV-M Recommended circuit diagram

- %The external circuit constant in figure is a recommended value.
- \*External resistance of PO1 and PO2 is a pull-up resistor.
- %Please wire the vicinity of the pin of IC for the power supply decoupling capacitor.

# ●Power dissipation

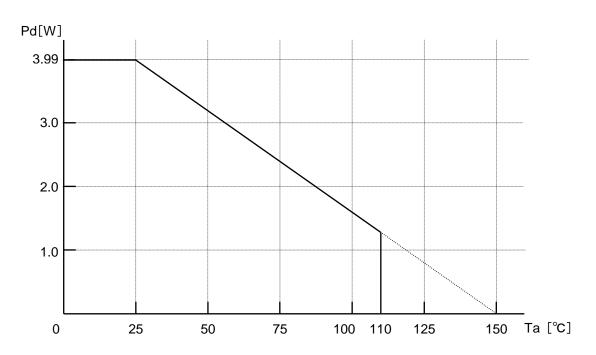


Figure8: BD16925EFV-M Power dissipation

IC mounted on ROHM standard board(70mm×70mm×1.6mm, glass epoxy 4 layers board )
Reduce power by 31.9mW for each degree above 25°C.

# ●ASO(Area of Safety Operation)

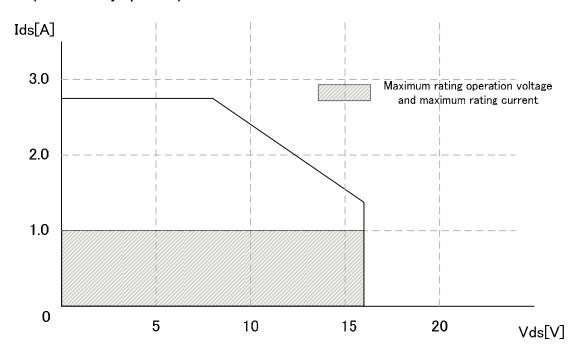


Figure9: BD16925EFV-M ASO characteristic

# ●Input/Output circuit diagram

\*Resistance in figure is Typ value.

# 1) IN1P,IN1N,IN2P,IN2N

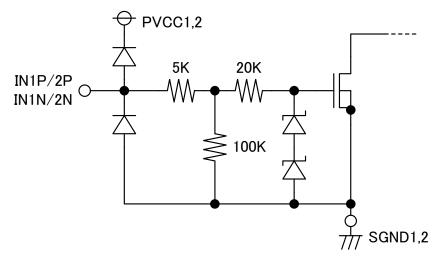


Figure 10: nput circuit diagram

### 2) PO1,2

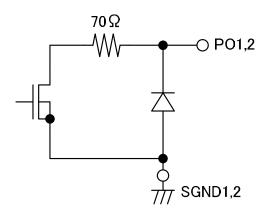


Figure11: PO circuit diagram

# 3) OUT1P,OUT1N,OUT2P,OUT2N

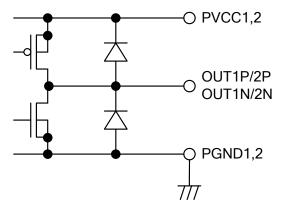


Figure 12: Output circuit diagram

### Operational Notes

### 1) Absolute maximum ratings

We are careful enough for quality control about this IC. So, there is no problem under normal operation, excluding that it exceeds the absolute maximum ratings. However, this IC might be destroyed when the absolute maximum ratings, such as impressed voltages or the operating temperature range(Topr), is exceeded, and whether the destruction is short circuit mode or open circuit mode cannot be specified. Please take into consideration the physical countermeasures for safety, such as fusing, if a particular mode that exceeds the absolute maximum rating is assumed.

### 2) GND line

The ground line is where the lowest potential and transient voltages are connected to the IC.

Please keep the lowest potential any state of operation about the potential of 3, 5, 6, 19, 20, 22PIN and the heat radiation pad on the back of the package.

### 3) Input terminal

Please do not add the voltage to each input terminal when you do not impress PVCC1,2 to IC.

Please set the voltage in each input terminal within the range of the guarantee of an electric characteristic or voltage of PVCC1,2 or less when you impress the PVCC1,2 voltage.

### 4) Reversely electromotive pressure

Reversely electromotive pressure might change depending on use conditions, the environment and individual characteristic of the motor. Please confirm there is no problem in the operation of IC etc. by reversely electromotive pressure enough.

### 5) Large current line

A large current flows to motor power supply PVCC1,2 and motor PGND1,2 terminal of IC. Therefore, the return to the input by this large current is caused according to the external circuit constant of the capacity of the capacitor between power supply-GND and board pattern layout. It becomes an undesirable result like the malfunction and the oscillation, etc.

### 6) PVCC1, 2

Please put the power supply decoupling capacitor between power supply and GND.

Please confirm there is no problem in various characteristics enough and decide the capacity value.

Recommended value is 10uF as shown in P.6

### 7) Power dissipation

Power dissipation is changed by the state of the board mounting and the mounting environment of IC, and take care enough about the heat design.

### 8) Power consumption

Power consumption changes greatly depending on the power supply voltage and the output current. Please design heat after considering the thermal resistance data and the transition thermal resistance data so as not to exceed ratings in consideration of power dissipation.

### 9) ASO

Please set not to exceed ASO (area of safe operation) the output current and the power supply voltage.

- 10) The circuit that limits the inrush current is not built into this IC. Therefore, please do physical measures such as putting the current limitation resistance.
- 11) There is a possibility that the trouble of the malfunction occurs if the potential of the output terminal widely swings to the potential of GND or less according to the condition of generation of heat, the power-supply voltage, and the use motor in this IC. For that case, please do measures that trouble doesn't occur such as schottky diode is added between outputs-GND.

# 12) Operation in strong electromagnetic field

This IC doesn't do the design that assumes use in strong electromagnetic field. Please confirm there is no problem in the operation of IC by the board pattern layout and the circuit constant enough

### 13) Heat radiation pad

The heat radiation pad is connected with sub of IC, and connect with the GND potential.

Please do not use the heat radiation pad as GND wiring.

### 14) Overpower supply voltage output OFF function

Overpower-supply voltage output OFF function is built into as output protection at the overvoltage this IC. When the impressed voltage to the terminal PVCC1,2 becomes 30V(typ) or more, the output terminal is opened. However, it becomes only normal rotation • reverse rotation • a brake as an operation condition. Please note that this function doesn't operate at the standby. Please do not exceed the absolute maximum rating so that there is a possibility of destruction when the absolute maximum rating of the power-supply voltage is exceeded though overpower-supply voltage output OFF function is built into.

### 15) Overcurrent protection

The overcurrent protection circuit is built into as destruction measures of output short in this IC.

This circuit opens the output during 290µsec(typ) when the current of 1.9A(typ) flows during 10µsec(typ), and returns to normal operation afterwards. If the overcurrent keeps flowing when returning normally, this state is repeated. Generation of heat and the deterioration of IC occur when the state of the overcurrent continues though the overcurrent protection is a function to prevent IC being destroyed by a short output etc. When the state continues keeping the flow of the overcurrent by using the terminal PO (When operating that the terminal PO shows in P.6), please do measures such as making IC a standby in the application.

### 16) Thermal shutdown

The thermal shutdown circuit is built into as an overheating protection measures this IC. When the Chip temperature of IC becomes 175°C(typ) or more, the output is opened. When the Chip temperature of IC becomes 150°C(typ) or less, returns to normal operation. The thermal shutdown circuit aims to intercept IC from high temperature. The guarantee and protection of IC are not purpose. Therefore, please do not use this IC after thermal shutdown circuit operates, nor use it for assumption that operates the thermal shutdown circuit.

### 17) Input terminal

This IC is a monolithic IC, and has P+ isolation and P substrate for the element separation. Therefore, a parasitic PN junction is firmed in this P-layer and N-layer of each element. For instance, the resistor or the transistor is connected to the terminal as shown in the figure below. When the GND voltage potential is greater than the voltage potential at Terminals A or B, the PN junction operates as a parasitic diode. In addition, the parasitic NPN transistor is formed in said parasitic diode and the N layer of surrounding elements close to said parasitic diode. These parasitic elements are formed in the IC because of the voltage relation. The parasitic element operating causes the wrong operation and destruction. Therefore, please be careful so as not to operate the parasitic elements by impressing to input terminals lower voltage than GND(P substrate). Please do not apply the voltage to the input terminal when the power-supply voltage is not impressed. Moreover, please impress each input terminal lower than the power-supply voltage or equal to the specified range in the guaranteed voltage when the power-supply voltage is impressing.

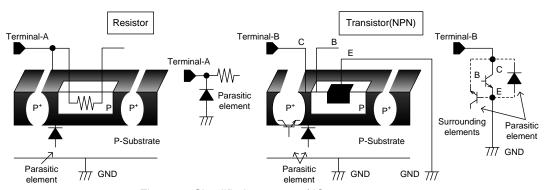


Figure 13: Simplified structure of IC

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

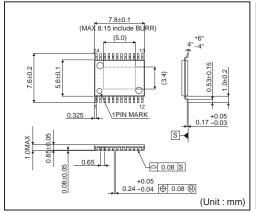
If there are any differences in translation version of this document formal version takes priority

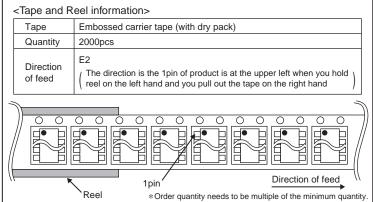
# Ordering Information



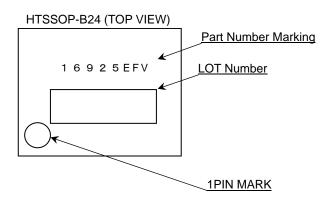
# ● Physical Dimension Tape and Reel Information

# HTSSOP-B24





# ● Marking Diagram(s)(TOP VIEW)



# **Notice**

### **Precaution on using ROHM Products**

1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

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JÁPAN	USA	EU	CHINA
CLASSⅢ	CLACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
- 3. Our Products are not designed under any special or extraordinary environments or conditions, as exemplified below. Accordingly, ROHM shall not be in any way responsible or liable for any damages, expenses or losses arising from the use of any ROHM's Products under any special or extraordinary environments or conditions. If you intend to use our Products under any special or extraordinary environments or conditions (as exemplified below), your independent verification and confirmation of product performance, reliability, etc, prior to use, must be necessary:
  - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

# **Precaution for Mounting / Circuit board design**

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

# **Precautions Regarding Application Examples and External Circuits**

- If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

# **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

### **Precaution for Product Label**

QR code printed on ROHM Products label is for ROHM's internal use only.

### **Precaution for Disposition**

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