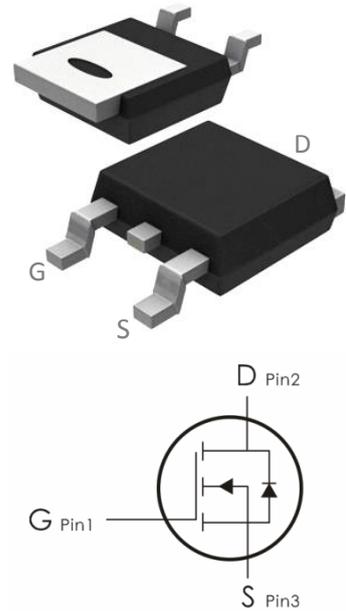


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=30V, I_D=30A, R_{DS(ON)} < 22m\ \Omega @ V_{GS}=4.5V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.



## Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOD30N03-L	30N03-L	TO- 252	2500 pcs/Reel

## Absolute Maximum Ratings: ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_C=25^\circ\text{C}^1$	30	A
	Continuous Drain Current- $T_C=100^\circ\text{C}$	16	
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	92	
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	20	mJ
$P_D$	Power Dissipation, $T_C=25^\circ\text{C}$	25	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ\text{C}$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	4	$^\circ\text{C}/\text{W}$

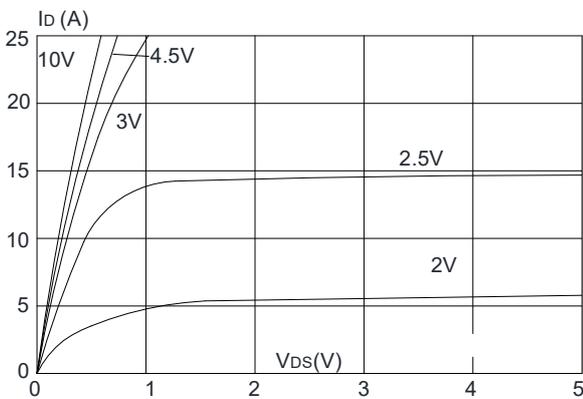
**Electrical Characteristics:** ( $T_A=25^{\circ}\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\ \mu\text{A}$	30	---	---	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{GS}=0V, V_{DS}=30V$	---	---	1	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0A$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	GATE-Source Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\ \mu\text{A}$	0.5	0.85	1	V
$R_{DS(on)}$	Drain-Source On Resistance	$V_{GS}=10V, I_D=10A$	---	17	22	$\text{m}\Omega$
	Drain-Source On Resistance	$V_{GS}=4.5V, I_D=8A$	---	19	25	$\text{m}\Omega$
	Drain-Source On Resistance	$V_{GS}=2.5V, I_D=6A$	---	25	35	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1\text{MHz}$	---	750	---	pF
$C_{oss}$	Output Capacitance		---	65	---	
$C_{rss}$	Reverse Transfer Capacitance		---	55	---	
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS}=4.5V, V_{DD}=15V, I_D=2A$ $R_{GEN}=3.3\Omega$	---	17	---	ns
$t_r$	Rise Time		---	48	---	ns
$t_{d(off)}$	Turn-Off Delay Time		---	20	---	ns
$t_f$	Fall Time		---	13	---	ns
$Q_g$	Total Gate Charge	$V_{GS}=4.5V, V_{DS}=15V,$ $I_D=4A$	---	9.5	---	nC
$Q_{gs}$	Gate-Source Charge		---	2.2	---	nC
$Q_{gd}$	Gate-Drain "Miller" Charge		---	2.0	---	nC
<b>Drain-Source Diode Characteristics</b>						
$V_{SD}$	Source-Drain Diode Forward Voltage	$I_D=30A$	---	---	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=10A, T_J=25^{\circ}\text{C}$	---	9.1	---	ns
$Q_{rr}$	Reverse Recovery Charge	$diF/dt=100A/\mu\text{s}$	---	2	---	nC

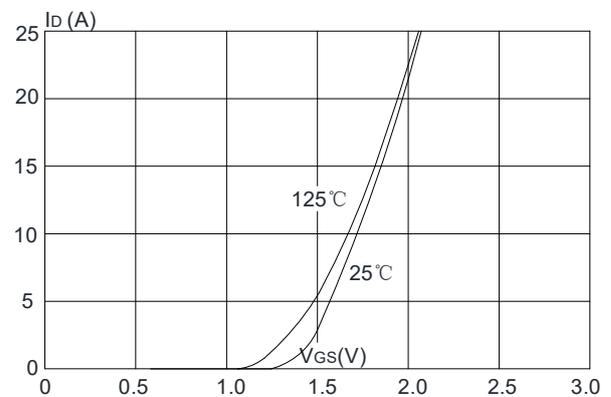
## Notes:

- 1.The data tested by surface mounted on a 1 inch 2 FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width  $\cong$  300us , duty cycle  $\cong$  2%
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=12.7A$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

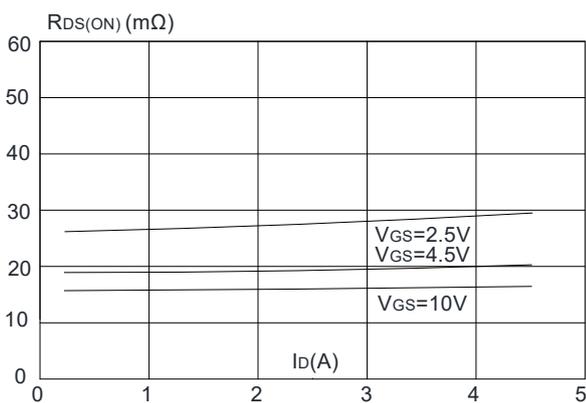
## Typical Characteristics: ( $T_c=25^\circ C$ unless otherwise noted)



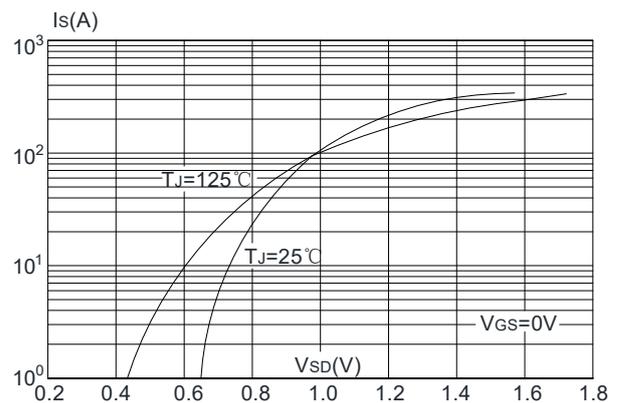
**Figure1:** Output Characteristics



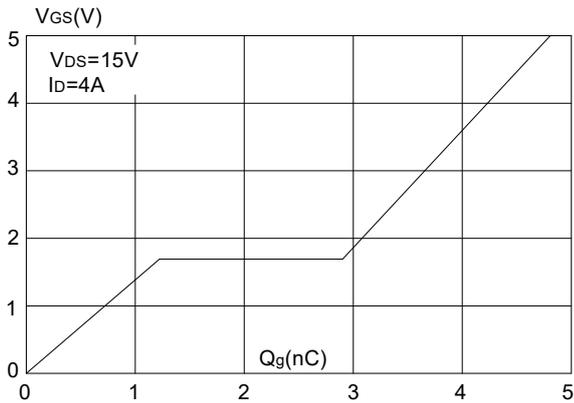
**Figure 2:** Typical Transfer Characteristics



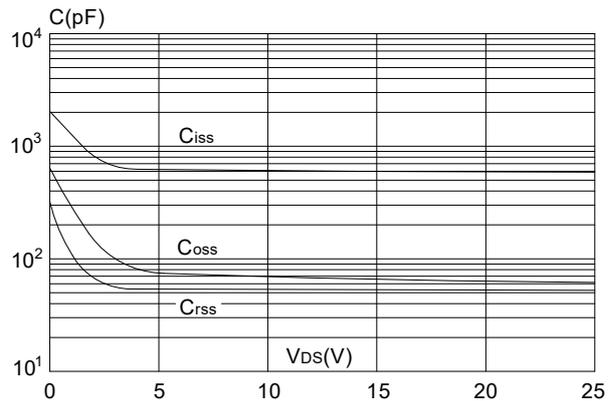
**Figure 3:** On-resistance vs. Drain Current



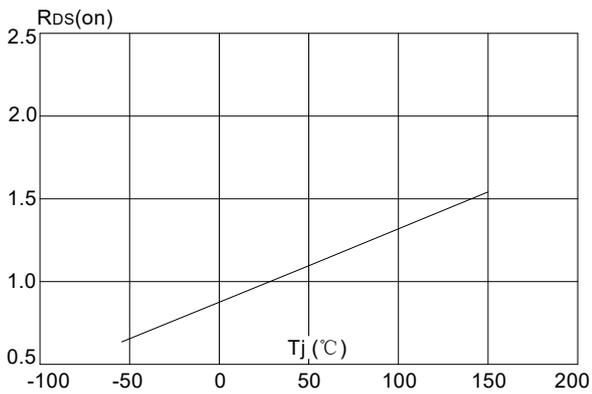
**Figure 4:** Body Diode Characteristics



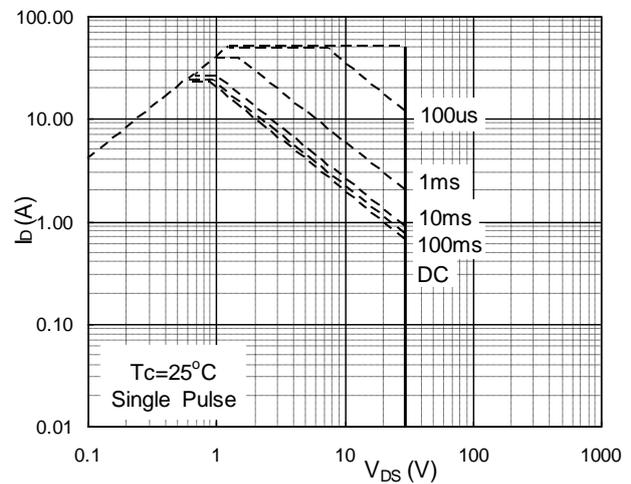
**Figure 5: Gate Charge Characteristics**



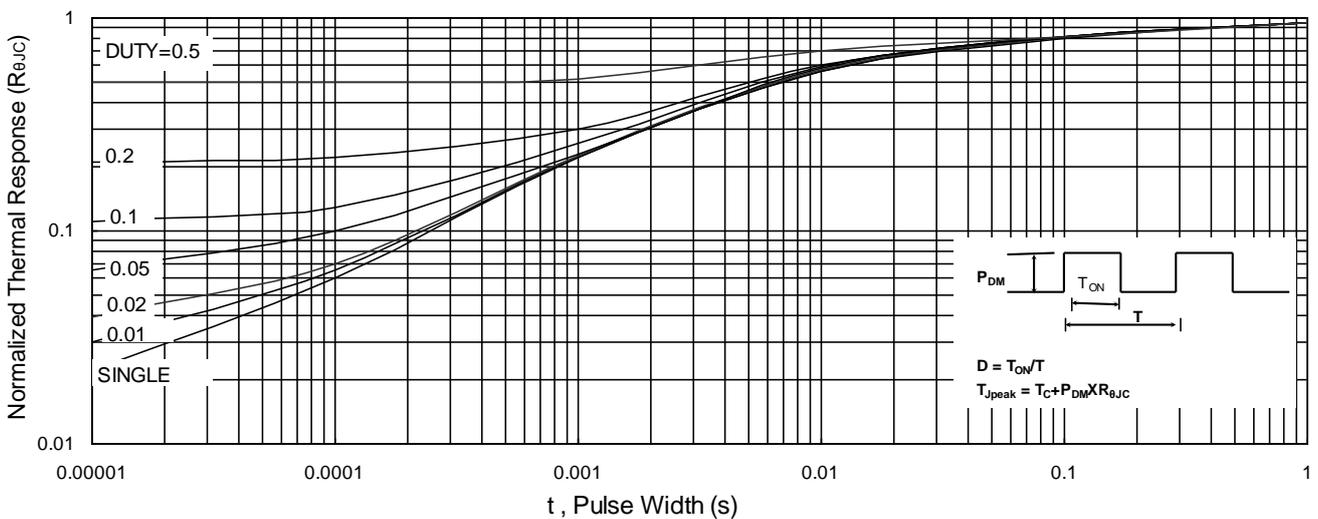
**Figure 6: Capacitance Characteristics**



**Figure 7: Normalized on Resistance vs. Junction Temperature**



**Figure 8: Safe Operating Area**

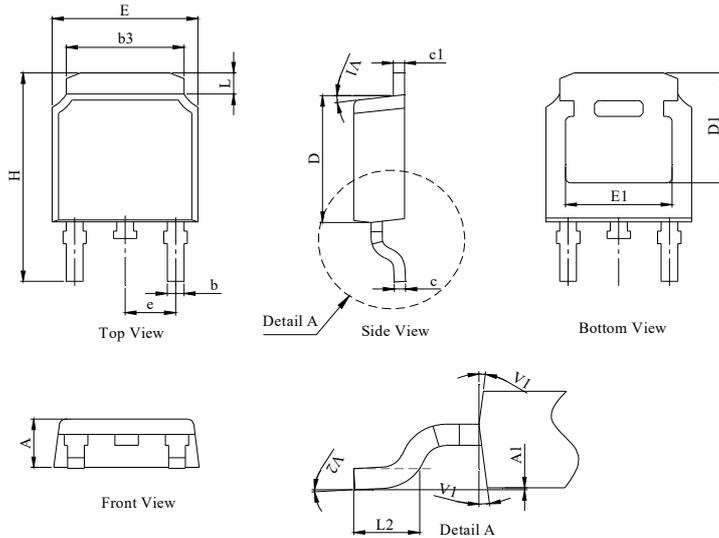


**Figure 9: Normalized Maximum Transient Thermal Impedance**

## TO-252 Package Information

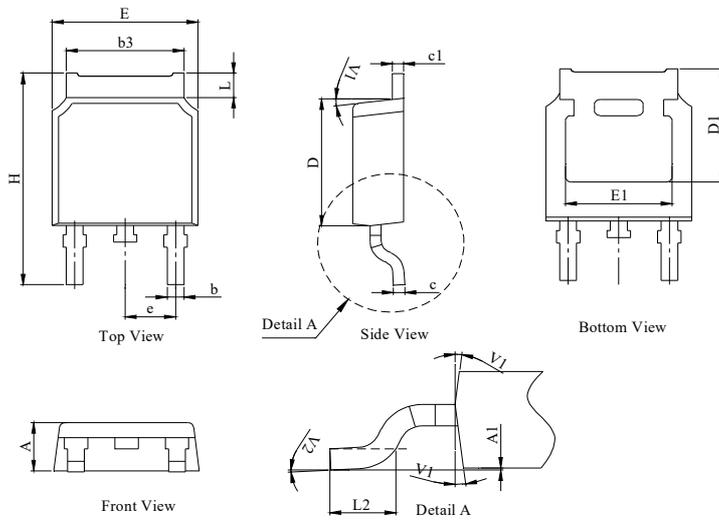
### Package Outline Type-A

UNIT: mm



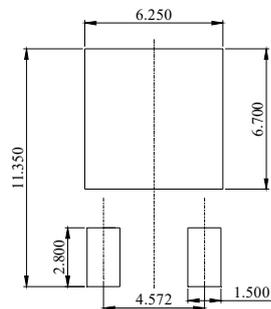
DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.18	2.30	2.39
A1	0	--	0.13
b	0.64	0.76	0.89
c	0.40	0.50	0.61
c1	0.46	0.50	0.58
D	5.97	6.10	6.23
D1	5.05	--	--
E	6.35	6.60	6.73
E1	4.32	--	--
b3	5.21	5.38	5.55
e	2.29 BSC		
H	9.40	10.00	10.40
L	0.89	--	1.27
L2	1.40	--	1.78
V1	7° REF		
V2	0°	--	6°

### Package Outline Type-B



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.10	2.30	2.40
A1	0	--	0.13
b	0.66	0.76	0.86
b3	5.21	5.38	5.55
c	0.40	0.50	0.60
c1	0.44	0.50	0.58
D	5.90	6.10	6.30
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.29 BSC		
H	9.50	10.00	10.70
L	1.09	--	1.21
L2	1.35	--	1.65
V1	7° REF		
V2	0°	--	6°

### Recommended Soldering Footprint



## Marking Information:

①. Doingter LOGO

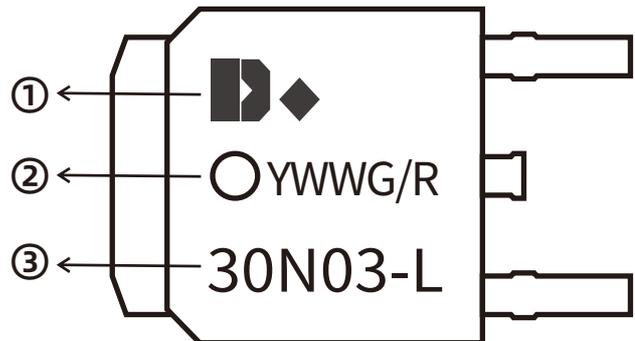
②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)

③. Part NO.



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