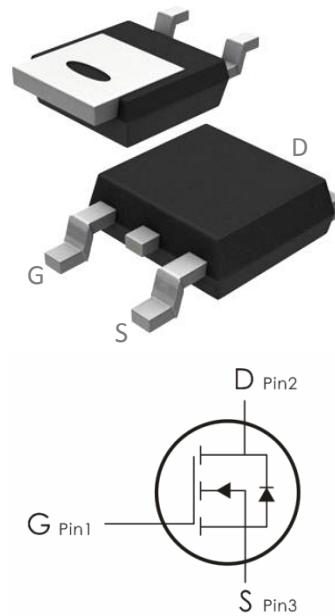


## Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(on)}$  with low gate charge. It can be used in a wide variety of applications.



## Features:

- 1)  $V_{DS}=30V, I_D=120A, R_{DS(ON)}<4m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.

## Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DC004NG-Y	C004N-Y	TO- 252	2500 pcs/Reel

Absolute Maximum Ratings: ( $T_C=25^\circ C$  unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_C=25^\circ C$	120	A
	Continuous Drain Current- $T_C=100^\circ C$	68	
	Pulsed Drain Current <sup>1</sup>	300	
$E_{AS}$	Single Pulse Avalanche Energy	144.7	mJ
$P_D$	Power Dissipation, $T_C=25^\circ C$	100	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +175	°C

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{eJC}$	Thermal Resistance,Junction to Case	1.25	°C/W
$R_{eJA}$	Thermal Resistance,Junction to Ambient	62	

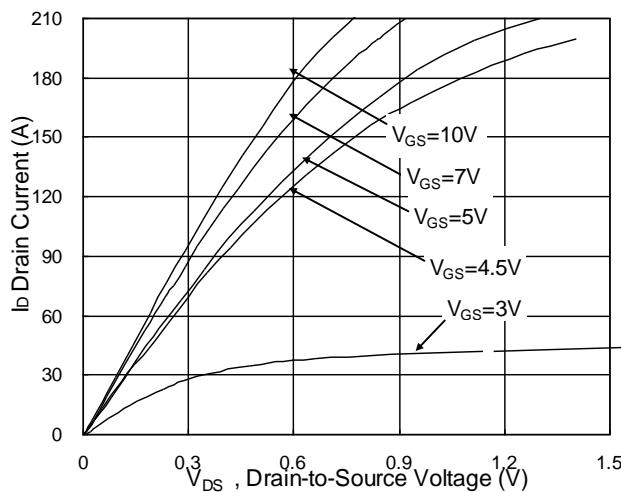
**Electrical Characteristics:** ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
<b><math>\text{BV}_{\text{DSS}}</math></b>	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30	---	---	V
<b><math>I_{\text{DSS}}</math></b>	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=30\text{V}$	---	---	1	$\mu\text{A}$
<b><math>I_{\text{GSS}}</math></b>	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
<b><math>V_{\text{GS(th)}}</math></b>	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	1.5	2.5	V
<b><math>R_{\text{DS(ON)}}</math></b>	Drain-Source On Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	---	3	4	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	---	5.2	6	
<b>Dynamic Characteristics</b>						
<b><math>C_{\text{iss}}</math></b>	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	3075	4000	$\text{pF}$
<b><math>C_{\text{oss}}</math></b>	Output Capacitance		---	400	530	
<b><math>C_{\text{rss}}</math></b>	Reverse Transfer Capacitance		---	315	---	
<b>Switching Characteristics</b>						
<b><math>t_{\text{d(on)}}</math></b>	Turn-On Delay Time <sup>3</sup>	$V_{\text{DD}}=15\text{V}, I_{\text{D}}=30\text{A}, V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=3.3\Omega$	---	9	---	ns
<b><math>t_r</math></b>	Rise Time <sup>2,3</sup>		---	19	---	ns
<b><math>t_{\text{d(off)}}</math></b>	Turn-Off Delay Time		---	58	---	ns
<b><math>t_f</math></b>	Fall Time <sup>2,3</sup>		---	15.2	---	ns
<b><math>Q_g</math></b>	Total Gate Charge <sup>3</sup>	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=30\text{A}$	---	31.6	---	nC
<b><math>Q_{\text{gs}}</math></b>	Gate-Source Charge		---	8.6	---	nC
<b><math>Q_{\text{gd}}</math></b>	Gate-Drain "Miller" Charge		---	11.7	---	nC
<b>Drain-Source Diode Characteristics</b>						
<b><math>V_{\text{SD}}</math></b>	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}$	---	---	1	V
<b><math>I_s</math></b>	Continuous Source Current	$V_G=V_D=0\text{V}, \text{Force Current}$	---	---	96	A
<b><math>I_{\text{sm}}</math></b>	Pulsed Source Current		---	---	192	A
<b><math>T_{\text{rr}}</math></b>	Reverse Recovery Time	$I_F=30\text{A}, dI/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	---	18	---	Ns
<b><math>q_{\text{rr}}</math></b>	Reverse Recovery Charge		---	8	---	nc

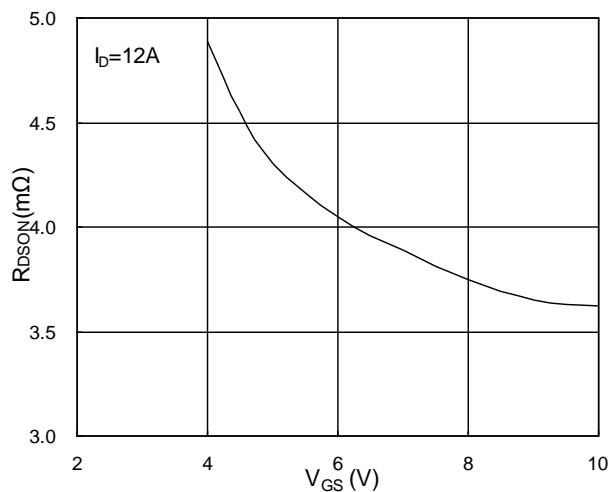
**Notes:**

- 1.The data tested by surface mounted on a 1 inch FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=53.8A$
- 4.The power dissipation is limited by  $175^{\circ}C$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

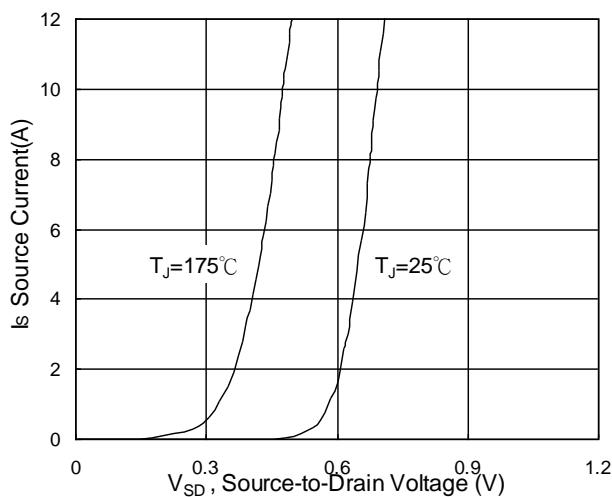
**Typical Characteristics:** ( $T_c=25^{\circ}C$  unless otherwise noted)



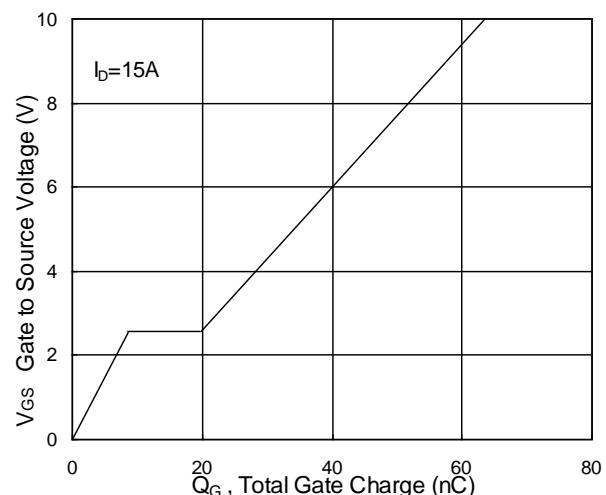
**Fig.1 Typical Output Characteristics**



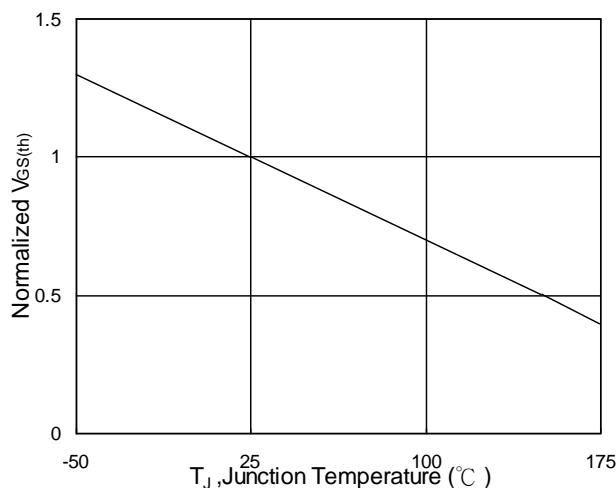
**Fig.2 On-Resistance vs. G-S Voltage**



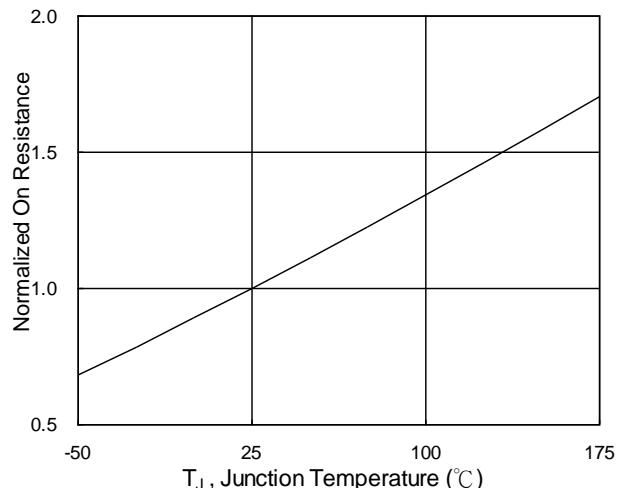
**Fig.3 Forward Characteristics of Reverse**



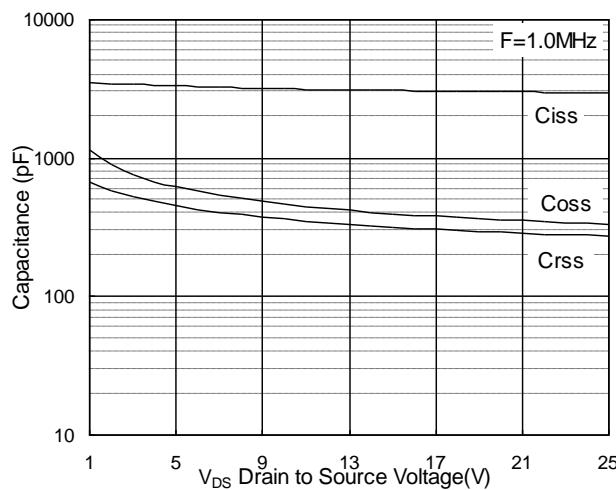
**Fig.4 Gate-Charge Characteristics**



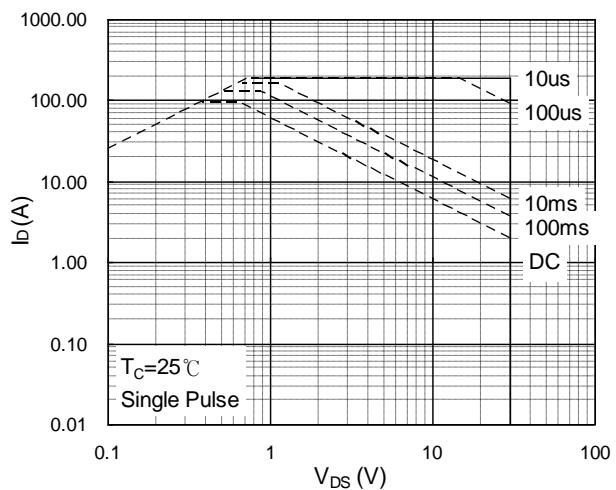
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



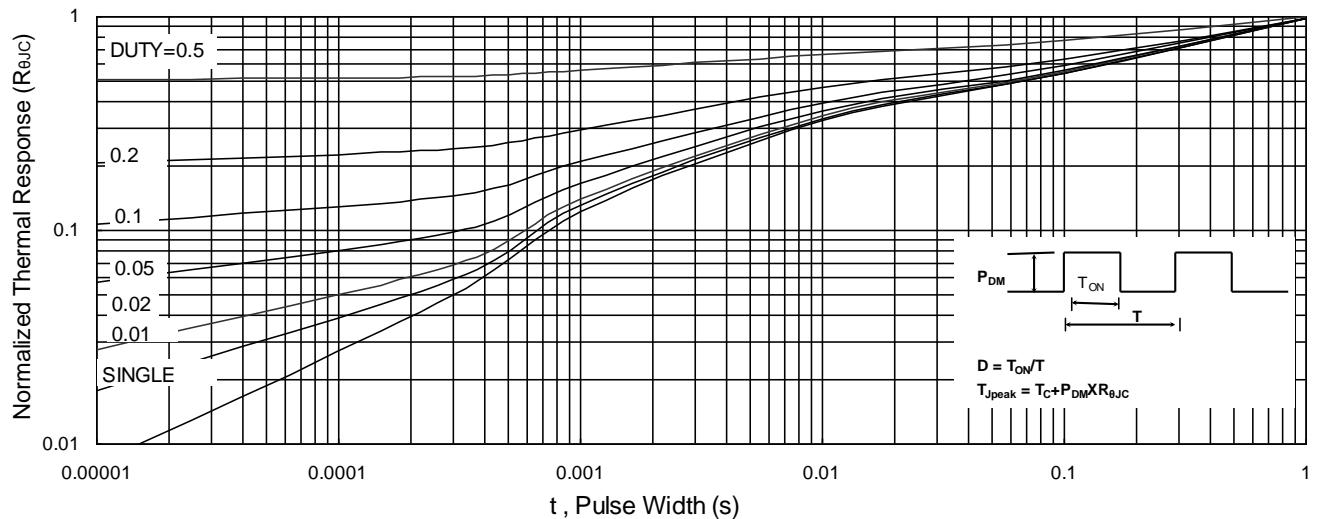
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



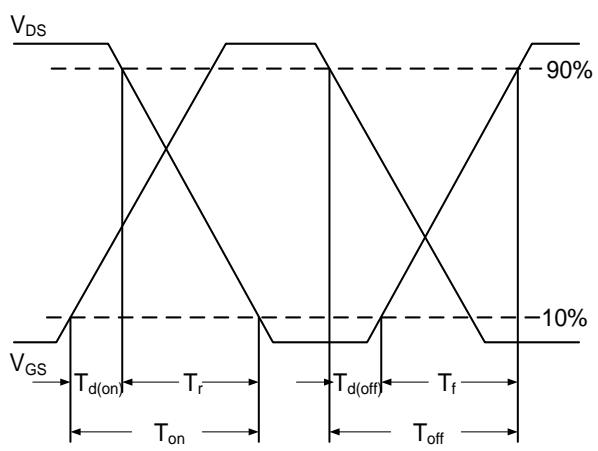
**Fig.7 Capacitance**



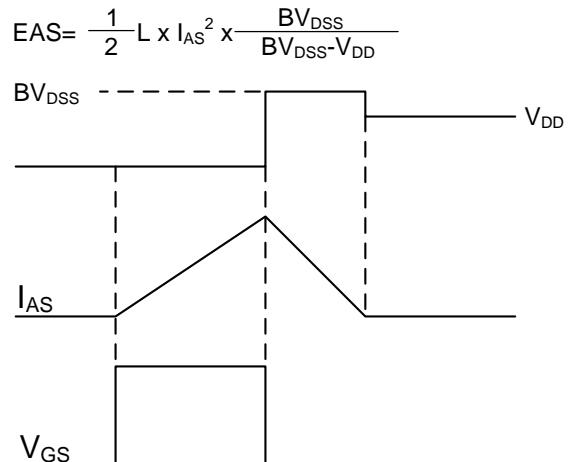
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



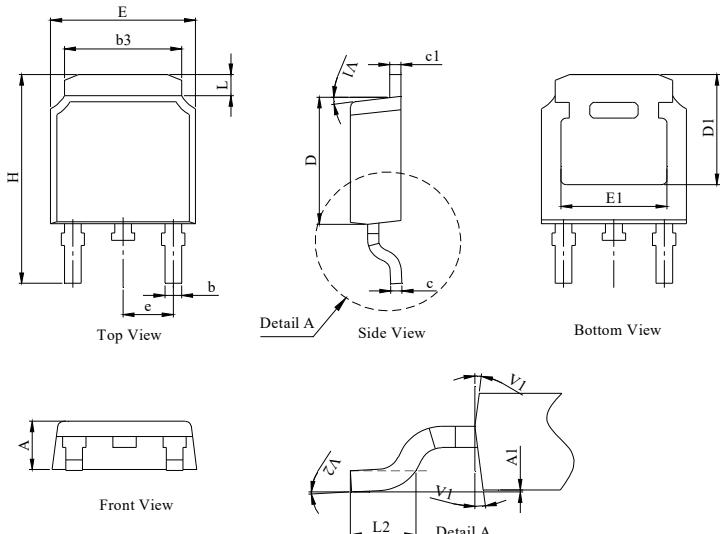
**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

## TO-252 Package Information

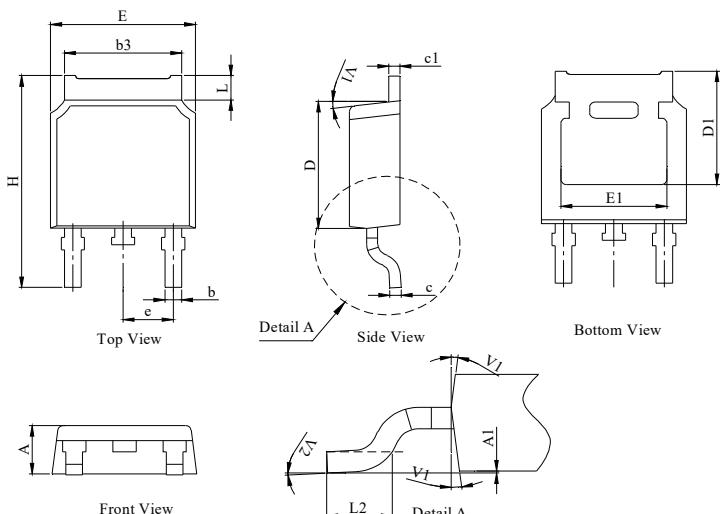
**Package Outline Type-A**



UNIT: mm

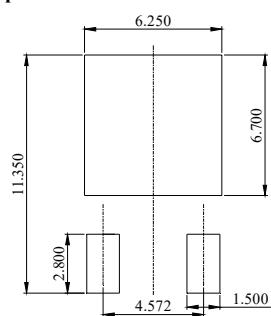
DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.18	2.30	2.39
A1	0	--	0.13
b	0.64	0.76	0.89
c	0.40	0.50	0.61
c1	0.46	0.50	0.58
D	5.97	6.10	6.23
D1	5.05	--	--
E	6.35	6.60	6.73
E1	4.32	--	--
b3	5.21	5.38	5.55
e	2.29 BSC		
H	9.40	10.00	10.40
L	0.89	--	1.27
L2	1.40	--	1.78
V1	7° REF		
V2	0°	--	6°

**Package Outline Type-B**



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.10	2.30	2.40
A1	0	--	0.13
b	0.66	0.76	0.86
b3	5.21	5.38	5.55
c	0.40	0.50	0.60
c1	0.44	0.50	0.58
D	5.90	6.10	6.30
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.29 BSC		
H	9.50	10.00	10.70
L	1.09	--	1.21
L2	1.35	--	1.65
V1	7° REF		
V2	0°	--	6°

**Recommended Soldering Footprint**



## Package Information:

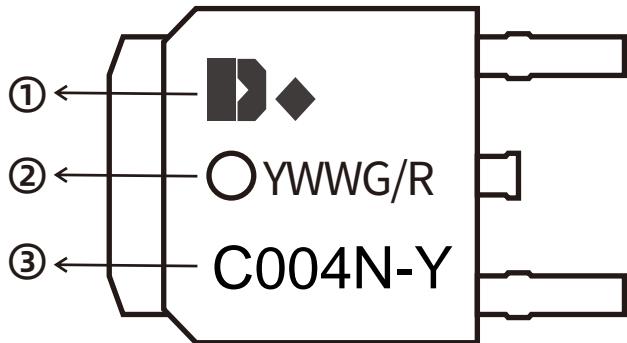
①. Doingter LOGO

②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)



③. Part NO.

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