

## Description:

This N+P Channel MOSFET uses advanced trench technology and

design to provide excellent  $R_{DS(on)}$  with low gate charge.

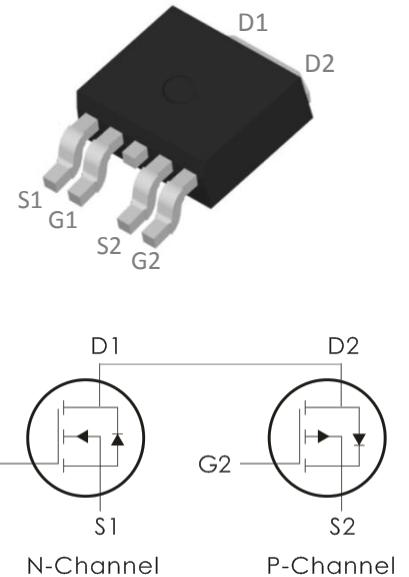
It can be used in a wide variety of applications.

## Features:

N-Channel:  $V_{DS}=40V, I_D=28A, R_{DS(ON)}<13m\Omega @ V_{GS}=10V$

P-Channel:  $V_{DS}=-40V, I_D=-35A, R_{DS(ON)}<18m\Omega @ V_{GS}=-10V$

- 1) Low gate charge.
- 2) Green device available.
- 3) Advanced high cell density trench technology for ultra low  $R_{DS(ON)}$ .
- 4) Excellent package for good heat dissipation.



## Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOD629C	D629C	TO- 252-4	2500 pcs/Reel

## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	N-Channel	P-Channel	Units
$V_{DS}$	Drain-Source Voltage	40	-40	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_c=25^\circ C$	28	-35	A
	Continuous Drain Current- $T_c=100^\circ C$	22	-25	
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	45	-52	A
$E_{AS}$	Single pulse avalanche energy <sup>2</sup>	28	70	mJ
$P_D$	Power Dissipation - $T_c=25^\circ C$	40	45	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150		°C

## Thermal Characteristics:

Symbol	Parameter	N-CH	P-CH	Units
$R_{\theta JC}$	Thermal Resistance,Junction to Cast	3.1	2.78	°C/W

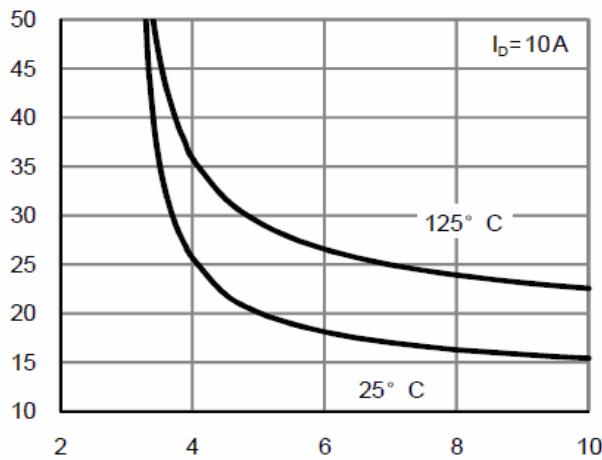
**N-Channel Electrical Characteristics:** ( $T_c=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	40	---	---	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=40\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{\text{GS}(\text{th})}$	Gate-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	1.5	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance <sup>3</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	10	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	---	13	20	
<b>Dynamic Characteristics<sup>4</sup></b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1396	---	$\text{pF}$
$C_{\text{oss}}$	Output Capacitance		---	210	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	131	---	
$Q_g$	Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=20\text{V}$ $I_{\text{D}}=10\text{A}$	---	25.2	---	$\text{nC}$
$Q_{\text{gs}}$	Gate-Source Charge		---	4.2	---	
$Q_{\text{gd}}$	Gate-Drain Charge		---	3.6	---	
<b>Switching Characteristics<sup>4</sup></b>						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=20\text{V}, R_{\text{L}}=2 \Omega$ , $R_{\text{REN}}=3 \Omega, V_{\text{GS}}=10\text{V}$	---	4.2	---	ns
$t_r$	Rise Time		---	12	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	18.9	---	ns
$t_f$	Fall Time		---	5.8	---	ns
<b>Drain-Source Diode Characteristics</b>						
$I_s$	Continuous Drain to Source Diode	$V_G=V_D=0\text{V}$	---	----	28	A
$I_{\text{SM}}$	Pulsed Drain to Source Diode	$V_G=V_D=0\text{V}$	---	---	45	A
$V_{\text{SD}}$	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=10\text{A}$	---	0.8	1.2	V

**Notes:**

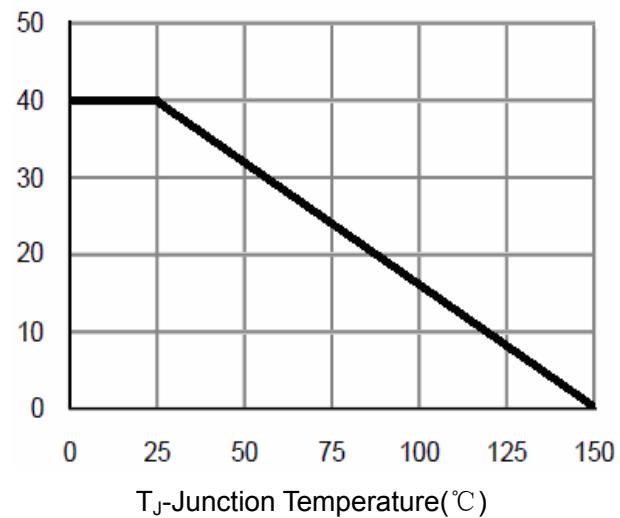
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition :  $T_J=25^\circ\text{C}, V_{\text{DD}}=20\text{V}, V_{\text{G}}=10\text{V}, L=0.5\text{mH}$
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

N-Typical Characteristics: (T<sub>C</sub>=25°C unless otherwise noted)



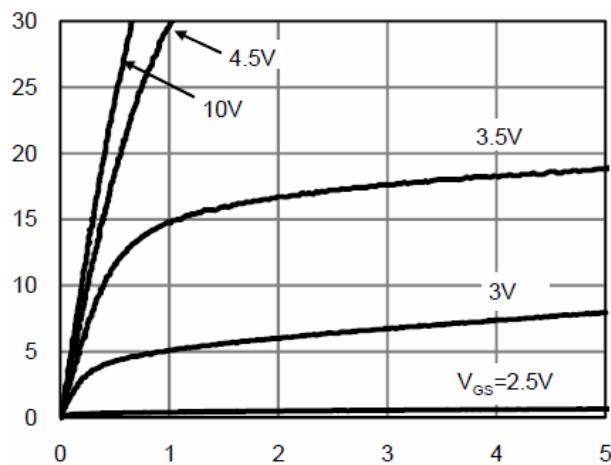
V<sub>gs</sub> Gate-Source Voltage (V)

**Figure 1 Rdson vs Vgs**



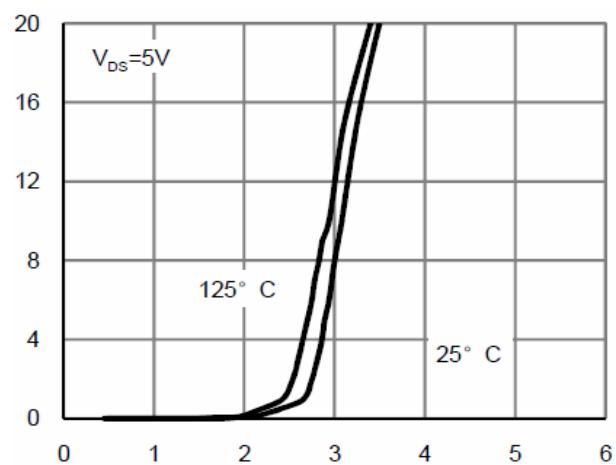
T<sub>j</sub>-Junction Temperature(°C)

**Figure 2 Power Dissipation**



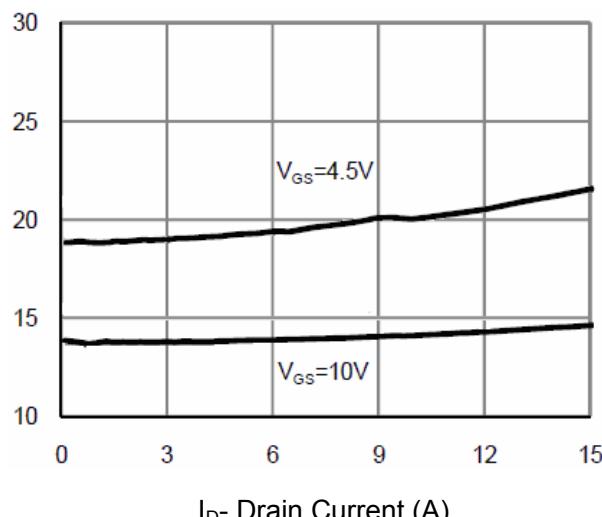
V<sub>ds</sub> Drain-Source Voltage (V)

**Figure 3 Output Characteristics**



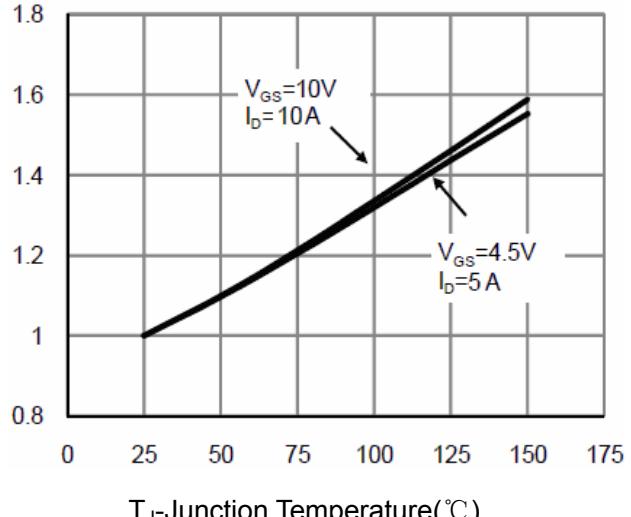
V<sub>gs</sub> Gate-Source Voltage (V)

**Figure 4 Transfer Characteristics**



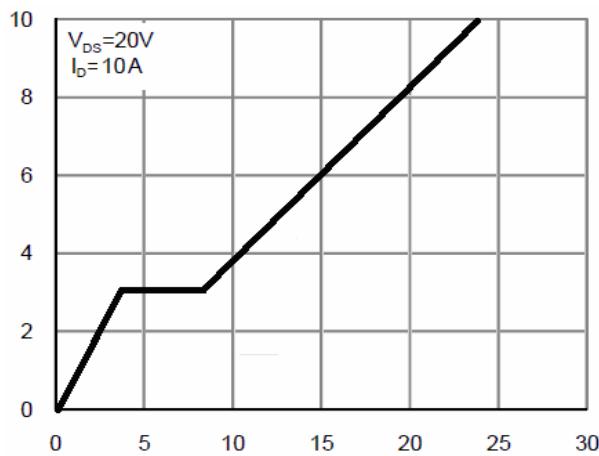
I<sub>D</sub>- Drain Current (A)

**Figure 5 Drain-Source On-Resistance**



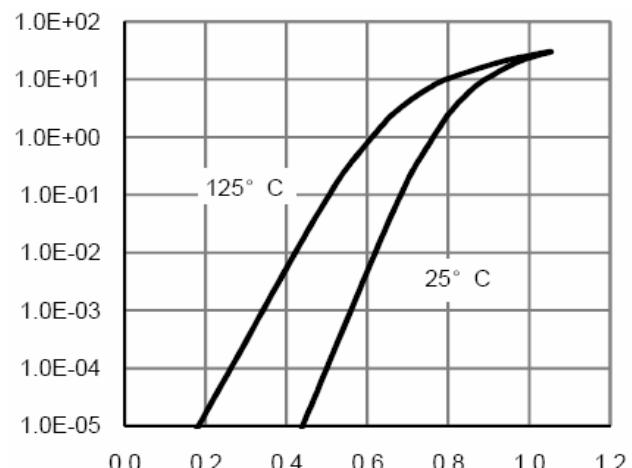
T<sub>j</sub>-Junction Temperature(°C)

**Figure 6 Drain-Source On-Resistance**



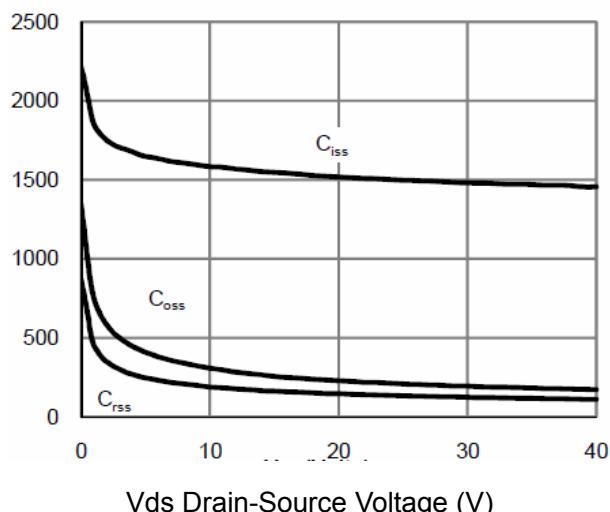
Qg Gate Charge (nC)

**Figure 7 Gate Charge**

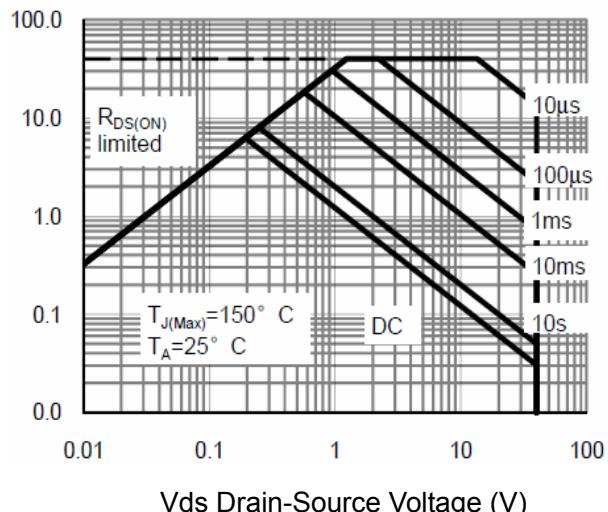


Vds Drain-Source Voltage (V)

**Figure 8 Source- Drain Diode Forward**



**Figure 9 Capacitance vs Vds**



**Figure 10 Safe Operation Area**

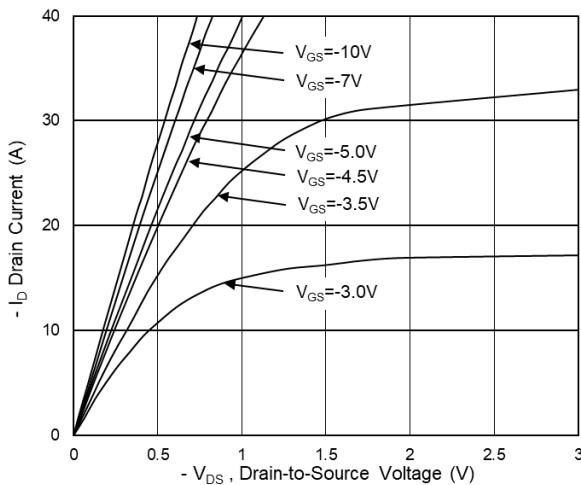
**P-Channel Electrical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
<b><math>\text{BV}_{\text{DSS}}</math></b>	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	-40	---	---	V
<b><math>I_{\text{DS}}^{\text{SS}}</math></b>	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-32\text{V}$	---	---	-1	$\mu\text{A}$
<b><math>I_{\text{GSS}}</math></b>	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics</b>						
<b><math>V_{\text{GS}(\text{th})}</math></b>	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	-1	-1.6	-2.2	V
<b><math>R_{\text{DS}(\text{ON})}</math></b>	Drain-Source On Resistance <sup>3</sup>	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-8\text{A}$	---	14	18	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-6\text{A}$	---	20	24	
<b>Dynamic Characteristics</b>						
<b><math>C_{\text{iss}}</math></b>	Input Capacitance	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1430	---	pF
<b><math>C_{\text{oss}}</math></b>	Output Capacitance		---	150	---	
<b><math>C_{\text{rss}}</math></b>	Reverse Transfer Capacitance		---	135	---	
<b>Switching Characteristics</b>						
<b><math>t_{\text{d(on)}}</math></b>	Turn-On Delay Time	$V_{\text{DD}}= -20\text{V}, I_{\text{D}}=-1\text{A}$	---	13.6	---	ns
<b><math>t_r</math></b>	Rise Time		---	25.2	---	ns
<b><math>t_{\text{d(off)}}</math></b>	Turn-Off Delay Time		---	29.4	---	ns
<b><math>t_f</math></b>	Fall Time		---	24.1	---	ns
<b><math>Q_g</math></b>	Total Gate Charge	$V_{\text{GS}}=-4.5\text{V}, V_{\text{DS}}=-20\text{V}, I_{\text{D}}=-8\text{A}$	---	20.4	---	nC
<b><math>Q_{\text{gs}}</math></b>	Gate-Source Charge		---	6.4	---	nC
<b><math>Q_{\text{gd}}</math></b>	Gate-Drain "Miller" Charge		---	6.9	---	nC
<b>Drain-Source Diode Characteristics</b>						
<b><math>I_s</math></b>	Continuous Drain to Source Diode	$V_D=V_G=0\text{V}$	---	---	-35	A
<b><math>I_{\text{SM}}</math></b>	Pulsed Drain to Source Diode		---	---	-52	---
<b><math>V_{\text{SD}}</math></b>	Source-Drain Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=-1\text{A}$	---	---	-1.2	V

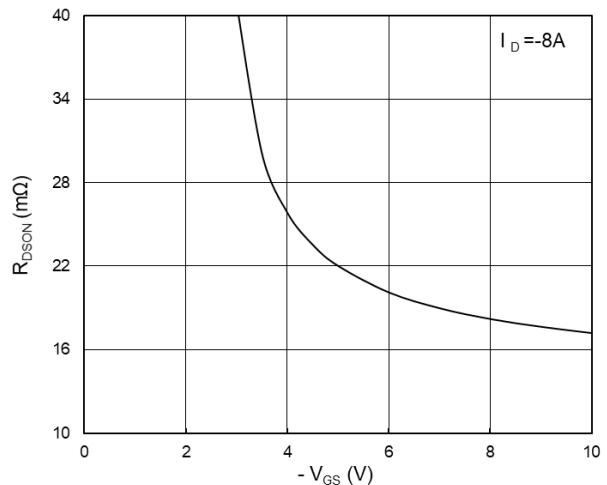
**Notes:**

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{DD}=-20\text{V}$ ,  $V_G=10\text{V}$ ,  $L=0.5\text{mH}$
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 0.5\%$

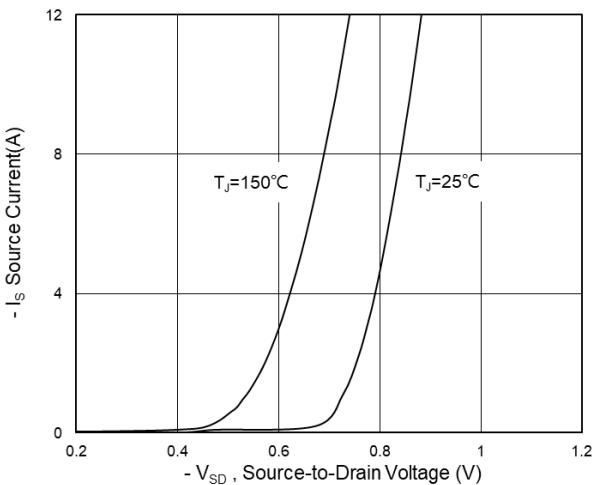
**P-Typical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)



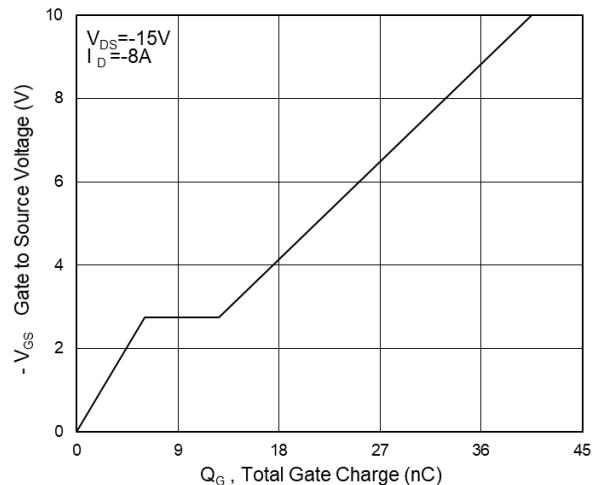
**Fig.1 Typical Output Characteristics**



**Fig.2 On-Resistance vs G-S Voltage**



**Fig.3 Source Drain Forward Characteristics**



**Fig.4 Gate-Charge Characteristics**

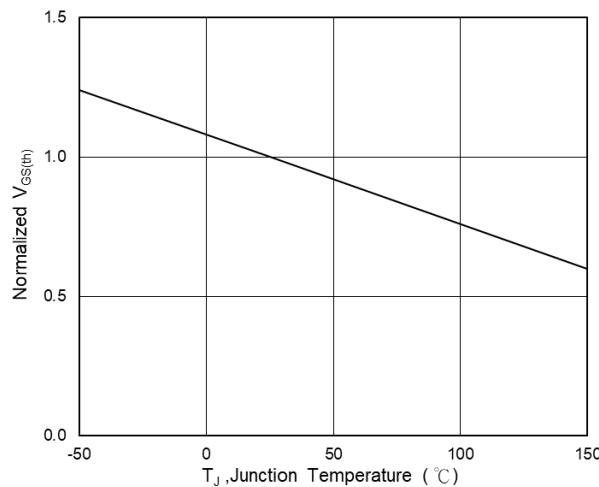
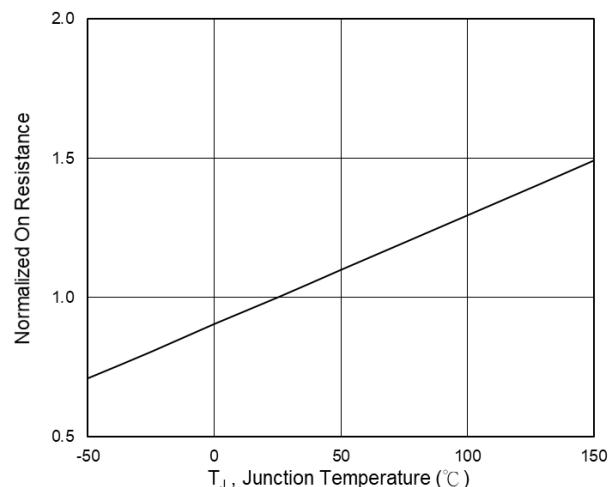
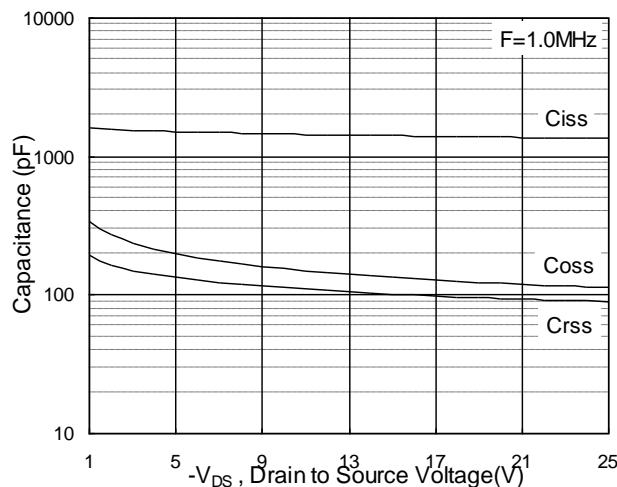
Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$ Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$ 

Fig.7 Capacitance

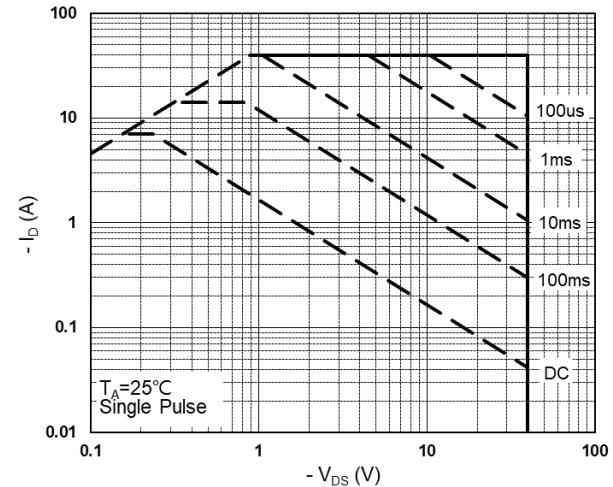


Fig.8 Safe Operating Area

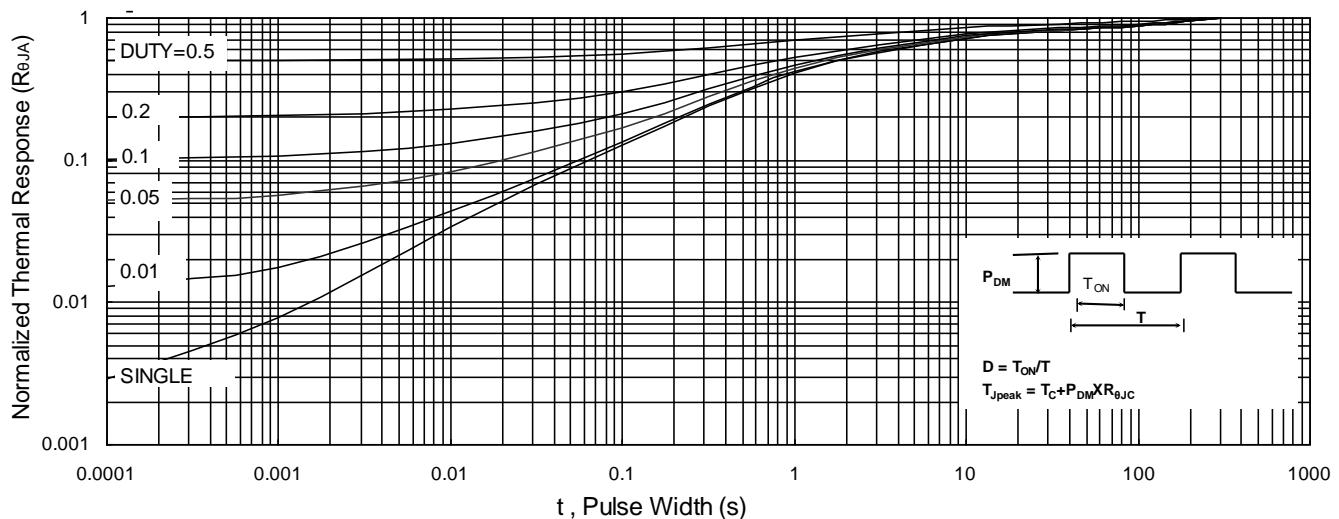
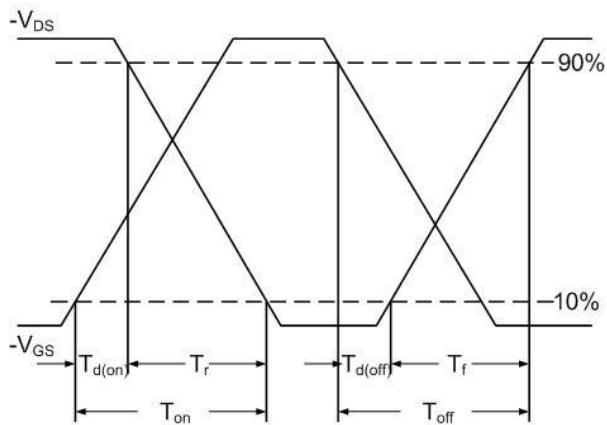
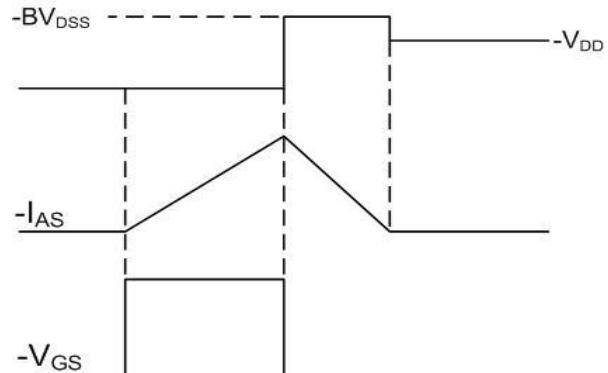


Fig.9 Normalized Maximum Transient Thermal Impedance



**Fig.10 Switching Time Waveform**

$$EAS = \frac{1}{2} L \times (-I_{AS})^2 \times \frac{-BV_{DSS}}{-BV_{DSS} - (-V_{DD})}$$

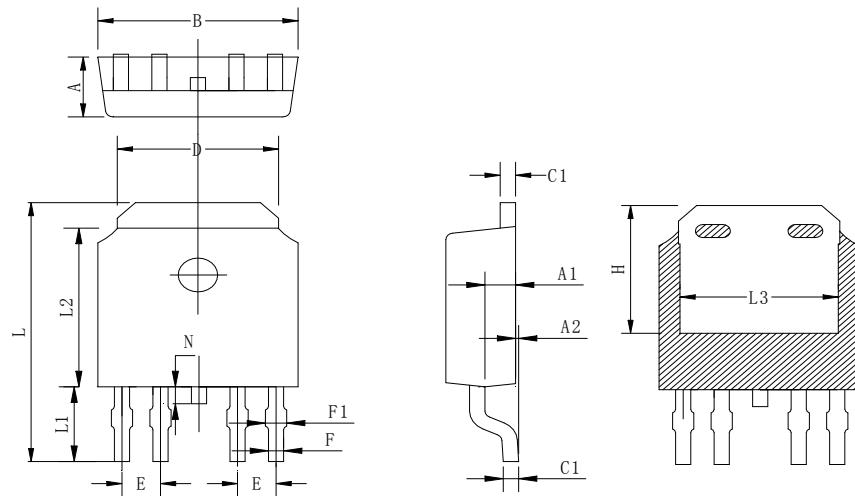


**Fig.11 Unclamped Inductive Switching Waveform**



## TO-252-4 Package Outline Data

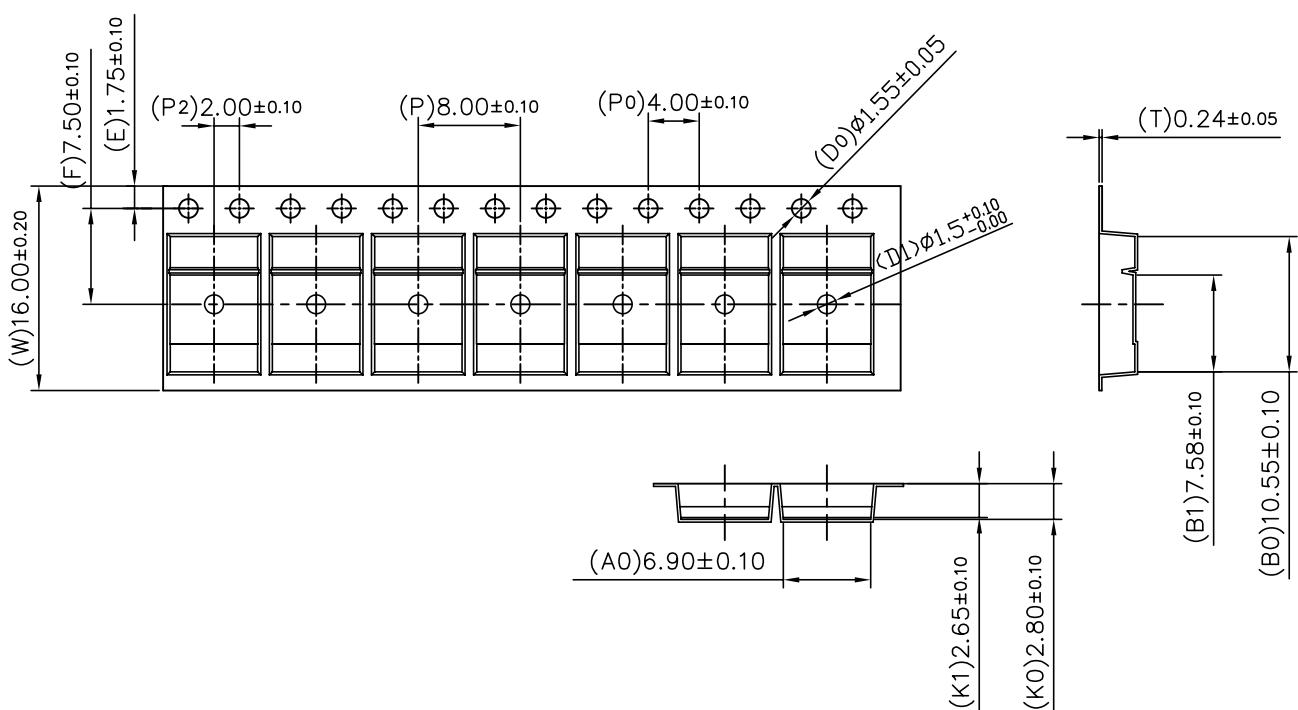
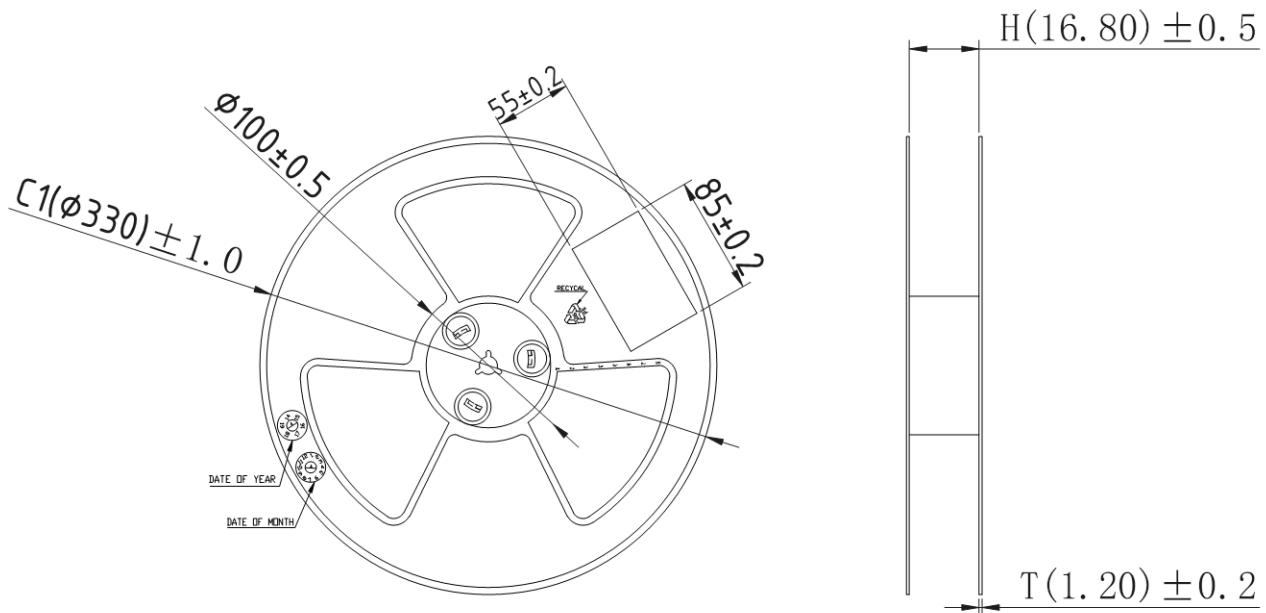
UNIT: mm



Symbol	Min	Typ	Max
A	2.20	2.30	2.40
A1	0.91	1.01	1.11
A2	0.05	0.15	0.25
B	6.45	6.60	6.75
C	0.45	0.50	0.58
C1	0.45	0.50	0.58
D	5.12	5.32	5.52
E		1.27 TYP	
F1	0.45	0.60	0.75
F	0.40	0.50	0.60
H	4.70	4.90	5.10
L	9.70	10.00	10.20
L1	2.6	2.8	3.0
L2	5.95	6.10	6.25
L3	5.00	5.20	5.40
N	0.45	0.65	0.85

## Tape & Reel Information

Dimensions in mm



## Marking Information:

①: Doingter LOGO

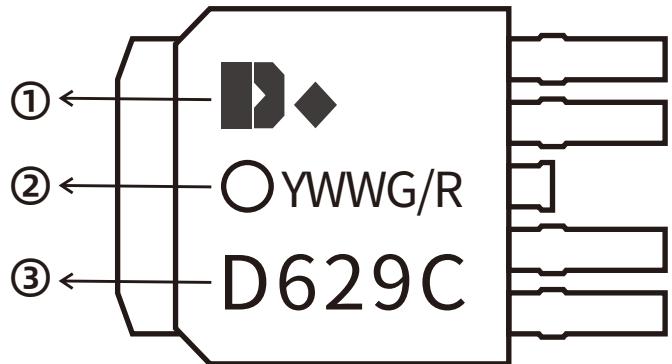
②: Date Code (YWWG / R)

Y: Year Code , last digit of the year

WW : Week Code (01-53)

G/R: G(Green) /R(Lead Free)

③: Part NO.



## Previous Version

Version	Date	Subjects (major changes since last revision)
1.0	2024-06-08	Release of final version

## Attention :

- Information furnished in this document is believed to be accurate and reliable. However, Shenzhen Doingter Semiconductor Co.,Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.
- Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Shenzhen Doingter complies with the agreement. Products and information provided in this document have no infringement of patents.
- Shenzhen Doingter assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



Is a registered trademark of Shenzhen Doingter Semiconductor Co., Ltd. Copyright © 2013 Shenzhen Doingter Semiconductor Co., Ltd. Printed All rights reserved.