

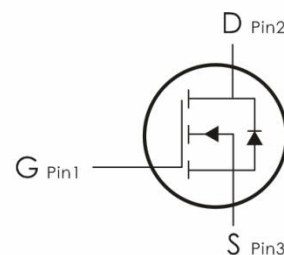
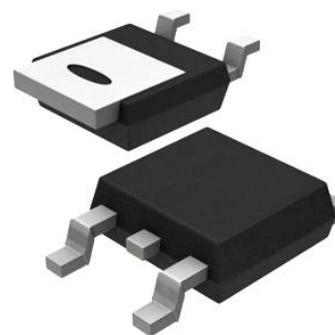
Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=40V, I_D=100A, R_{DS(ON)} < 5.5m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Package Marking and Ordering Information:

Part NO.	Marking	Package	Packing
DOD100N04	100N04	TO- 252	2500 pcs/Reel

Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current- $T_C=25^\circ C$	100	A
	Continuous Drain Current- $T_C=100^\circ C$	50	
I_{DM}	Pulsed Drain Current ¹	320	
P_D	Power Dissipation	142	W
E_{AS}	Single pulse avalanche energy ²	156	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55-+150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.88	$^\circ C/W$

R_{θJA}	Thermal Resistance, Junction to Ambient ³	31	°C/W
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Electrical Characteristics: (T_C=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	40	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =40V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1.3	1.9	2.5	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =30A	---	4.2	5.5	m Ω
		V _{GS} =4.5V, I _D =20A	---	5.7	7.4	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	3777	---	pF
C _{Oss}	Output Capacitance		---	266	--	
C _{rss}	Reverse Transfer Capacitance		---	223	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =20V, I _D =30A, R _{ENG} =3 Ω ,V _{GS} =10V	---	11	---	ns
t _r	Rise Time		---	28	---	ns
t _{d(off)}	Turn-Off Delay Time		---	59	---	ns
t _f	Fall Time		---	15	---	ns
Q _{gs}	Total Gate Charge	V _{GS} =10V, V _{DS} =20V, I _D =30A	---	14	---	nc
Q _{gd}	Gate-Source Charge		---	15	---	nc
Q _g	Gate-Drain “Miller” Charge		---	72	---	nc
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _{SD} =30A	---	---	1.2	V
I _S	Continuous Drain Current	V _D =V _G =0V	---	---	100	A
I _{SM}	Pulsed Drain Current		---	---	320	A
T _{rr}	Reverse Recovery Time	I _F =20A, T _J =25 °C	---	16	---	ns

Qrr	Reverse Recovery Charge	di/dt=100A/us	---	10	---	nc
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Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. EAS condition: Starting $T_J=25^{\circ}\text{C}$, $V_{DD}=20\text{V}$, $V_G=10\text{V}$, $R_G=25\text{ohm}$, $L=0.5\text{mH}$, $I_{AS}=25\text{A}$
3. $R_{\theta JA}$ is measured with the device mounted on a 1inch² pad of 2oz copper FR4 PCB
4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$.

Typical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

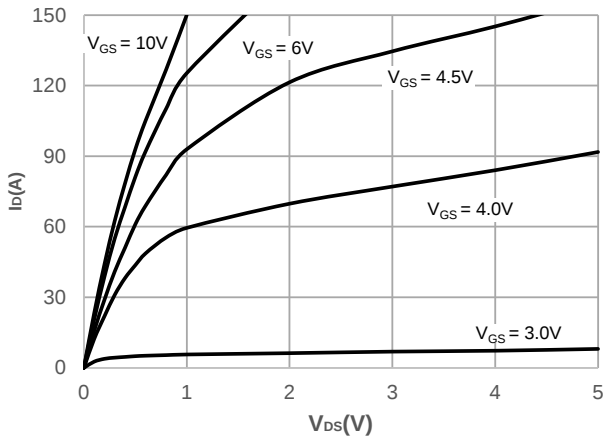


Figure 1: Output Characteristics

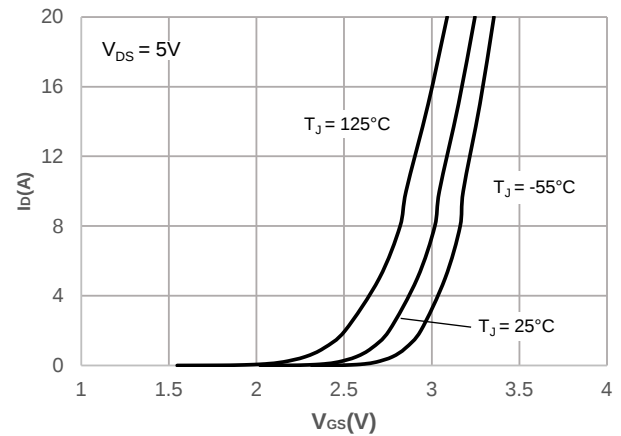


Figure 2: Typical Transfer Characteristics

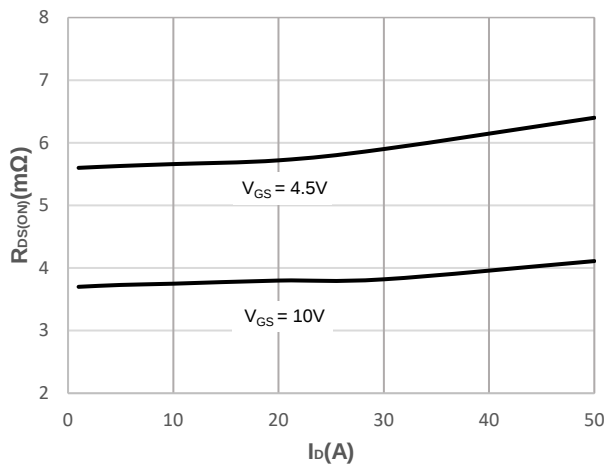


Figure 3: On-resistance vs. Drain Current

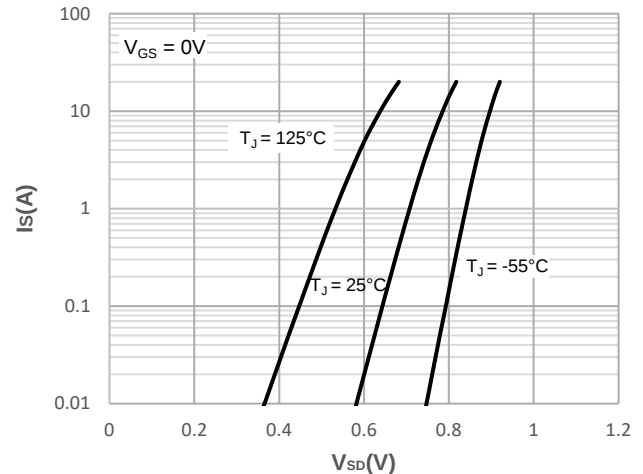


Figure 4: Body Diode Characteristics

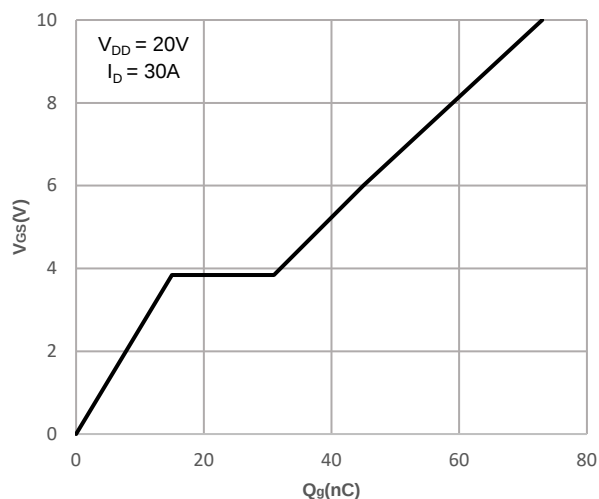


Figure 5: Gate Charge Characteristics

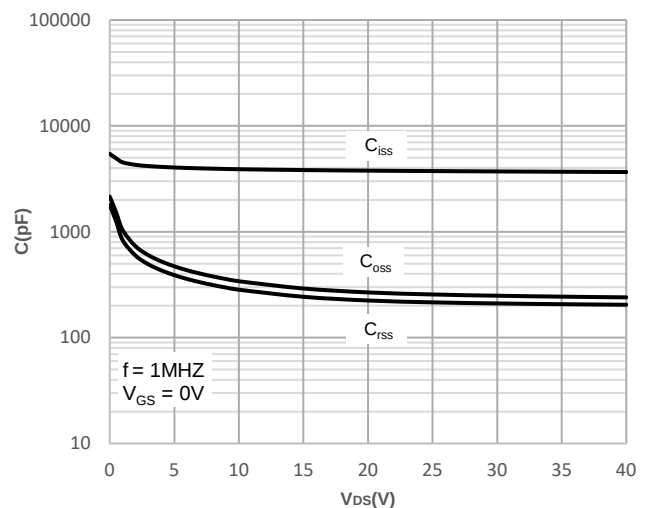


Figure 6: Capacitance Characteristics

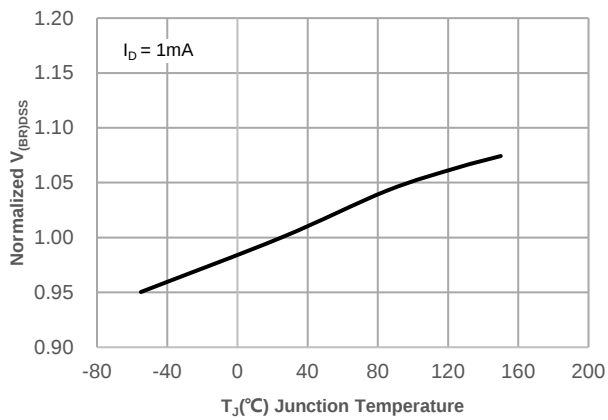


Figure 7: Normalized Breakdown voltage vs. Junction Temperature

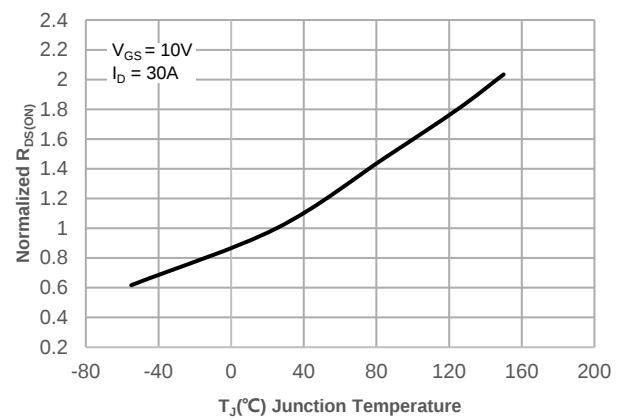


Figure 8: Normalized on Resistance vs. Junction Temperature

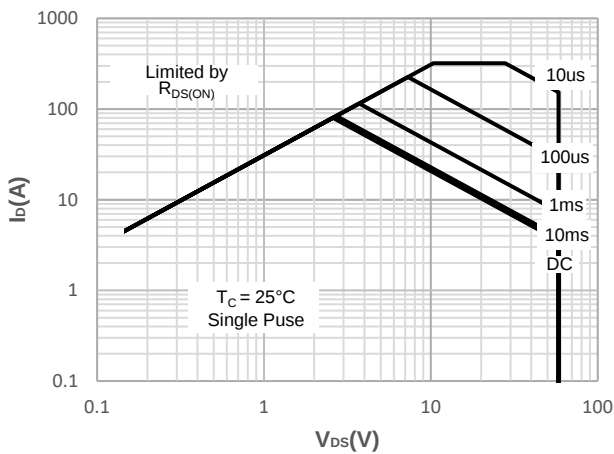


Figure 9: Maximum Safe Operating Area

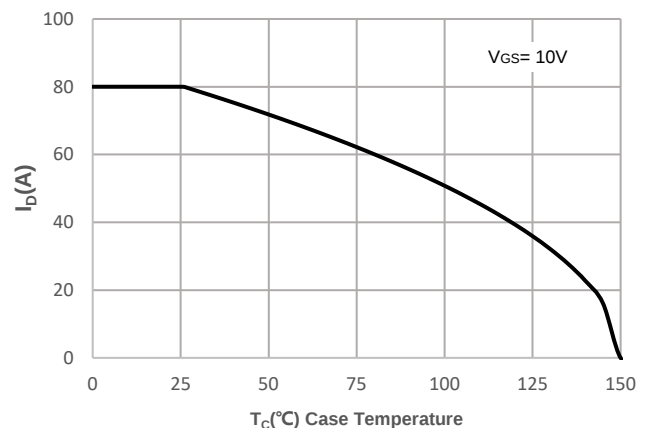


Figure 10: Maximum Continuous Drian Current vs. Case Temperature

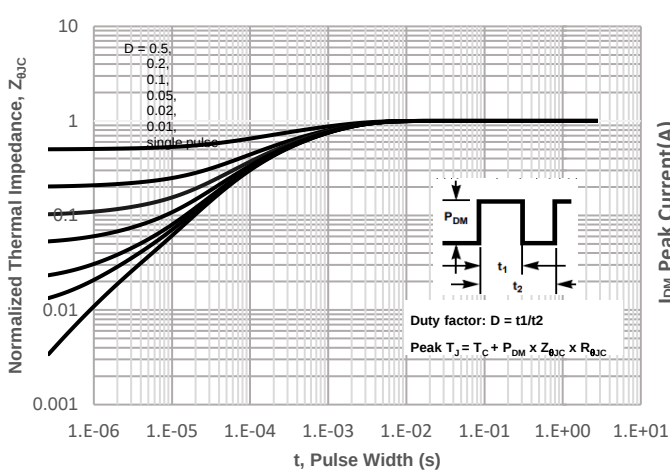


Figure 11: Normalized Maximum Transient Thermal Impedance

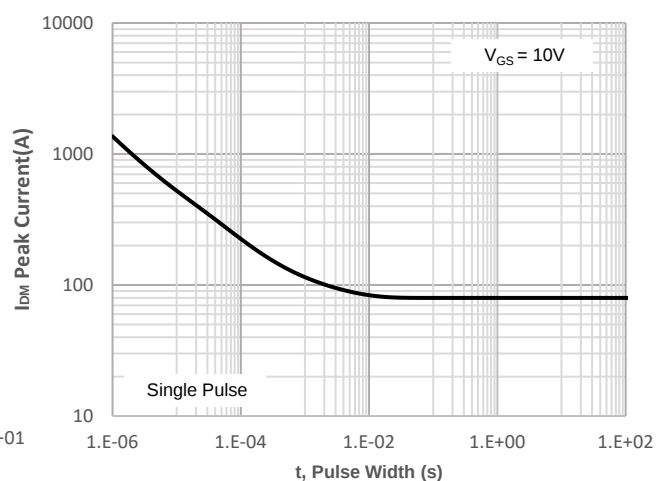
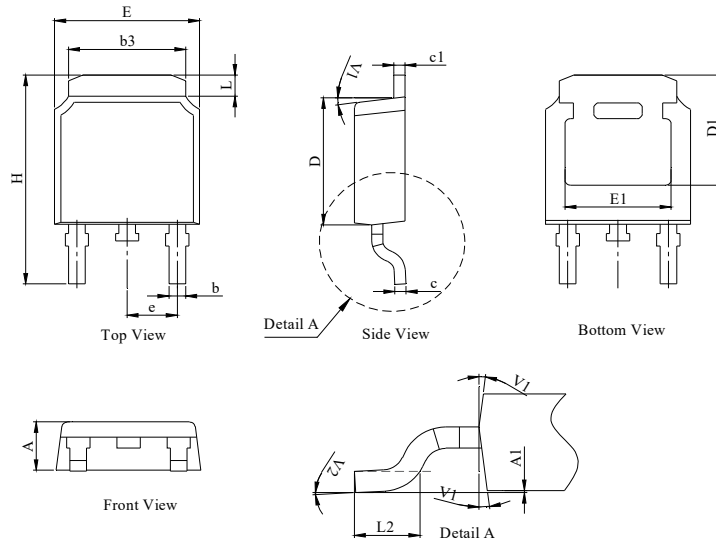


Figure 12: Peak Current Capacity

TO-252 Package Information

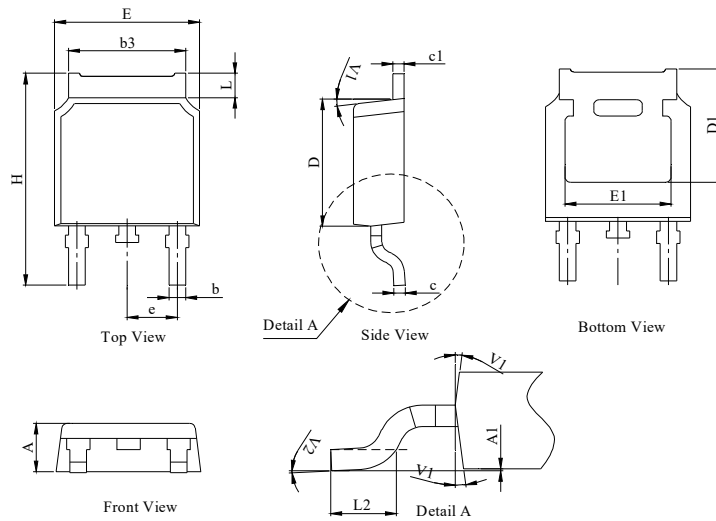
Package Outline Type-A

UNIT: mm



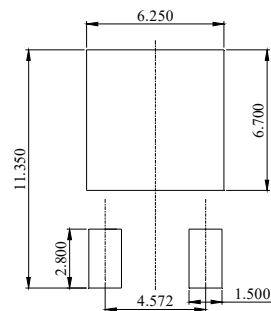
DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.18	2.30	2.39
A1	0	--	0.13
b	0.64	0.76	0.89
c	0.40	0.50	0.61
c1	0.46	0.50	0.58
D	5.97	6.10	6.23
D1	5.05	--	--
E	6.35	6.60	6.73
E1	4.32	--	--
b3	5.21	5.38	5.55
e	2.29 BSC		
H	9.40	10.00	10.40
L	0.89	--	1.27
L2	1.40	--	1.78
V1	7° REF		
V2	0°	--	6°

Package Outline Type-B



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.10	2.30	2.40
A1	0	--	0.13
b	0.66	0.76	0.86
b3	5.21	5.38	5.55
c	0.40	0.50	0.60
c1	0.44	0.50	0.58
D	5.90	6.10	6.30
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.29 BSC		
H	9.50	10.00	10.70
L	1.09	--	1.21
L2	1.35	--	1.65
V1	7° REF		
V2	0°	--	6°

Recommended Soldering Footprint



Marking Information:

①. Doingter LOGO

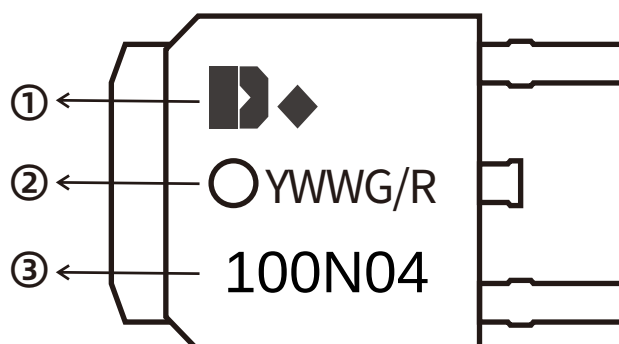
②. Date Code(YWWG / R)

Y : Year Code , last digit of the year

WW : Week Code(01-53)

G/R : G(Green) /R(Lead Free)

③. Part NO.



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