

● General Description

The AGM10N15R combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

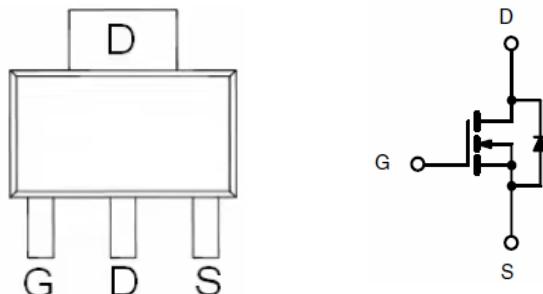
● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDS(ON)	ID
150V	245mΩ	8.2A

SOT-223 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM10N15R	AGM10N15R	SOT-223	----	----	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	150	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	8.2	A
	Drain Current-Continuous(Tc=100°C)	5.1	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	35	A
PD	Maximum Power Dissipation(Tc=25°C)	39	W
	Maximum Power Dissipation(Tc=100°C)	15.5	W
EAS	Avalanche energy (Note 3)	1.25	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady State) ¹	---	65	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	3.2	°C/W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

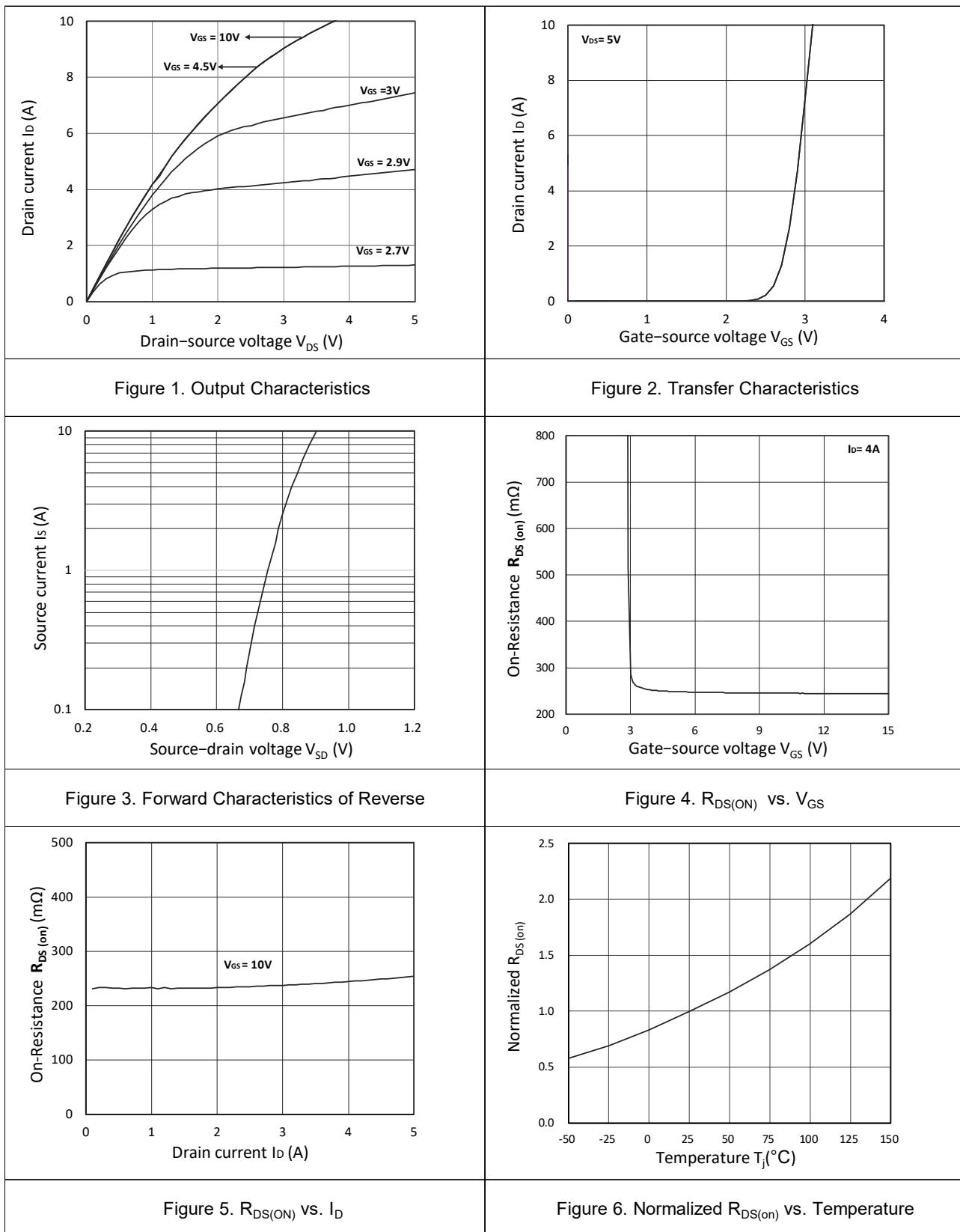
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250µA	150	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=150V, VGS=0V	--	--	1	µA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250µA	1.5	1.75	2.5	V
gFS	Forward Transconductance	VDS=5V, ID=4A	--	25	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=4A	--	245	300	mΩ
		VGS=4.5V, ID=3A	--	--	--	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=75V, VGS=0V, F=1MHZ	--	450	--	pF
Coss	Output Capacitance		--	23	--	pF
Crss	Reverse Transfer Capacitance		--	14	--	pF
Rg	Gate resistance	f=1.0MHz	--	1.5	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V, VDS=75V, ID=1A, RGEN=6Ω	--	8.2	--	nS
tr	Turn-on Rise Time		--	10.2	--	nS
td(off)	Turn-Off Delay Time		--	20.5	--	nS
tf	Turn-Off Fall Time		--	15.3	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=75V, ID=1.5A	--	8.2	--	nC
Qgs	Gate-Source Charge		--	1.5	--	nC
Qgd	Gate-Drain Charge		--	2.2	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	10	A
VSD	Forward on Voltage	VGS=0V, IS=1A	--	--	1.2	V
trr	Reverse Recovery Time	Isd=1A, dl/dt=100A/µs, TJ=25°C	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Typical Characteristics



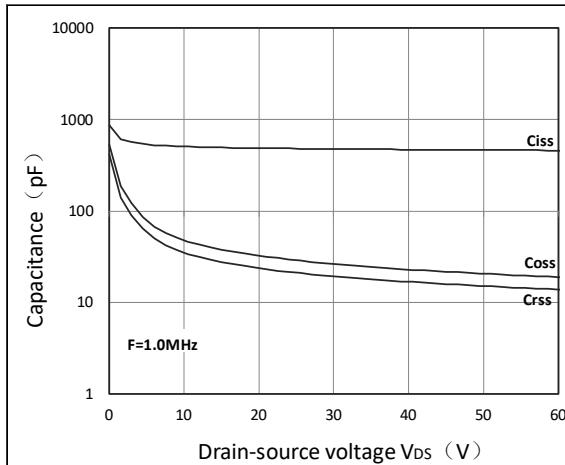


Figure 7. Capacitance Characteristics

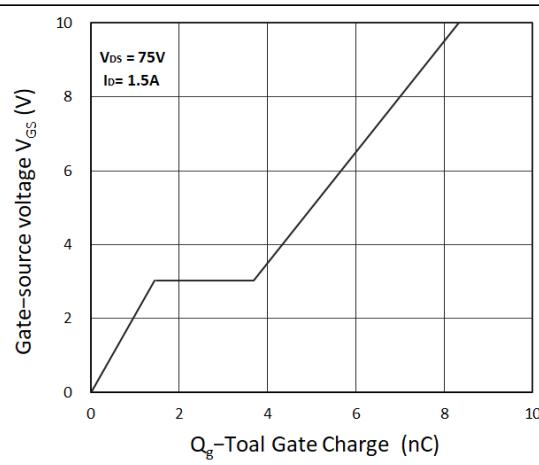


Figure 8. Gate Charge Characteristics

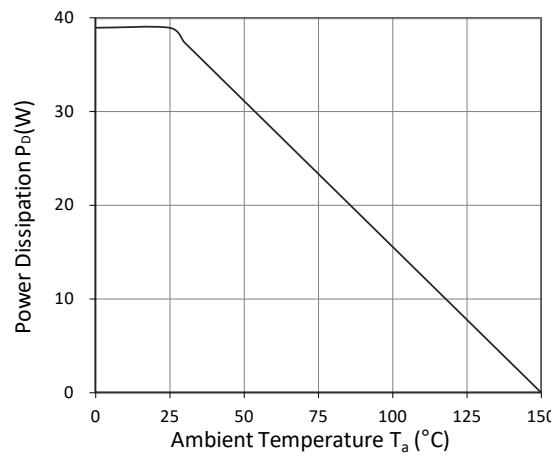


Figure 9. Power Dissipation

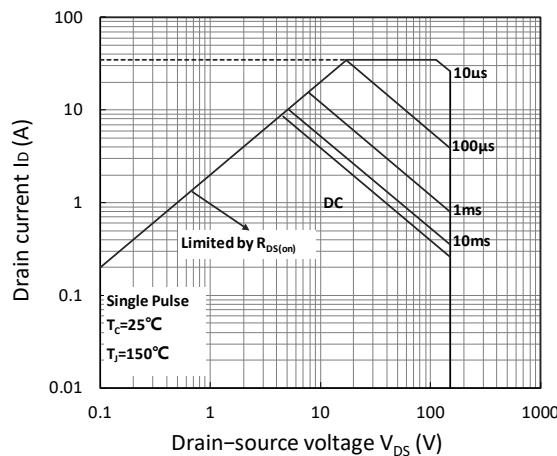


Figure 10. Safe Operating Area

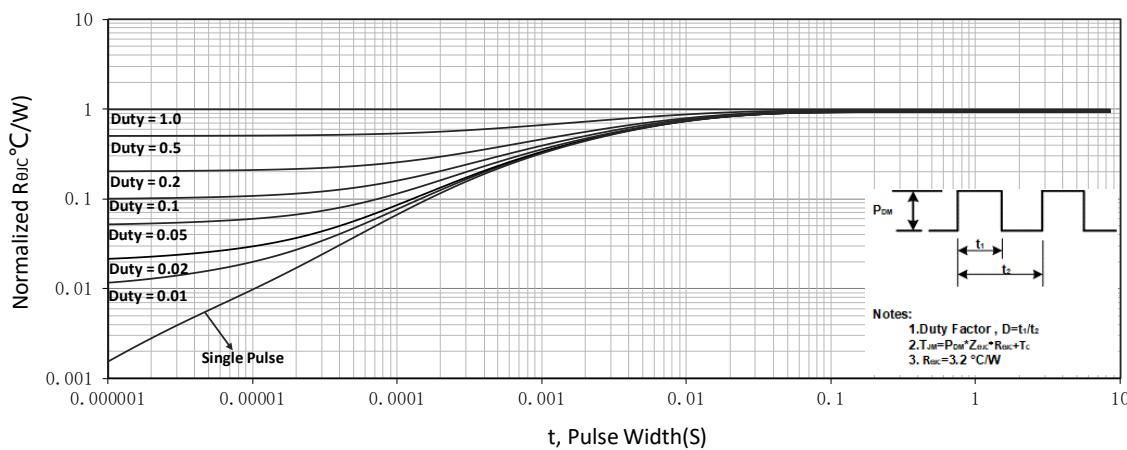
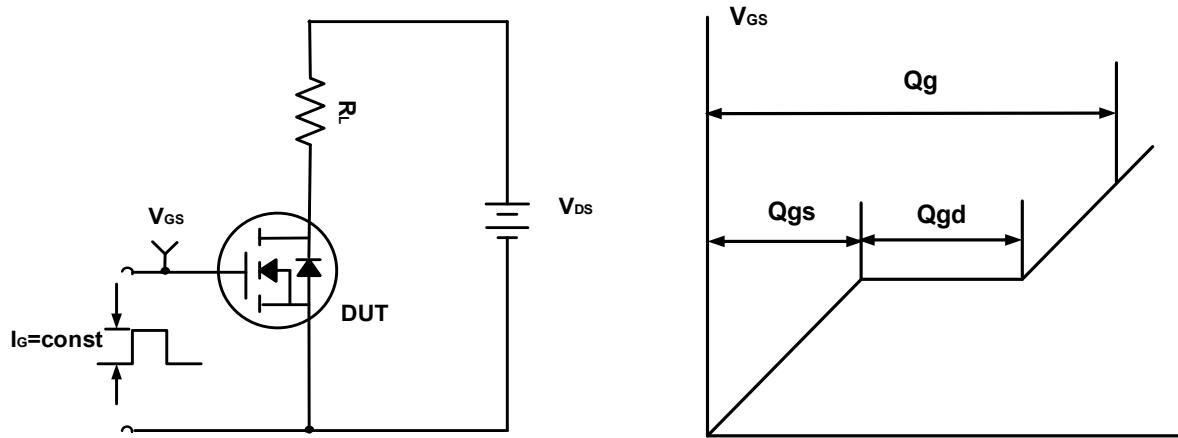
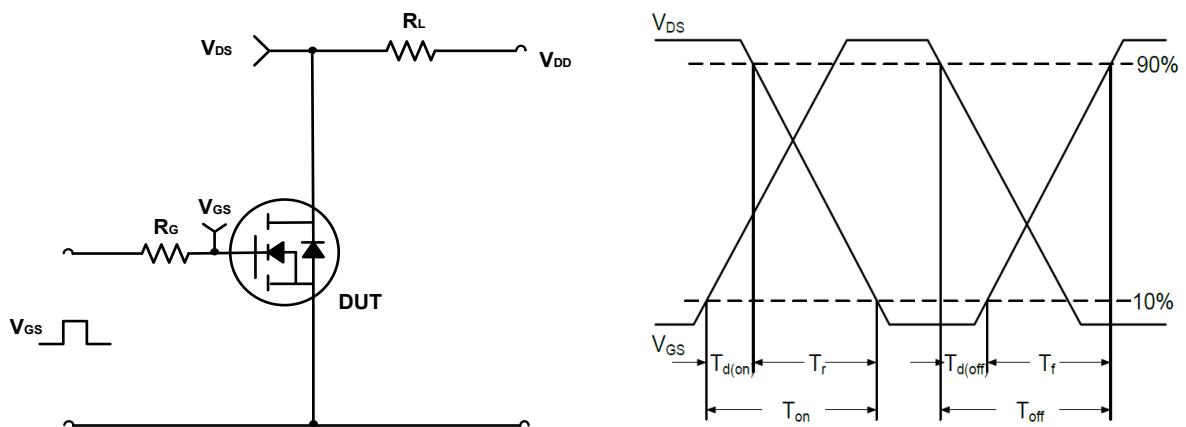
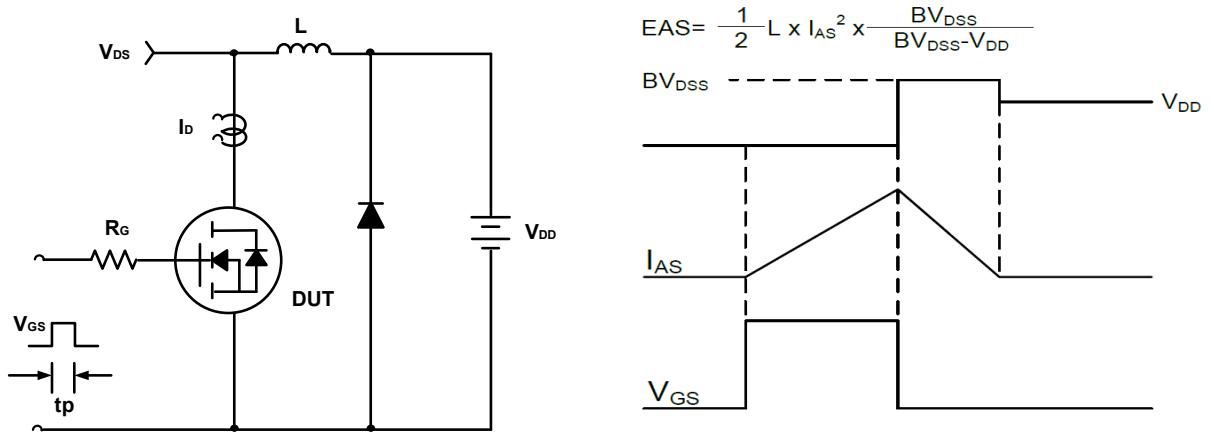
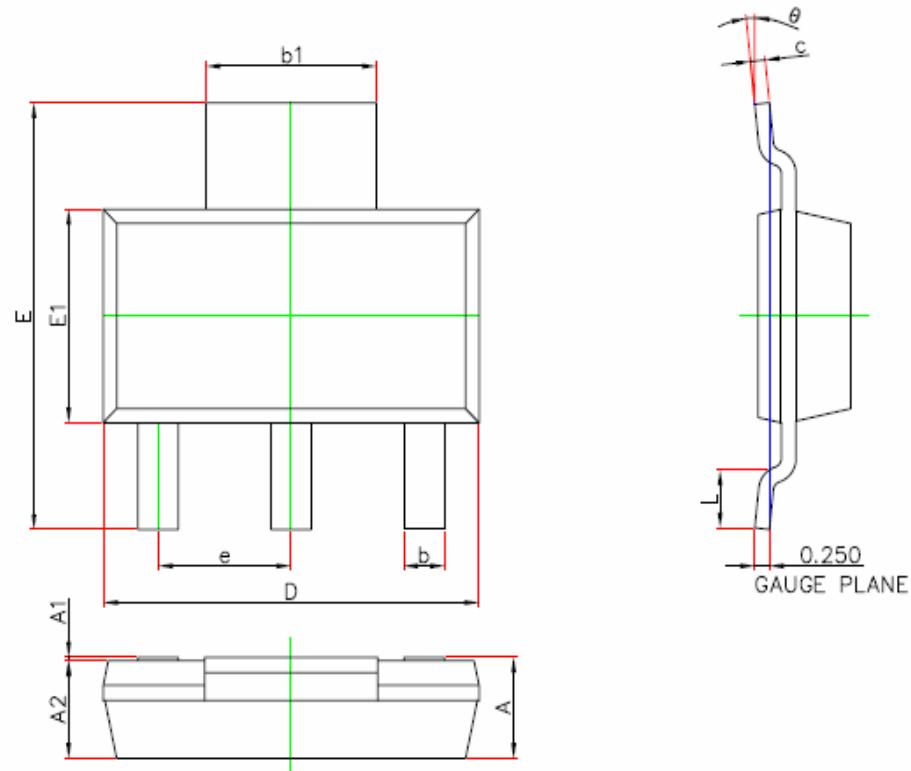


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit**Figure A. Gate Charge Test Circuit & Waveforms****Figure B. Switching Test Circuit & Waveforms****Figure C. Unclamped Inductive Switching Circuit & Waveforms**

SOT-223 Package Dimensions

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	—	1.800	—	0.071
A1	0.020	0.100	0.001	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.840	0.026	0.033
b1	2.900	3.100	0.114	0.122
c	0.230	0.350	0.009	0.014
D	6.300	6.700	0.248	0.264
E	6.700	7.300	0.264	0.287
E1	3.300	3.700	0.130	0.146
e	2.300(BSC)		0.091(BSC)	
L	0.750	—	0.030	—
θ	0°	10°	0°	10°

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