

**N-Channel MOSFET** 

### **General Description**

The WSD28N10DN33 is the highest performance N-Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD28N10DN33 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

#### Features

- 100% UI S + Rg Tested
- Reli able a nd R ugged
- Lead F ree and Green Devices Available

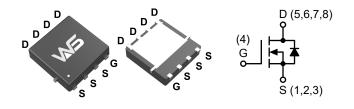
### **Product Summery**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub>
100V	45mΩ	25A

#### Applications

- Power Management in DC/DC Converter.
- POE Protection Switch.

### **DFN3X3-8L Pin Configuration**



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Symbol	Parameter	Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage	100	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	V	
I₀@T₀=25℃	Orationana Daria Oracat	25		
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	15.5	A	
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current, T <sub>C</sub> =25°C	75		
E <sub>AS</sub> <sup>b</sup>	Avalanche Energy, Single pulse, L=0.5mH	16	mJ	
I <sub>AS</sub> <sup>b</sup>	Avalanche Current, Single pulse, L=0.5mH	8	A	
P <sub>D</sub> @T <sub>C</sub> =25℃		25	w	
P <sub>D</sub> @T <sub>C</sub> =100℃	Total Power Dissipation	10		
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150		

### Thermal Data

Symbol	Parameter		Max.	Unit	
R <sub>θJA</sub> <sup>c</sup>	Thermal Resistance Junction-Ambient		80	°C/W	
R <sub>eJC</sub>	Thermal Resistance Junction-Case		5	°C/W	

Note a : Pulse width limited by maximum junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}$ C (initial temperature  $T_j=25^{\circ}$ C).

Note c : Surface Mounted on 1in<sup>2</sup> pad area.



#### **N-Channel MOSFET**

### **Electrical Characteristics** (T<sub>A</sub> =25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	100			V
D	Static Drain-Source On-Resistance <sup>d</sup>	V <sub>GS</sub> =10V, I <sub>DS</sub> =7A		45	54	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =7A		55	70	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250 uA$	1.0	1.7	2.5	V
1	Drain Source Leekage Current	$V_{DS}$ =80V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}\mathrm{C}$			1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =85℃			30	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm20V$ , $V_{DS}$ =0V			±100	nA
R <sub>G</sub>	Gate Resistance	$V_{\text{DS}}$ =0V , $V_{\text{GS}}$ =0V , f=1MHz		1.5		Ω
Qg	Total Gate Charge			7.5	11	
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =50V , $V_{GS}$ =10V , $I_{DS}$ =7A		2.2		nC
$Q_gd$	Gate-Drain Charge			1.6		
T <sub>d(on)</sub>	Turn-On Delay Time			6	11	
Tr	Rise Time	V <sub>DD</sub> =30V, R <sub>L</sub> =30Ω, I <sub>DS</sub> =1A,		9	17	
T <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GEN}$ =10V, $R_{G}$ =6 $\Omega$		12	22	ns
T <sub>f</sub>	Fall Time			14	26	
Ciss	Input Capacitance			2116		
C <sub>oss</sub>	Output Capacitance V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz			117		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			97		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	$V_G=V_D=0V$ , Force Current			25	A
V <sub>SD</sub>	Diode Forward Voltage <sup>d</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =7A		0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	IF=7A.dl/dt=100A/us		30		nS
Q <sub>rr</sub>	Reverse Recovery Charge	$r = r A, u r u = r U U A r \mu S$		40		nC

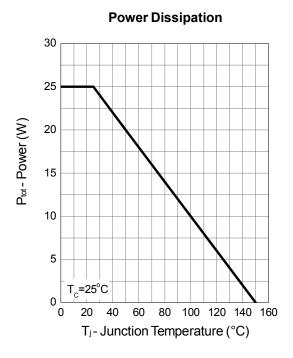
Note d  $\cdot$  Pulse test ; pulse width $\leq$ 300 $\mu$ s, duty cycle $\leq$ 2%.

Note e  $\underline{\cdot}$  Guaranteed by design, not subject to production testing.

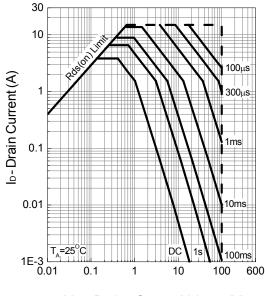


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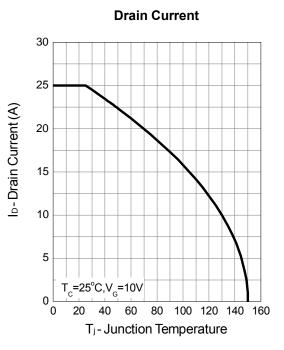
## **Typical Characteristics**



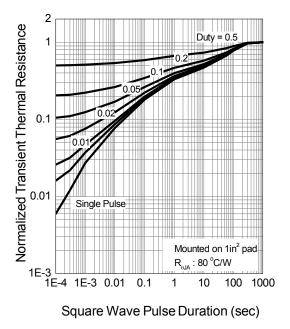
Safe Operation Area



V<sub>DS</sub> - Drain - Source Voltage (V)



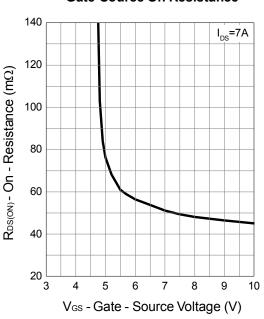
**Thermal Transient Impedance** 





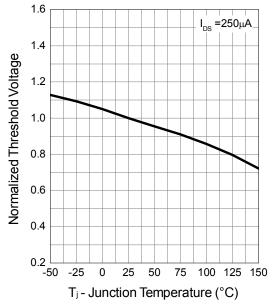
**N-Channel MOSFET** 

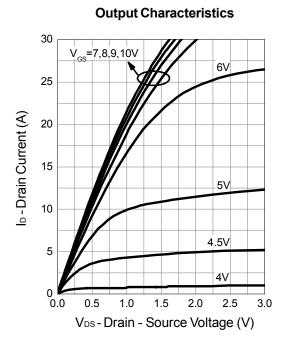
# **Typical Characteristics (Cont.)**



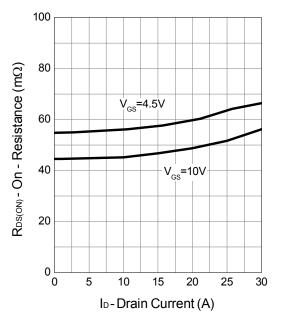
Gate-Source On Resistance

Gate Threshold Voltage





Drain-Source On Resistance

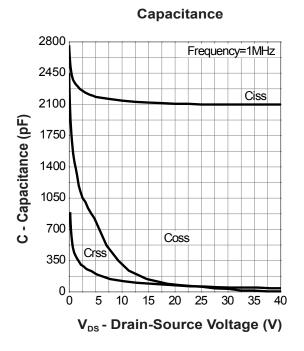


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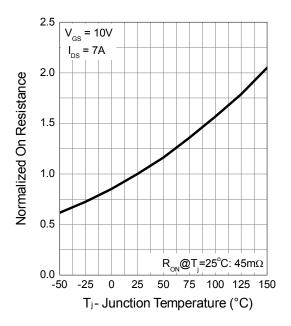


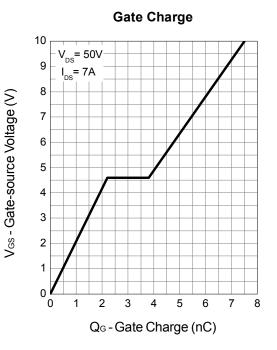
**N-Channel MOSFET** 

## **Typical Characteristics (Cont.)**

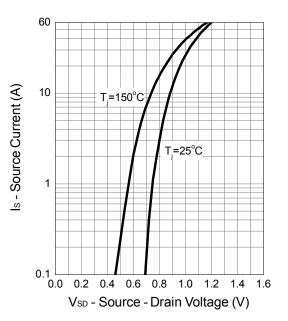


**Drain-Source On Resistance** 





Source-Drain Diode Forward

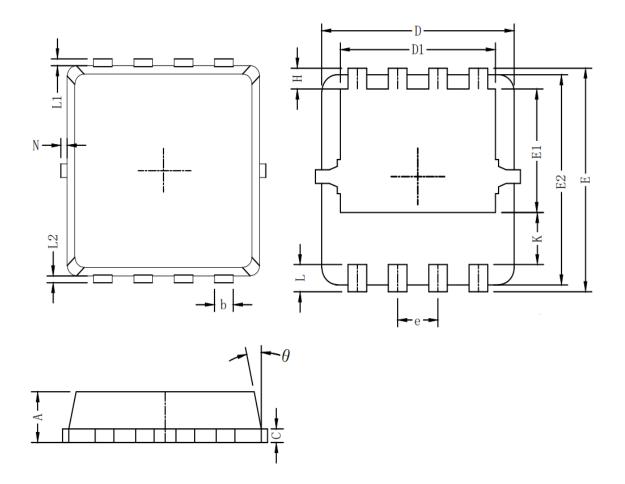


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# **Packaging information**



Symbol	Dim in mm				
	min	typ	max		
А	0.6	0.75	0.9		
b	0.2	0.3	0.4		
С	0.15	0.2	0.25		
D	3	3.1	3.2		
D1	2.3	2.45	2.6		
E	3.15	3.3	3.45		
E1	1.43	1.73	1.93		
E2	2.9	3.05	3.2		
е	0.65BSC				
Н	0.2	0.35	0.5		
К	0.57	0.77	0.87		
L	0.3	0.4	0.5		
L1/L2	0.1REF				
θ	8°	10°	13°		
Ν	0		0.15		



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