

### General Description

The WSD30N10DN56T is the highest performance SGT Dual N-Ch MOSFET with extreme high cell density, which provide excellent R<sub>DS(on)</sub> and gate charge for most of the synchronous buck converter applications.

The WSD46N10DN56 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent C<sub>dv/dt</sub> effect decline
- Green Device Available

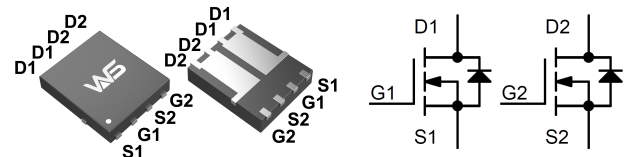
### Product Summary

BVDSS	R <sub>DS(on)</sub>	I <sub>D</sub>
100V	70mΩ	12A

### Applications

- DC-DC Converter.
- Motor Control.

### DFN5X6C-8-EP2 Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	100	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	12	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	7	A
I <sub>DM</sub> <sup>a</sup>	Pulsed Drain Current	48	A
E <sub>AS</sub> <sup>b</sup>	Single Pulse Avalanche Energy	12	mJ
I <sub>AS</sub> <sup>b</sup>	Avalanche Current	7	A
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation	31	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub> <sup>c</sup>	Thermal Resistance Junction-ambient	---	60	°C/W
R <sub>θJC</sub> <sup>c</sup>	Thermal Resistance-Junction to Case	---	4.0	°C/W

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T<sub>J</sub>=25°C).

Note c : Surface Mounted on 1in<sup>2</sup> pad area.

**Electrical Characteristics ( $T_J=25^{\circ}\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=250\mu A$	100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.098	---	V/ $^{\circ}\text{C}$
$R_{DS(ON)}^d$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V$ , $I_D=6A$	---	70	95	m $\Omega$
$R_{DS(ON)}^d$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=4.5V$ , $I_D=4A$	---	85	100	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	1.2	1.5	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5.52	---	mV/ $^{\circ}\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80V$ , $V_{GS}=0V$ , $T_J=25^{\circ}\text{C}$	---	---	1	$\mu A$
		$V_{DS}=80V$ , $V_{GS}=0V$ , $T_J=55^{\circ}\text{C}$	---	---	30	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
$R_g^e$	Gate Resistance	$V_{DS}=0V$ , $V_{GS}=0V$ , $f=1\text{MHz}$	---	2.5	---	$\Omega$
$Q_g^e$	Total Gate Charge (10V)	$V_{DS}=30V$ , $V_{GS}=10V$ , $I_D=5A$	---	16	---	nC
$Q_{gs}^e$	Gate-Source Charge		---	2.8	---	
$Q_{gd}^e$	Gate-Drain Charge		---	3.5	---	
$T_{d(on)}^e$	Turn-On Delay Time	$V_{DD}=30V$ , $V_{GEN}=10V$ , $R_G=6\Omega$ $I_D=1A$ , $R_L=30\Omega$	---	11	---	ns
$T_r^e$	Rise Time		---	7	---	
$T_{d(off)}^e$	Turn-Off Delay Time		---	28	---	
$T_f^e$	Fall Time		---	8	---	
$C_{iss}^e$	Input Capacitance	$V_{DS}=25V$ , $V_{GS}=0V$ , $f=1\text{MHz}$	---	780	---	pF
$C_{oss}^e$	Output Capacitance		---	45	---	
$C_{rss}^e$	Reverse Transfer Capacitance		---	30	---	

**Diode Characteristics**

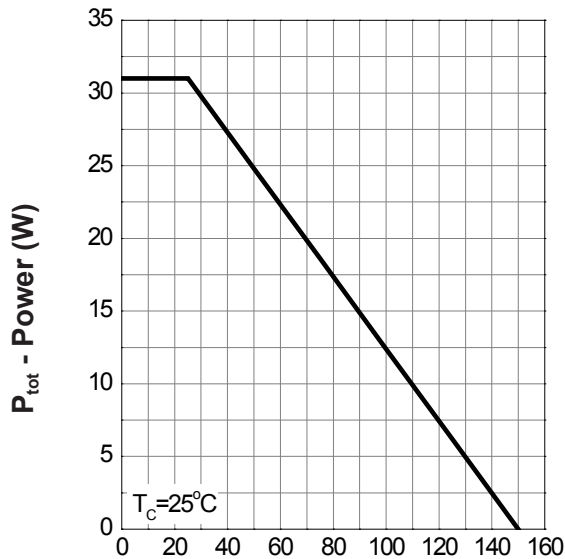
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	6	A
$V_{SD}^d$	Diode Forward Voltage	$V_{GS}=0V$ , $I_S=1A$ , $T_J=25^{\circ}\text{C}$	---	---	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_S=1A$ , $dI/dt=100A/\mu s$	---	30	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	41	---	nC

Note d : Pulse test ; pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .

Note e : Guaranteed by design, not subject to production testing.

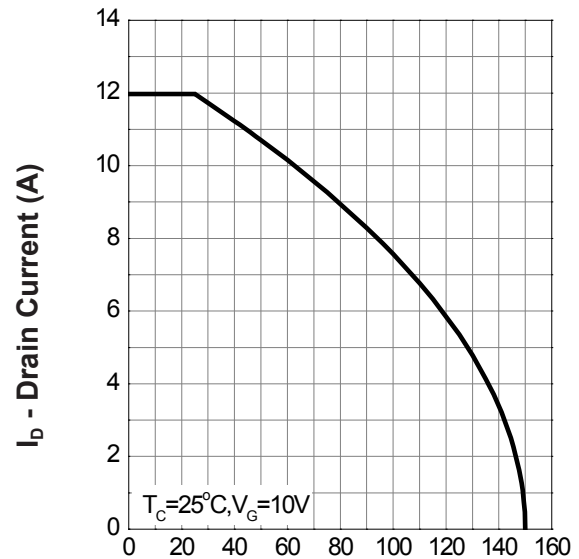
## Typical Operating Characteristics

Power Dissipation



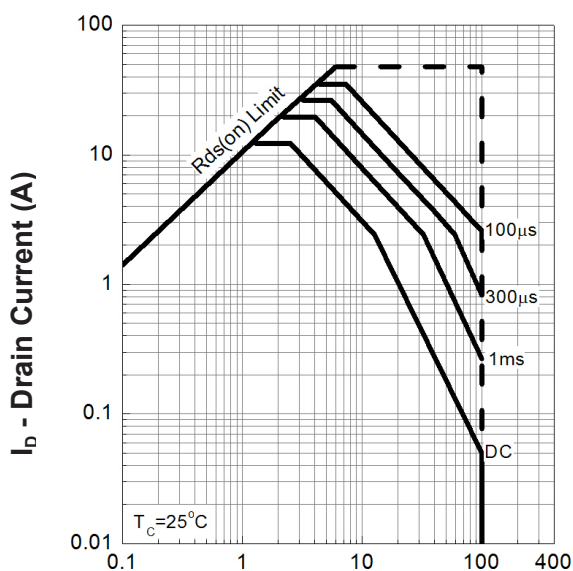
$T_j$  - Junction Temperature (°C)

Drain Current



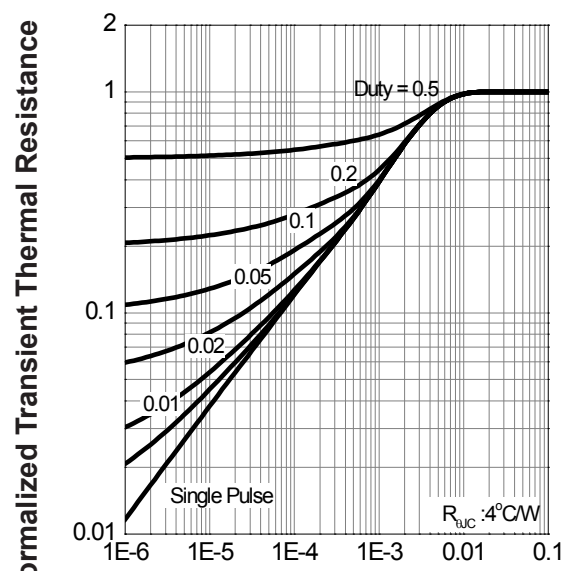
$T_j$  - Junction Temperature (°C)

Safe Operation Area



$V_{DS}$  - Drain - Source Voltage (V)

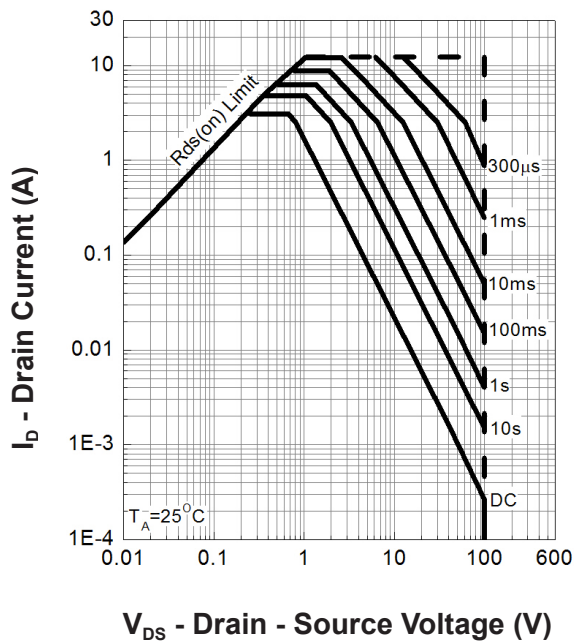
Thermal Transient Impedance



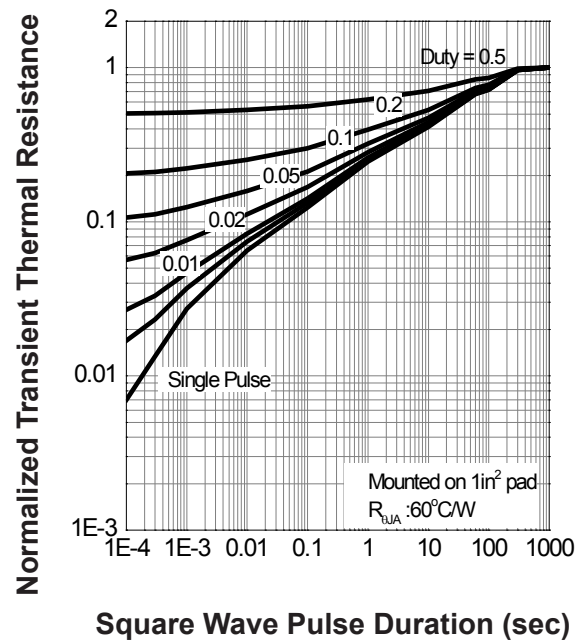
Square Wave Pulse Duration (sec)

## Typical Operating Characteristics

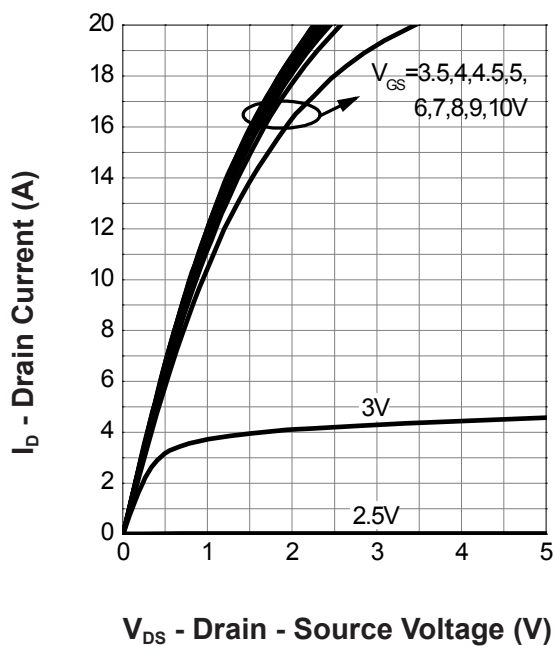
Safe Operation Area



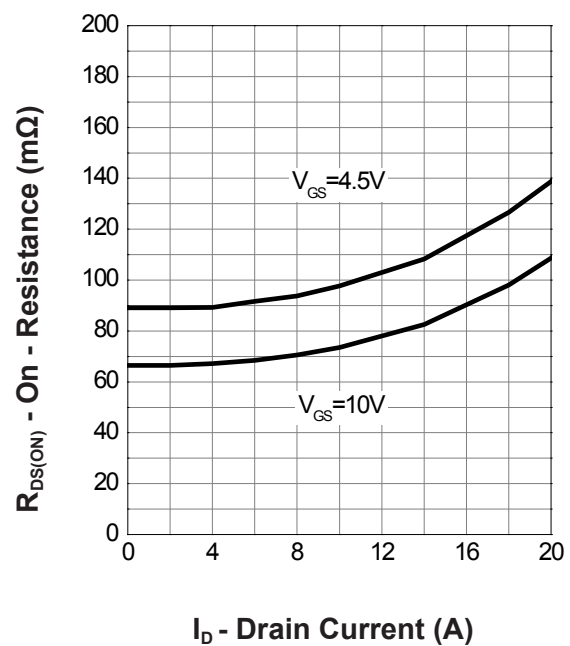
Thermal Transient Impedance



Output Characteristics

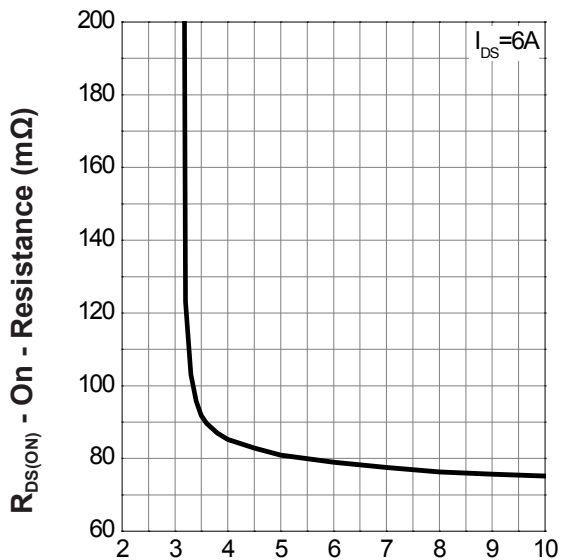


Drain-Source On Resistance



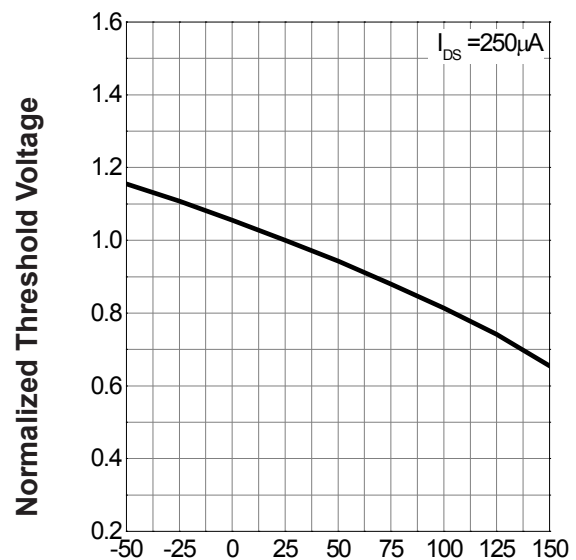
## Typical Operating Characteristics

Gate-Source On Resistance



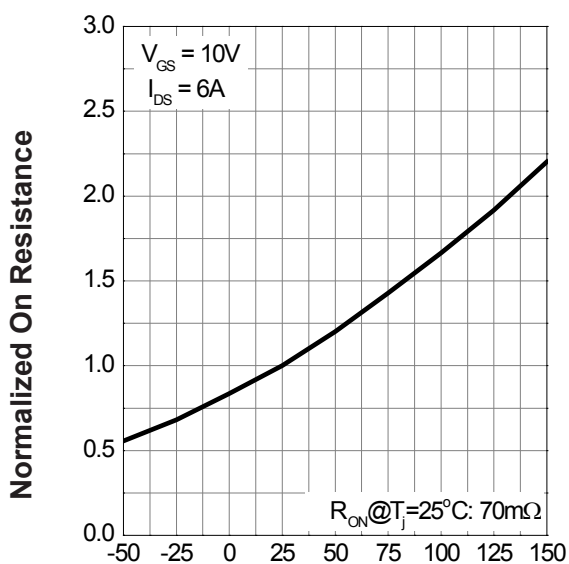
$V_{GS}$  - Gate - Source Voltage (V)

Gate Threshold Voltage



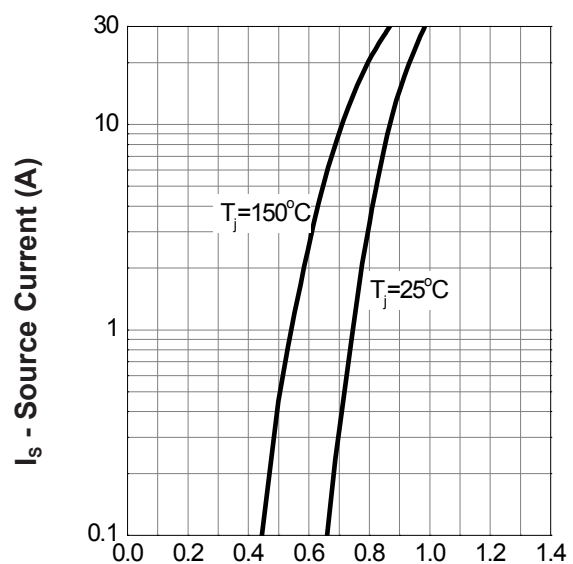
$T_J$  - Junction Temperature ( $^{\circ}C$ )

Drain-Source On Resistance



$T_J$  - Junction Temperature ( $^{\circ}C$ )

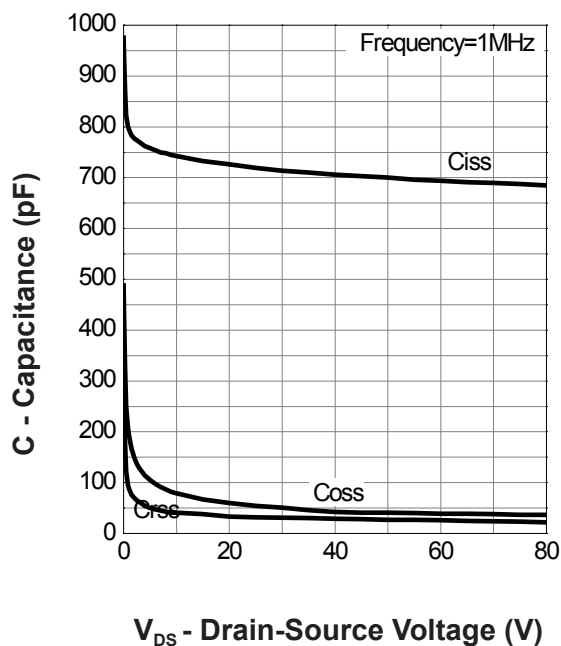
Source-Drain Diode Forward



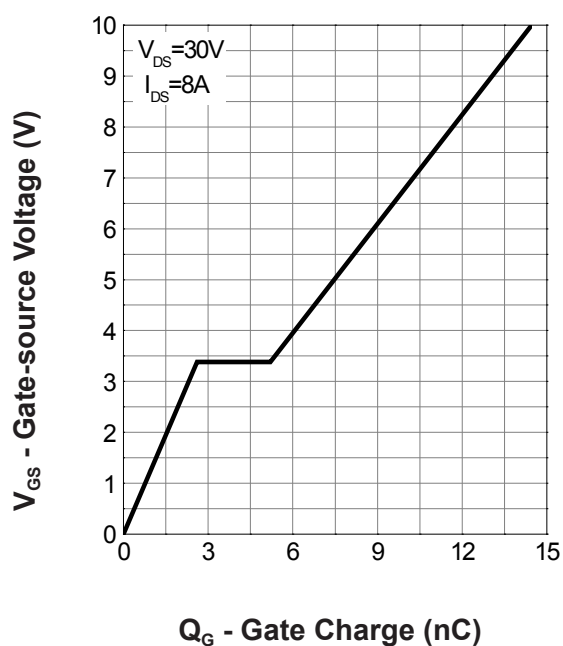
$V_{SD}$  - Source - Drain Voltage (V)

## Typical Operating Characteristics

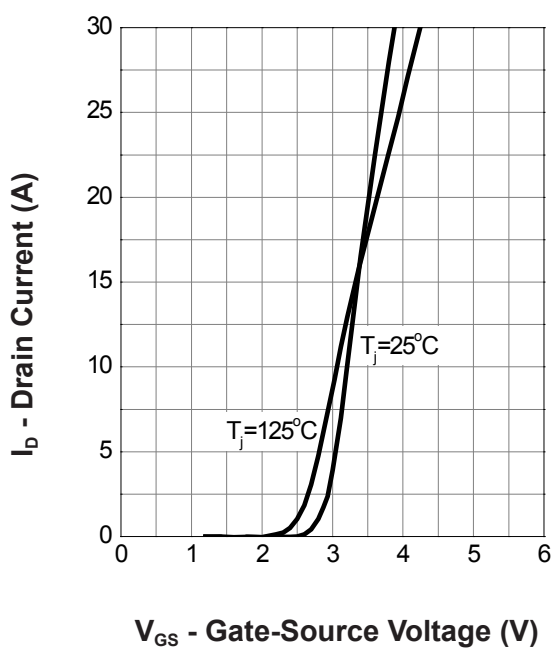
Capacitance



Gate Charge



Transfer Characteristics



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