

N-Channel MOSFET

General Description

The WSD3050DN33 is the highest performance trench N-Channel MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications .

The WSD3050DN33 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline

Absolute Maximum Ratings

- 100% E_{AS} Guaranteed
- Green Device Available

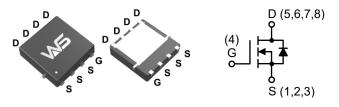
Product Summery

BV _{DSS}	R _{DS(ON)}	I _D
30V	6.7mΩ	50A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

DFN3X3-8L Pin Configuration



Cumhal	Barranatar	Rating		Unite	
Symbol	Parameter	10s	Steady State	Units	
V _{DS}	Drain-Source Voltage	30		V	
V _{GS}	Gate-Source Voltage	±20			
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	5	50		
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	37			
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	14	12	А	
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	11.4	9.7		
I _{DM}	Pulsed Drain Current ²	100			
E _{AS}	Single Pulse Avalanche Energy ³	50		mJ	
I _{AS}	Avalanche Current	14		А	
P _D @T _C =25°C	Power Dissipation ⁴	26		W	
P _D @T _A =25°C	Power Dissipation ⁴	2.5	1.6	VV	
T _{STG}	Storage Temperature Range -55 to 150		°C		
TJ	Operating Junction Temperature Range	ange -55 to 150		U	

Thermal Data

Symbol	Parameter	Тур.	Max.	Units
R _{θJA}	Thermal Resistance, Junction-to-Ambient ¹		70	
R _{θJA}	Thermal Resistance, Junction-to-Ambient ¹ (t ≤10s)		50	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case ¹		4.7	



N-Channel MOSFET

Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250µA	30			V
$\Delta BV_{DSS}/\Delta T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA		0.024		V/°C
Р	Otatis Davis Oceano Or Davistance 2	V _{GS} =10V , I _D =15A		6.7	8.5	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =4.5V , I _D =10A		8.2	11	11177
V _{GS(th)}	Gate Threshold Voltage		1.5	1.8	2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	- V _{GS} =V _{DS} , Ι _D =250μΑ		-3.5		mV/°C
		V _{DS} =24V , V _{GS} =0V , T _J =25°C			1.0	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			5.0	μA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V , V_{DS} =0V			±100	nA
9 _{fs}	Forward Transconductance V _{DS} =5V , I _D =30A			40		S
R _g	Gate Resistance $V_{DS}=0V$, $V_{GS}=0V$, f = 1.0MHz			1.0	1.5	Ω
Qg	Total Gate Charge (4.5V)	Charge (4.5V)		10	12	
Q _{gs}	Gate-Source Charge	V _{DS} =15V,V _{GS} =10V,I _D =15A		3.5	4.1	nC
Q _{gd}	Gate-Drain Charge			4.2	4.7	
T _{d(on)}	Turn-On Delay Time			9	17	
Tr	$\begin{tabular}{lllllllllllllllllllllllllllllllllll$			11	23	
T _{d(off)}				29	52	ns
T _f	Fall Time			7	12	
C _{iss}	Input Capacitance			1200	1400	
C _{oss}	Output Capacitance V_{DS} =15V , V_{GS} =0V , f = 1.0MHz			185	220	pF
C _{rss}	Reverse Transfer Capacitance			113	140	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
۱ _S	Continuous Source Current ^{1,6}	(-1)			20	
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V,Force Current			100	A
V _{SD}	Diode Forward Voltage ²	V_{GS} =0V , I_{S} =2A , T_{J} =25°C			1.0	V
t _{rr}	Reverse Recovery Time			15		ns
Q _{rr}	Reverse Recovery Charge	l _F =15A,dl _{SD} /dt=100A/µs,T _J =25°C		7		nC

Note:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper,t≤10sec.

2. The data tested by pulsed , pulse width \leq 300us , duty cycle $\leq 2\%$

3. The $\,E_{AS}\,$ data shows Max. rating . The test condition is $\,V_{DD}$ =25V, V_{GS} =10V, L=0.1mH, I_{AS} =20A

4. The power dissipation is limited by 150°C junction temperature.

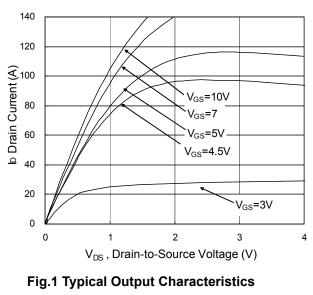
5. The Min. value is 100% $\,{\sf E}_{AS}\,$ tested guarantee.

6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



N-Channel MOSFET

Typical Characteristics



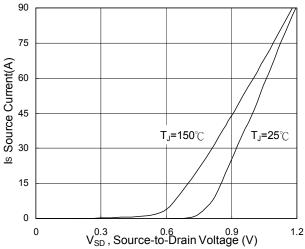


Fig.3 Forward Characteristics of Reverse

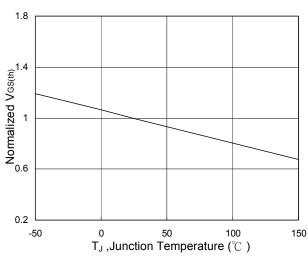


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

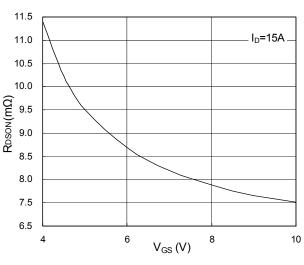


Fig.2 On-Resistance vs. Gate-Source

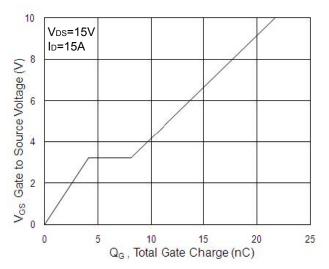
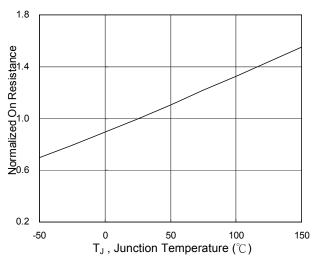


Fig.4 Gate-Charge Characteristics







N-Channel MOSFET

Typical Characteristics (Cont.)

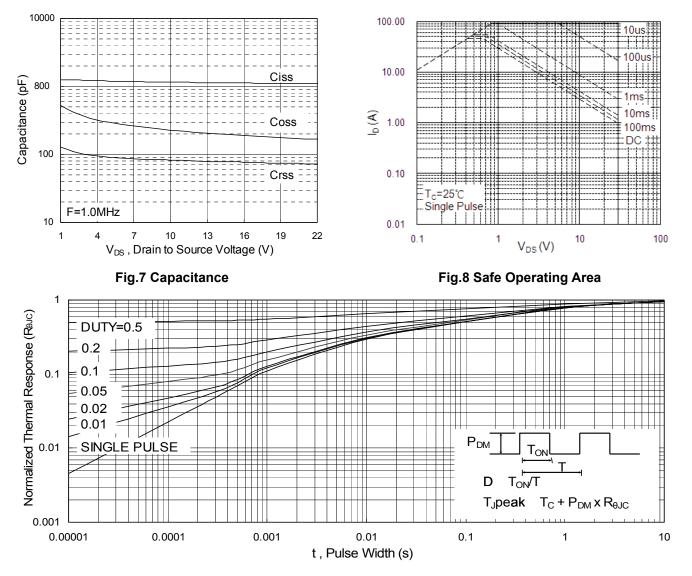


Fig.9 Normalized Maximum Transient Thermal Impedance

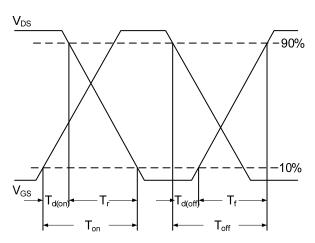


Fig.10 Switching Time Waveform

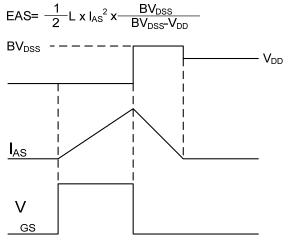
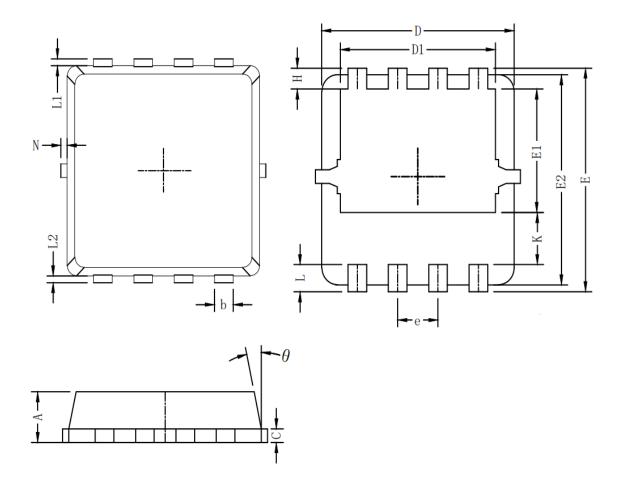


Fig.11 Unclamped Inductive Switching Waveform



N-Channel MOSFET

Packaging information



Symbol	Dim in mm				
Symbol	min	typ	max		
А	0.6	0.75	0.9		
b	0.2	0.3	0.4		
С	0.15	0.2	0.25		
D	3	3.1	3.2		
D1	2.3	2.45	2.6		
E	3.15	3.3	3.45		
E1	1.43	1.73	1.93		
E2	2.9	3.05	3.2		
е	0.65BSC				
Н	0.2	0.35	0.5		
K	0.57	0.77	0.87		
L	0.3	0.4	0.5		
L1/L2	0.1REF				
θ	8°	10°	13°		
Ν	0		0.15		



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