

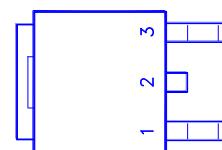
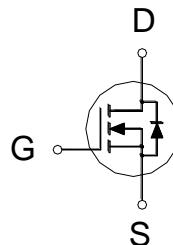
**NIKO-SEM****N-Channel Enhancement Mode  
Field Effect Transistor****P5506BDA**

TO-252

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
60V	55mΩ	15A

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNITS
Drain-Source Voltage		$V_{DS}$	60		V
Gate-Source Voltage		$V_{GS}$	$\pm 20$		V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	15		A
	$T_C = 100^\circ\text{C}$		9.5		
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	30		A
Avalanche Current		$I_{AS}$	14.7		
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	10.8		mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	27		W
	$T_C = 100^\circ\text{C}$		11		
Junction & Storage Temperature Range		$T_J, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		4.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

<sup>1</sup>Pulse width limited by maximum junction temperature.**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.75	2.3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
		$V_{DS} = 40\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	

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Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 8A$	49	72	$m\Omega$
		$V_{GS} = 10V, I_D = 10A$	43	55	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 10A$	20		S
<b>DYNAMIC</b>					
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$	376		$pF$
Output Capacitance	$C_{oss}$		54		
Reverse Transfer Capacitance	$C_{rss}$		37		
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$	1.6		$\Omega$
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{GS} = 10V$	10		$nC$
			6		
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		1.2		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		3.9		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 30V, V_{GS} = 10V, I_D = 10A$	9.5		$nS$
Rise Time <sup>2</sup>	$t_r$		36		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		21		
Fall Time <sup>2</sup>	$t_f$		60		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>					
Continuous Current	$I_S$			15	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 10A, V_{GS} = 0V$		1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 10A, dI_F/dt = 100A / \mu S$	18		$nS$
Reverse Recovery Charge	$Q_{rr}$		7		$nC$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

**NIKO-SEM**

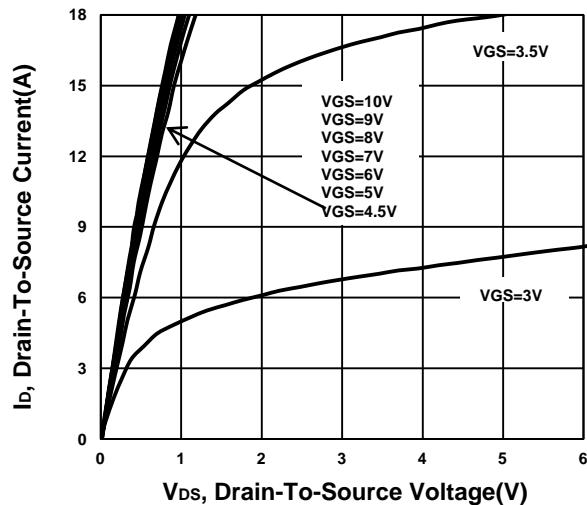
**N-Channel Enhancement Mode  
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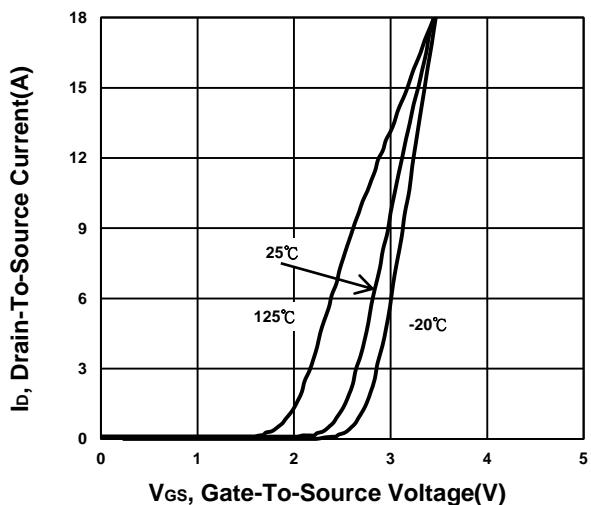
**TO-252**

**Halogen-Free & Lead-Free**

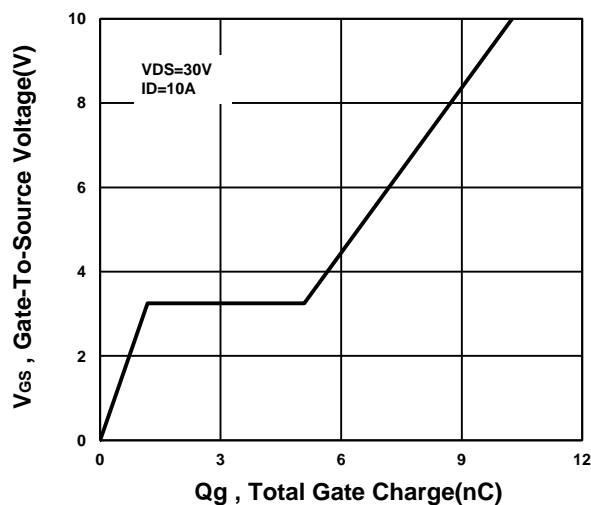
**Output Characteristics**



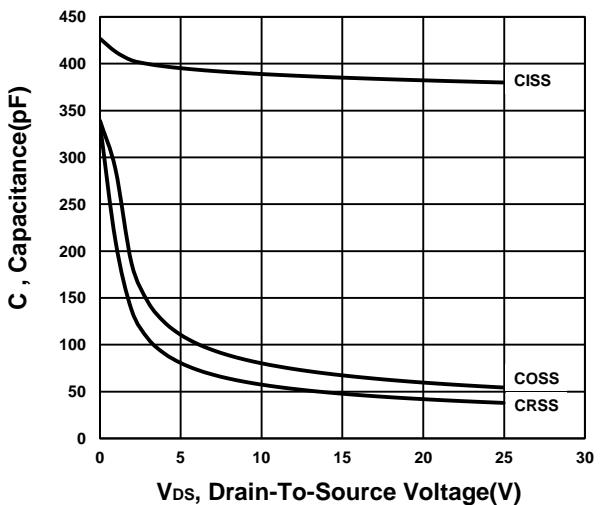
**Transfer Characteristics**



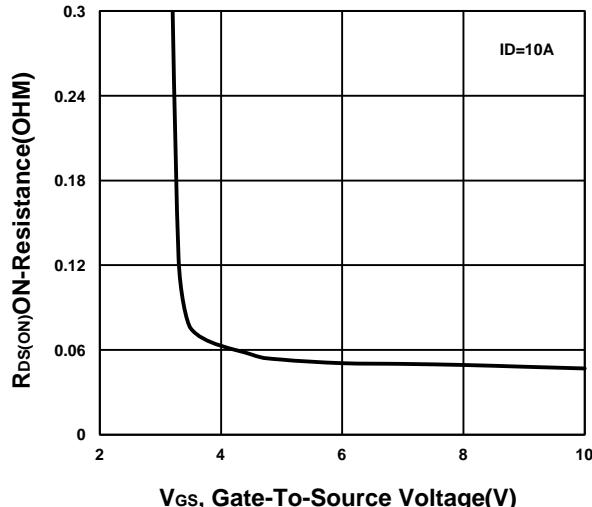
**Gate charge Characteristics**



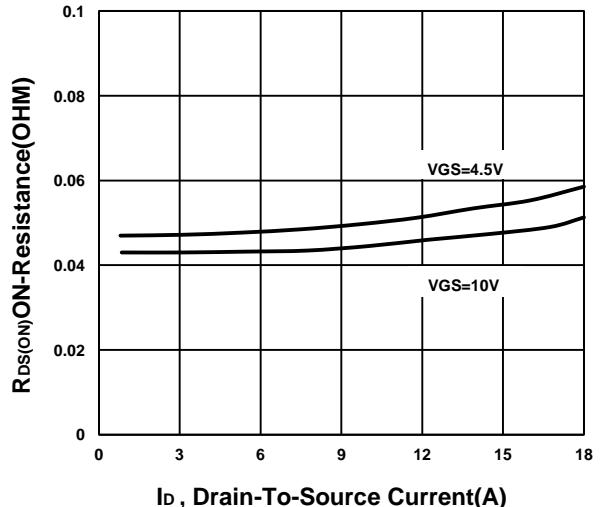
**Capacitance Characteristic**



**On-Resistance VS Gate-To-Source Voltage**



**On-Resistance VS Drain Current**



**NIKO-SEM**

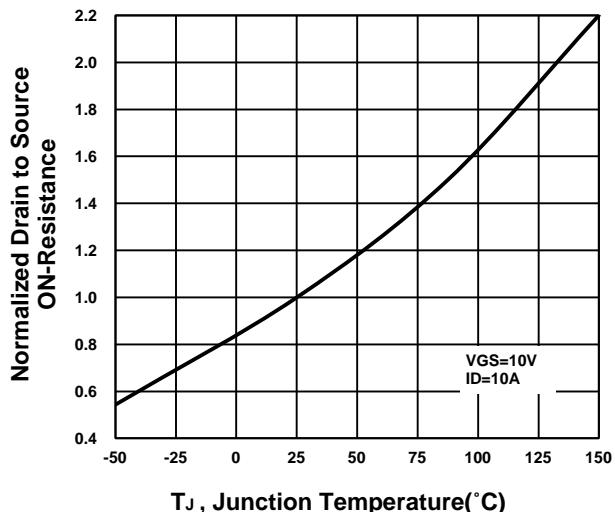
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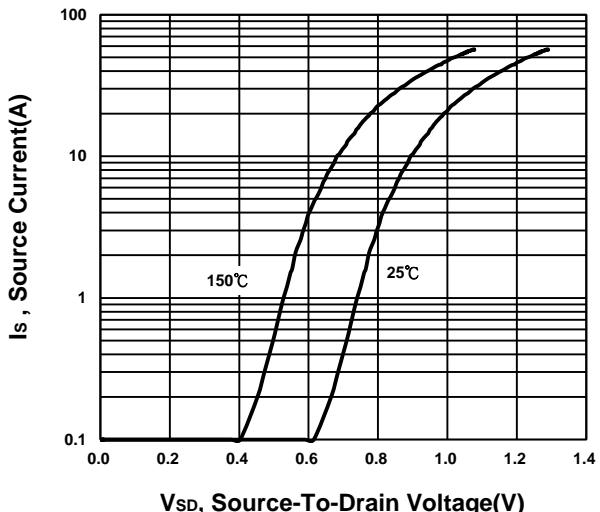
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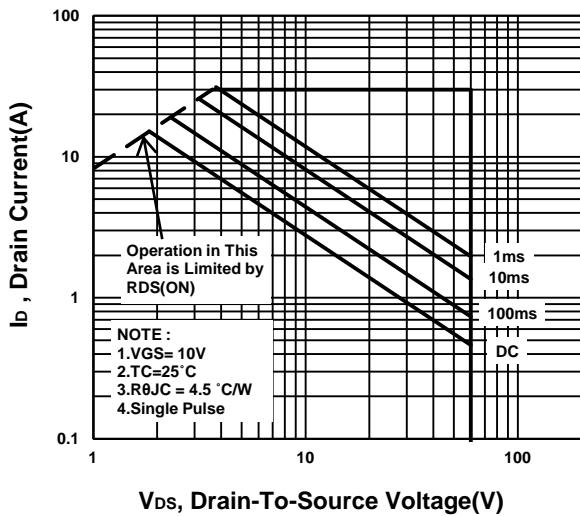
**On-Resistance VS Temperature**



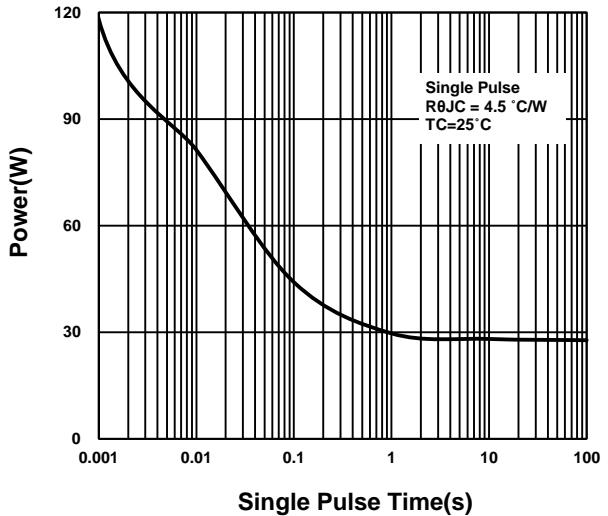
**Source-Drain Diode Forward Voltage**



**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

