

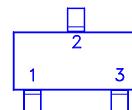
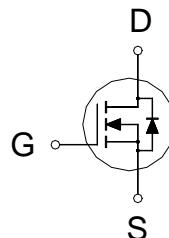
**NIKO-SEM****N-Channel Logic Level Enhancement Mode  
Field Effect Transistor****P8503BMG**

SOT-23

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	85m $\Omega$	2.4A



1. GATE
2. DRAIN
3. SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS		UNITS
Gate-Source Voltage		$V_{GS}$	$\pm 20$		V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	2.4		A
	$T_A = 100^\circ\text{C}$		1.5		
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	10		
Avalanche Current		$I_{AS}$	12		A
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	7		mJ
Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	0.75		W
	$T_A = 100^\circ\text{C}$		0.3		
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150		$^\circ\text{C}$
Lead Temperature (1/16" from case for 10 sec.)		$T_L$	275		

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		166	$^\circ\text{C} / \text{W}$

<sup>1</sup>Pulse width limited by maximum junction temperature.

**ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.8	1.7	2.5	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
		$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$			10	

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On-State Drain Current <sup>1</sup>	$I_{D(ON)}$	$V_{DS} = 10V, V_{GS} = 10V$	10			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 1.5A$		72	115	$m\Omega$
		$V_{GS} = 10V, I_D = 3A$		50	85	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 15V, I_D = 3A$		16		S

**DYNAMIC**

Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		217		pF
Output Capacitance	$C_{oss}$			68		
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS} = 0.5V_{(BR)DSS}, I_D = 3A$		46		nC
				3		
Total Gate Charge <sup>2</sup>	$Q_{g(4.5V)}$	$V_{DS} = 0.5V_{(BR)DSS}, I_D = 3A$		6.2		nC
	$Q_{g(10V)}$			0.7		
Gate-Source Charge <sup>2</sup>	$Q_{gs(4.5V)}$	$V_{DS} = 15V, R_L = 1\Omega$		0.7		nS
	$Q_{gs(10V)}$			1.5		
Gate-Drain Charge <sup>2</sup>	$Q_{gd(4.5V)}$	$I_D \cong 3A, V_{GS} = 10V, R_{GS} = 2.5\Omega$		2.1		nS
	$Q_{gd(10V)}$			6.0		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, R_L = 1\Omega$		6.0		nS
Rise Time <sup>2</sup>	$t_r$			6.0		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			20		
Fall Time <sup>2</sup>	$t_f$			5.0		

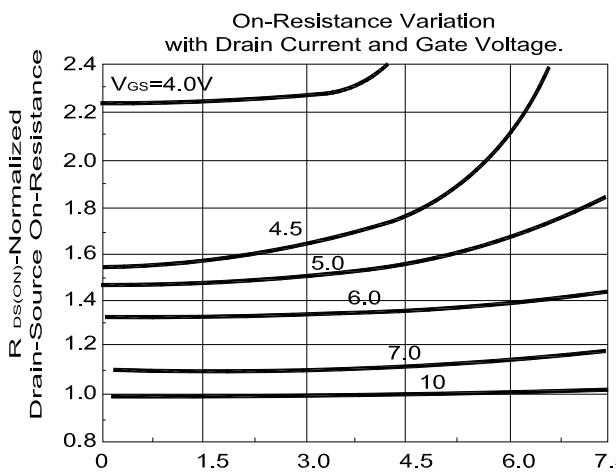
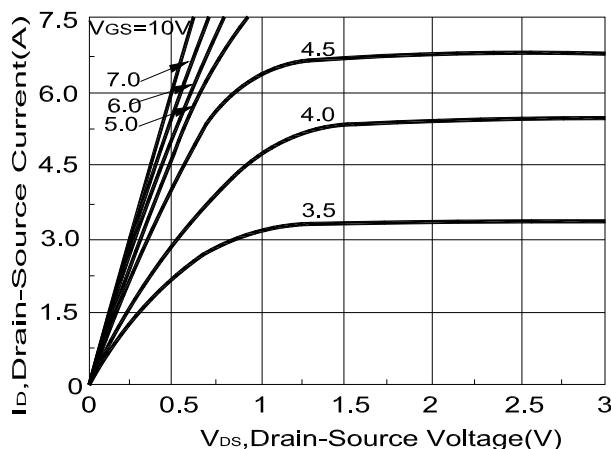
**SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_A = 25^\circ C$ )**

Continuous Current	$I_S$				2.3	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = I_S, V_{GS} = 0V$			1.5	V

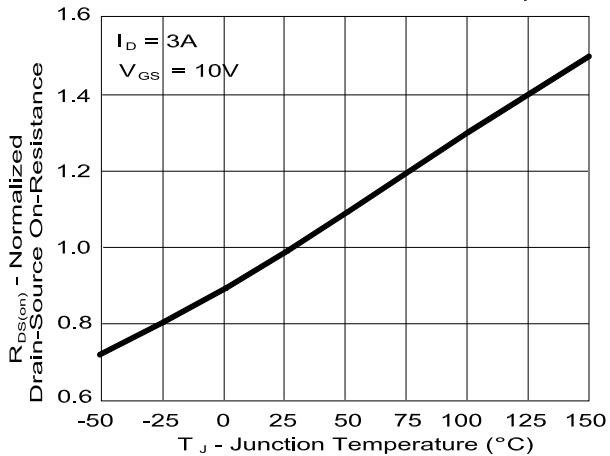
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.**REMARK: THE PRODUCT MARKED WITH “1JYWW”, DATE CODE or LOT #**

## Typical Electrical Characteristics

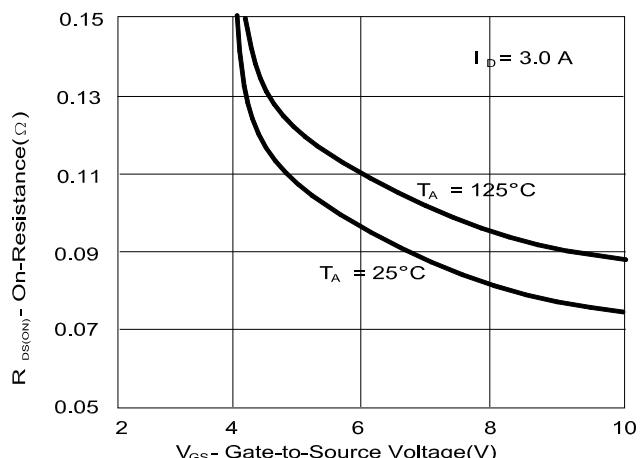
On-Region Characteristics.



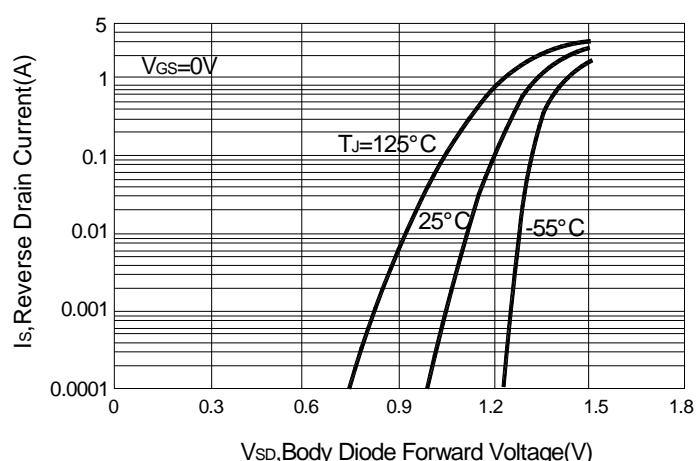
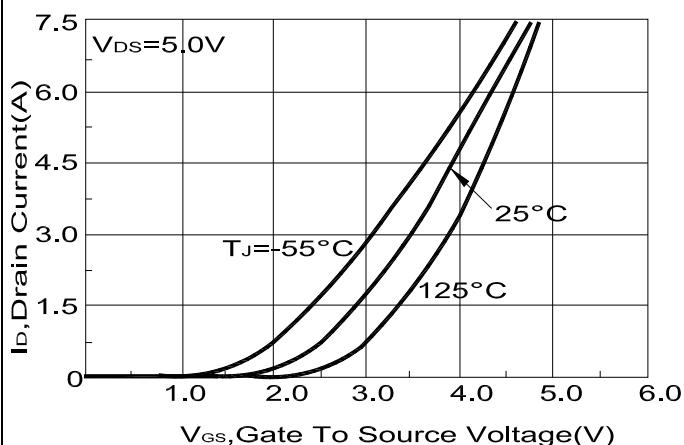
On-Resistance Variation with Temperature



On-Resistance Variation with Gate-to-Source Voltage



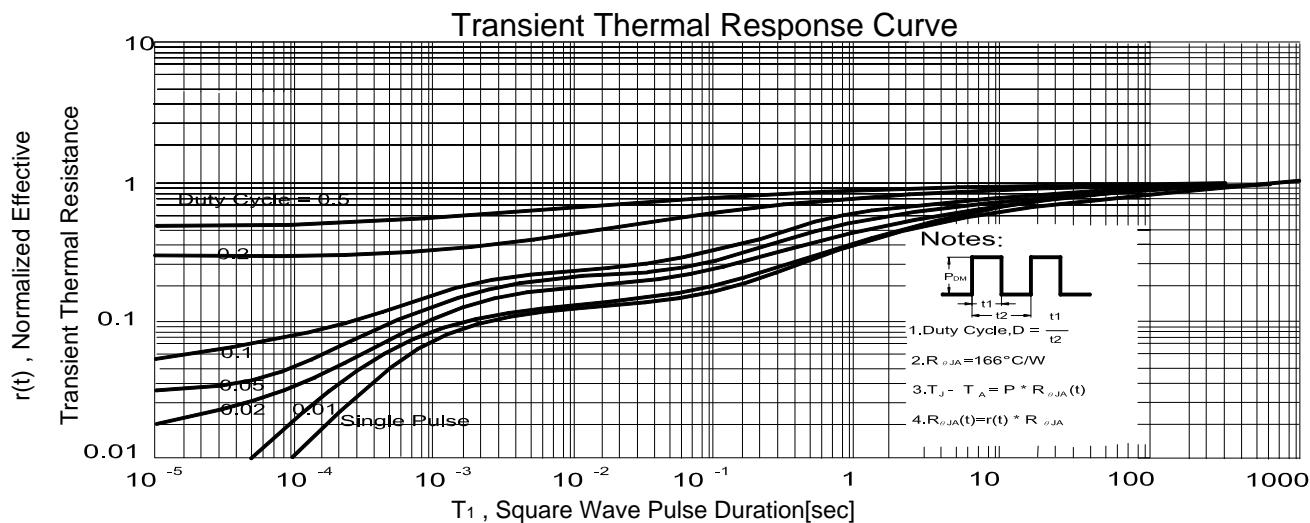
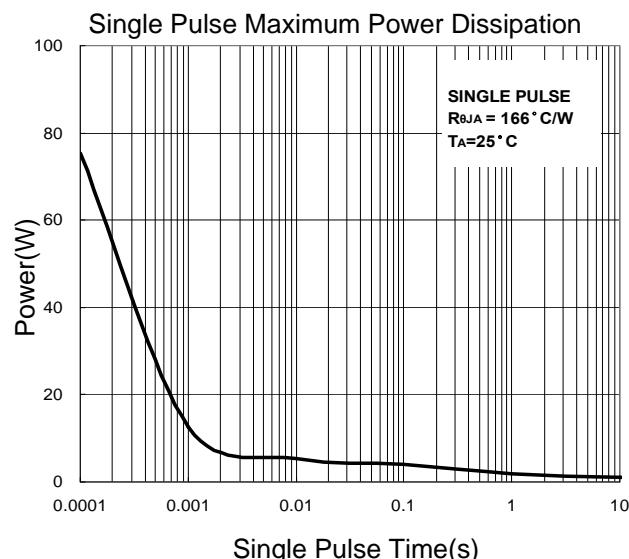
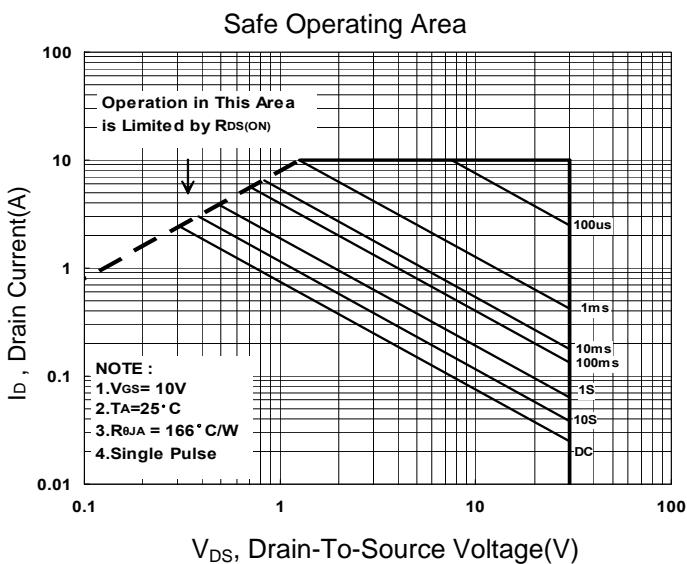
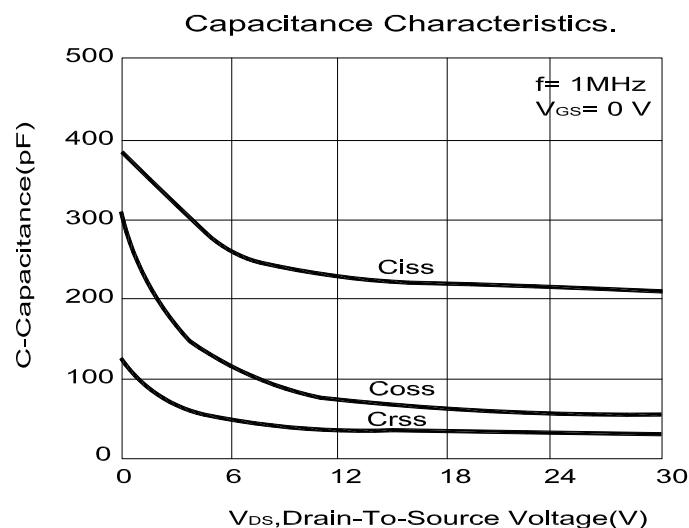
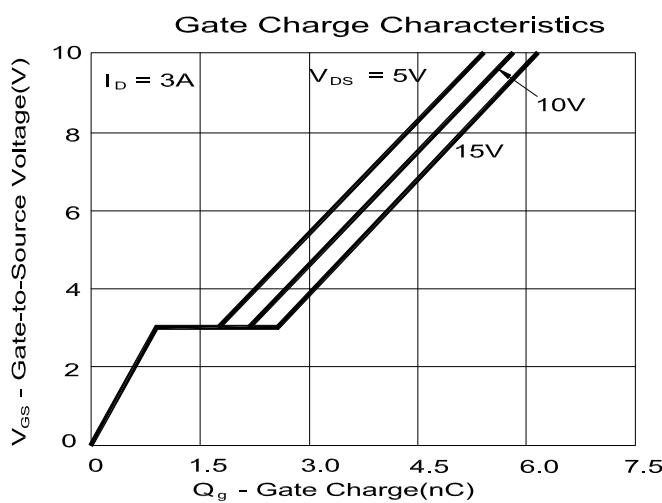
Transfer Characteristics.



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**SOT-23 (M3) MECHANICAL DATA**

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.85	0.95	1.15	H	0.1	0.15	0.35
B	2.4		3	I	0.2		0.6
C	1.4	1.6	1.8	J			
D	2.7	2.9	3.1	K			
E	0.9	1.1	1.4	L			
F	0		0.1	M			
G	0.3	0.4	0.5	N			

