

MS40P05(AU)

P-Channel 40-V (D-S) MOSFET

Description

The device is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent RDS(ON) and gate charge for most of the synchronous buck converter applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- R_{DS(ON)} =70mΩ @ V_{GS} =-10V
- R_{DS(ON)} =100mΩ @ V_{GS} =-4.5V
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Typical Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- AEC-Q101 qualification available for suffix-AU

Package type : SOT-23

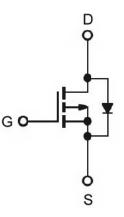
Packing & Order Information

3,000/Reel

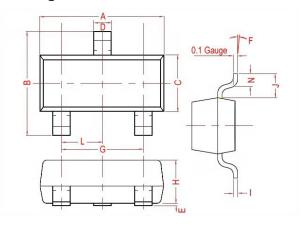




Graphic Symbol

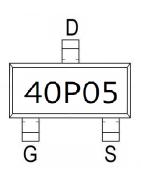


Package Dimension



م REF. م	Millimeter @			Millimeter @		
	Min. 🖉	Max. @	REF. 🖉	Min. 🖉	Max. 🖉	
A $_{e^{2}}$	2.70 0	3.10 <i>÷</i>	G e	1.90 Ref. @		
B 🕫	2.30 @	3.00 <i>⊷</i>	H 🖓	⇔ 0.90	1.30 @	
C 👳	1.20 @	1.75 @	Ιø	0.05 @	0.21 @	
D e	₀ 0.30	0.50 <i>~</i>	J $_{arphi}$	0.58 Ref. @		
E e	0.01 ↔	0.15 @	Le	0.95 Typ. 🖉		
F e	د₊ °0	10° +2	N 🕫	0.20 Min. «		

Marking



RoHS Compliant



P-Channel 40-V (D-S) MOSFET

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
VDS	Drain-Source Voltage	-40	V		
VGS	Gate-Source Voltage	±20	V		
ID	Continuous Drain Current ¹ (TA =25°C)	-4.6	А		
	Continuous Drain Current ¹ (TA =100°C)	-2	А		
IDM	Pulsed Drain Current ^{1,2}	-18	A		
IAS	Single Pulse Avalanche Current, L =0.1mH ³	-13	А		
EAS	Single Pulse Avalanche Energy, L =0.1mH ³	8.4	mJ		
PD	Power Dissipation ⁴ (TA =25°C)	2.5	W		
TJ/TSTG	Operating Junction and Storage Temperature	-55 to +175	°C		

Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
R _{0JA}	Maximum Junction-to-Ambient ¹	125	°C/W		
R _{θJC}	Maximum Junction-to-Case ¹	80	°C/W		



P-Channel 40-V (D-S) MOSFET

Electrical Characteristics (TJ=25°C unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$V_{\text{GS}(th)}$	Gate Threshold Voltage	V_{DS} =V _{GS} , I _D =-250µA	-1.0	-	-2.5	V
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250µA	-40	-	-	V
g fs	Forward Transconductance	V _{DS} =-5V, I _D =-3A	-	5.8	-	S
Igss	Gate-Source Leakage Current	V_{DS} =0V, V_{GS} =±20V	-	-	±100	nA
I _{DSS}	Drain-Source Leakage Current	V_{DS} =-36V, V_{GS} =0V, T_{J} =25°C		-	-1	μA
		V _{DS} =-36V, V _{GS} =0V, T _J =55°C	-		-10	
RDS (on)	Drain-Source On-Resistance ²	V_{GS} =-10V, I _D =-3A	-	-	- 70	mΩ
		V _{GS} =-4.5V, I _D =-2A	-	-	100	
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =-25V, I _{AS} =-3.5A	6.1	-	-	mJ
V_{SD}	Diode Forward Voltage ²	IS = -1A, VGS = 0, V T _J =25°C	-	-	-1.2	V
ls	Continuous Source Current ^{1,6}		-	-	-3.2	Α
Ism	Pulsed Source Current ^{2,6}	$V_{\rm G} = V_{\rm D} = 0V$, Force Current	-	_	-16	A



P-Channel 40-V (D-S) MOSFET

Dynamic						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	V _{DS} =-32V		6.4		
Qgs	Gate-Source Charge	I _D =-3A		2.1		nC
Q _{gd}	Gate-Drain Charge	V _{GS} =-4.5V		2.5		
t _{d(on)}	Turn-On Delay Time ²	V _{DS} =-20V		4.2		
tr	Rise Time	I _D =-3A		23		
t _{d(off)}	Turn-Off Delay Time	V _{GS} =-4.5V		26.8		ns
t _f	Fall Time	R _G =3.3Ω		20.6		
Ciss	Input Capacitance	V _{DS} =-15V		620		
Coss	Output Capacitance	V _{GS} =0V		65		pF
Crss	Reverse Transfer Capacitance	f =1.0MHz		53		1
Rg	Gate Resistance	V _{DS} =0V, f =1.0MHz		4.3		Ω

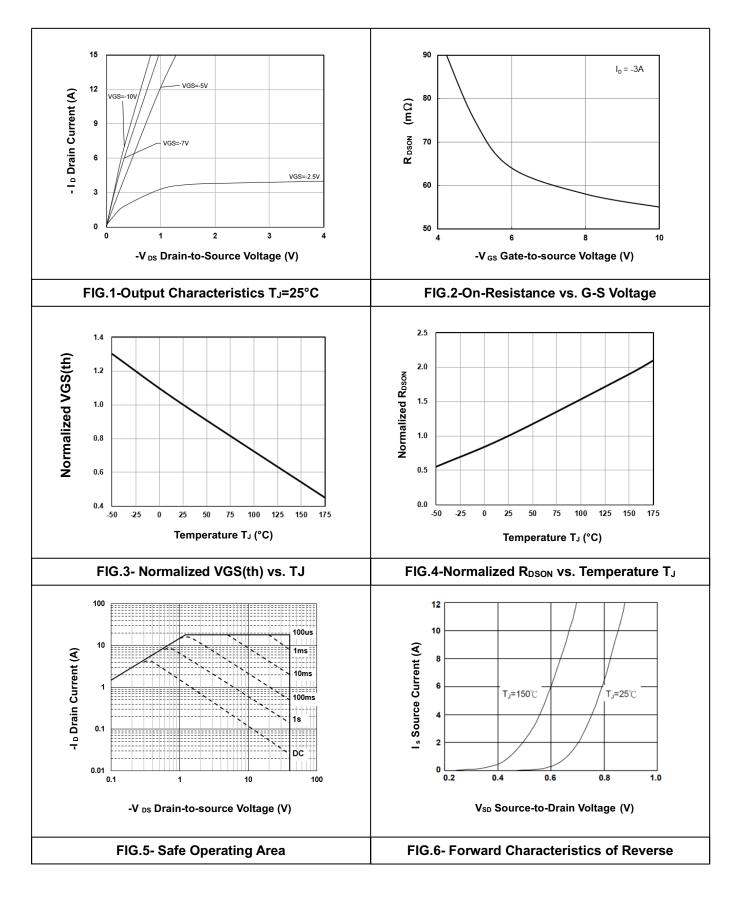
Notes

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 3. The EAS data shows maximum rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-13A.
- 4. The power dissipation is limited by 175°C junction temperature.
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



P-Channel 40-V (D-S) MOSFET

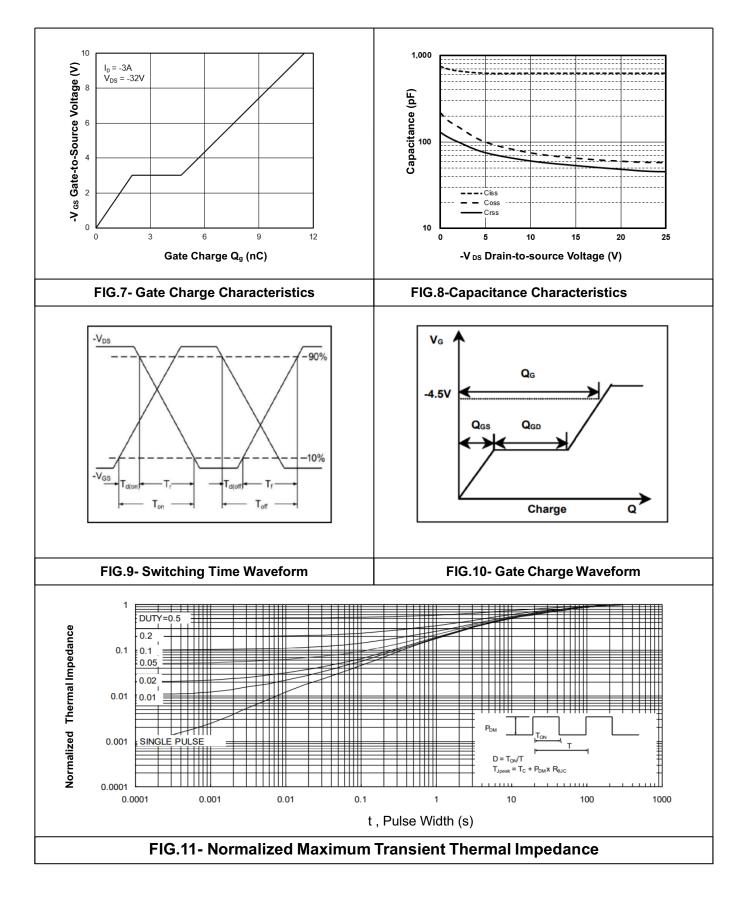
• Typical Electrical Characteristics





P-Channel 40-V (D-S) MOSFET

• Typical Electrical Characteristics





P-Channel 40-V (D-S) MOSFET

WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE. Bruckewell Technology Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Bruckewell"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product. Bruckewell makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Bruckewell disclaims

- (i) Any and all liability arising out of the application or use of any product.
- (ii) Any and all liability, including without limitation special, consequential or incidental damages.

(iii) Any and all implied warranties, including warranties of fitness for particular purpose, noninfringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Bruckewell's knowledge of typical requirements that are often placed on Bruckewell products in generic applications.

Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time.

Product specifications do not expand or otherwise modify Bruckewell's terms and conditions of purchase, including but not limited to the warranty expressed therein.