

N-Channel 100-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize $R_{DS(ON)}$, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- R_{DS(ON)} =4.5mΩ @ V_{GS} =10V
- Super Low Gate Charge
- Excellent dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

Typical Applications

- Motor Driver
- Load Switch
- Synchronous Rectifier
- BMS

Package type: PDFN 5X6

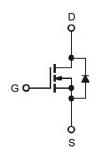
Packing & Order Information

3,000/Reel

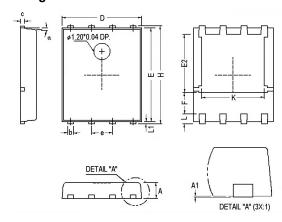


RoHS Compliant

Graphic Symbol

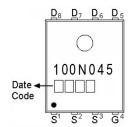


Package Dimension



555	Millimeter			5	Millimeter			
REF.	Min.	Nom.	Max.	REF.	Min.	Nom.	Max.	
Α	0.85	1.00	1.15	E	5.70	-	5.90	
A1	0.00	-	0.10	е	-	1.27	-	
b	0.30	-	0.51	Н	5.90	-	6.20	
С	0.20	-	0.30	L	-	0.60	-	
D	4.80	-	5.00	L1	0.06	-	0.20	
F	1.10 Ref.		α	0°	-	12°		
F2		50 Ref		K	3 70	3 90	4 10	

Marking





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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
V _{DS}	Drain-Source Voltage	100	V		
V_{GS}	Gate-Source Voltage	±20	V		
lo	Continuous Drain Current¹ (Tc =25°C)	100	А		
	Continuous Drain Current¹ (Tc=100°C)	95	Α		
I _{DM}	Pulsed Drain Current ^{1,2}	480	Α		
I _{AS}	Single Pulse Avalanche Current, L =0.5mH³	53	Α		
Eas	Single Pulse Avalanche Energy, L =0.5mH³	702	mJ		
P _D	Power Dissipation ⁴ (T _C =25°C)	208	W		
T _J /T _{STG}	Operating Junction and Storage Temperature	-55 to 150	°C		

Thermal Resistance Ratings						
Symbol	Parameter	Maximum	Units			
ReJA	Maximum Junction-to-Ambient ¹	62	°C/W			
Rejc	Maximum Junction-to-Case ¹	0.6	°C/W			

Electrical Characteristics (T _J =25°C unless otherwise specified)							
Symbol	Parameter Test Conditions		Min.	Тур.	Max.	Units	
V _{GS (th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	2	3	4	V	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	100	-	-	V	
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =100V, V _{GS} =0V, T _J =25°C	-	-	1	μА	
		V _{DS} =100V, V _{GS} =0V, T _J =125°C			10		
R _{DS} (on)	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	-	3.7	4.5	mΩ	
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L =0.5mH, I _{AS} =20A	100	_	_	mJ	
V _{SD}	Diode Forward Voltage ²	I _S =50A, V _{GS} =0V, T _J =25°C	-	-	1.3	V	
ls	Continuous Source Current ^{1,6}	V V 9V 5	-	-	100		
Ism	Pulsed Source Current ^{2,6}	V _G =V _D =0V, Force Current	-	_	200	A	



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Dynamic						
Symbol	Parameter Test Conditions		Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	V _{DS} =50V		72		
Qgs	Gate-Source Charge	I _D =20A		28		nC
Qgd	Gate-Drain Charge	V _{GS} =10V		15		
td(on)	Turn-On Delay Time ²	V _{DD} =50V I _D =20A V _{GS} =10V		35		
tr	Rise Time			18		
td(off)	Turn-Off Delay Time			45		ns
tf	Fall Time	$R_G = 3.0\Omega$		55		
Ciss	Input Capacitance	V _{DS} =50V		4725		
Coss	Output Capacitance	V _{GS} =0V f=1.0MHz		609		pF
Crss	Reverse Transfer Capacitance			14		
Rg	Gate Resistance	V _{GS} =V _{DS} =0V, f =1.0MHz		1		Ω
trr	Reverse Recovery Time	I=-20A -11/4t-400A/ T' 05°0		70		nS
Qrr	Reverse Recovery Charge	IF=30A, dl/dt=100A/μs, Tj=25°C		170		nC

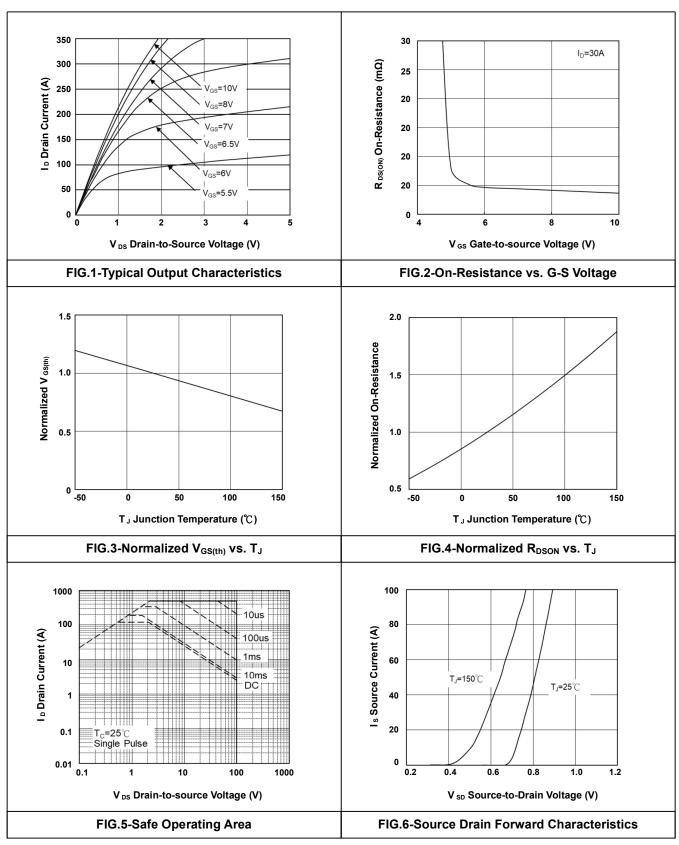
Notes

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.
- 3. The EAS data shows maximum rating. The test condition is V_{DD} =25V, V_{GS} =10V, L=0.5mH, I_{AS} =53A.
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



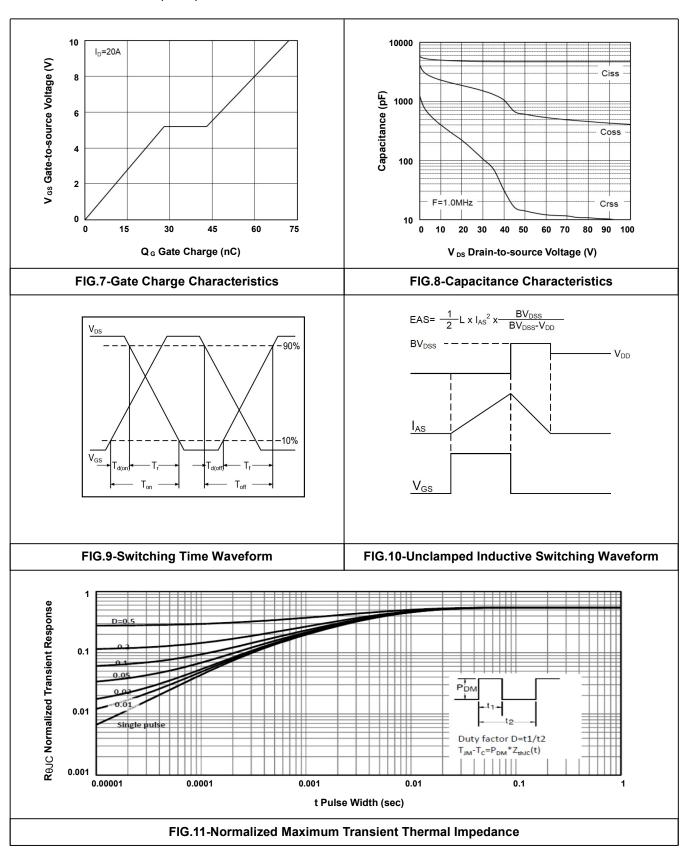
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• Typical Electrical Characteristics





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