

N-Channel 100-V (D-S) MOSFET

Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize $R_{DS(ON)}$, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- R_{DS(ON)} = 5.5mΩ@ V_{GS} = 10V
- Fast switching
- Improve dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

Typical Applications

- Networking
- Load Switch
- Synchronous Rectifier
- Quick Charger

Package type : PDFN 5X6

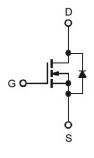
Packing & Order Information

3,000/Reel

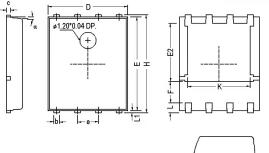


RoHS Compliant

Graphic Symbol



Package Dimension



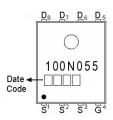


DETAIL "A" (3X:1)

A1

REF.	Millimeter		REF.	Millimeter			
NEF.	Min.	Nom.	Max.	NEF.	Min.	Nom.	Max.
Α	0.85	1.00	1.15	E	5.70	-	5.90
A1	0.00	-	0.10	е	-	1.27	-
b	0.30	-	0.51	Н	5.90	-	6.20
С	0.20	-	0.30	L	-	0.60	-
D	4.80	-	5.00	L1	0.06	-	0.20
F	1	.10 Ref.		α	0 °	-	12°
E2	3	3.50 Ref.		K	3.70	3.90	4.10

Marking





N-Channel 100-V (D-S) MOSFET

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings					
Symbol	Parameter	Value	Units		
V _{DS}	Drain-Source Voltage	100	V		
V _{GS}	Gate-Source Voltage	+20/-12	V		
Ιp	Continuous Drain Current ¹ (T _c =25°C)	70	А		
ID	Continuous Drain Current ¹ (T _c =100°C)	44	А		
IDM	Pulsed Drain Current ^{1,2}	280	А		
las	Single Pulse Avalanche Current, L =0.1mH ³	80	А		
E _{AS}	Single Pulse Avalanche Energy, L =0.1mH ³	320	mJ		
5	Power Dissipation ⁴ (T _c =25°C)	62.5	W		
PD	Power Dissipation ⁴ (T _A =25°C)	2	W		
TJ/TSTG	Operating Junction and Storage Temperature	-50 to +150	°C		

Thermal Resistance Ratings						
Symbol	Parameter	Maximum	Units			
Reja	Maximum Junction-to-Ambient ¹	60	°C/W			
Rejc	Maximum Junction-to-Case ¹	2	°C/W			

Electrical Characteristics (T」=25°C unless otherwise specified)						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	1.0	1.6	2.5	V
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	100	-	-	V
g fs	Forward Transconductance	V _{DS} =10V, I _D =5A	-	18	-	S
I _{GSS}	Gate-Source Leakage Current	V _{DS} =0V, V _{GS} =20V	-	-	100	nA
IDSS	Drain-Source Leakage Current	V _{DS} =100V, V _{GS} =0V, T _J =25°C	-	-	1	μA
		V _{DS} =80V, V _{GS} =0V, T _J =85°C			10	
RDS (on)	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	-	4.6	5.5	mΩ
		V _{GS} =4.5V, I _D =10A	-	6.2	7.8	
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V, L =0.1mH, I _{AS} =40A	80		-	mJ
Vsd	Diode Forward Voltage ²	Is =1A, V _{GS} =0V, T _J =25°C	-	-	1.0	V
ls	Continuous Source Current ^{1,6}		-	-	70	_
I _{SM}	Pulsed Source Current ^{2,6}	$V_G = V_D = 0V$, Force Current	-	-	140	A

Notes

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.

3. The EAS data shows maximum rating. The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS}=80A.

4. The power dissipation is limited by 150 $^\circ\!\mathrm{C}$ junction temperature.

5. The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



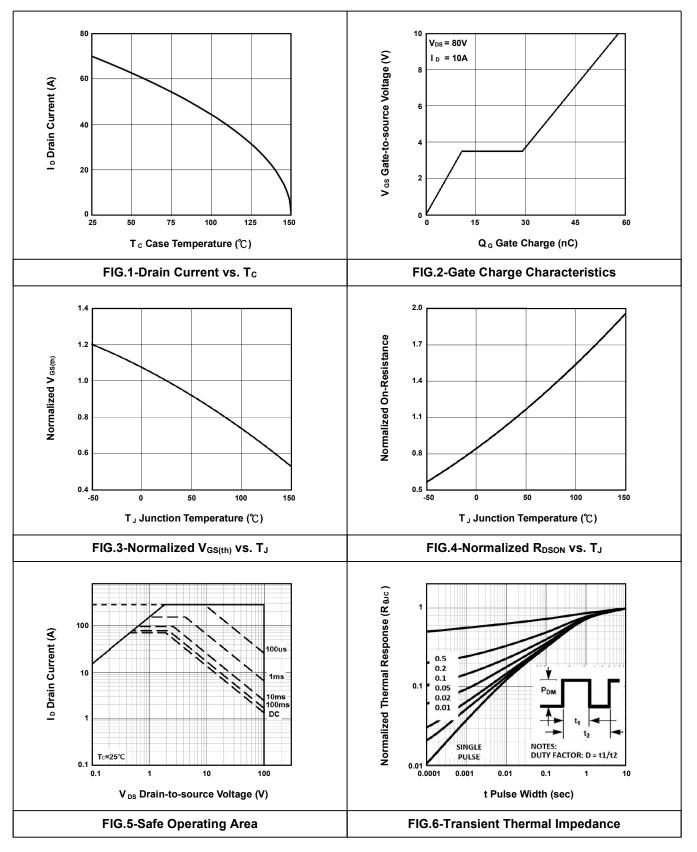
N-Channel 100-V (D-S) MOSFET

Dynamic						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	V _{DS} =80V		58.2		
Qgs	Gate-Source Charge	I _D =10A		9.2		nC
Qgd	Gate-Drain Charge	V _{GS} =10V		20.8		
td(on)	Turn-On Delay Time ²	V _{DS} =50V		24		
tr	Rise Time	I _D =1A		19.8		
td(off)	Turn-Off Delay Time	V _{GS} =10V		46		ns
tf	Fall Time	$R_G = 6\Omega$		26		
CISS	Input Capacitance	V _{DS} = 25V		4570		
Coss	Output Capacitance	V _{GS} =0V		1180		pF
CRSS	Reverse Transfer Capacitance	f=1.0MHz		49		1
Rg	Gate Resistance	$V_{GS} = V_{DS} = 0V$, f = 1.0MHz		2		Ω



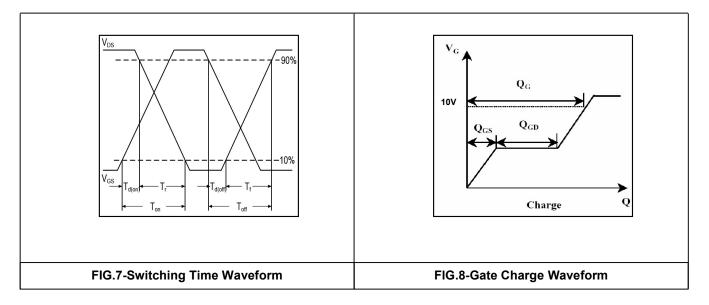
N-Channel 100-V (D-S) MOSFET

• Typical Electrical Characteristics





N-Channel 100-V (D-S) MOSFET





N-Channel 100-V (D-S) MOSFET

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE. Bruckewell Technology Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Bruckewell"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product. Bruckewell makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Bruckewell disclaims

- (i) Any and all liability arising out of the application or use of any product.
- (ii) Any and all liability, including without limitation special, consequential or incidental damages.

(iii) Any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Bruckewell's knowledge of typical requirements that are often placed on Bruckewell products in generic applications.

Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time.

Product specifications do not expand or otherwise modify Bruckewell's terms and conditions of purchase, including but not limited to the warranty expressed therein.