

### N-Channel 100-V (D-S) MOSFET

#### Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize  $R_{DS(ON)}$ , provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### Features

- R<sub>DS(ON)</sub> = 5.5mΩ@ V<sub>GS</sub> = 10V
- Fast switching
- Improve dv/dt Capability
- 100% EAS Guaranteed
- Green Device Available

### **Typical Applications**

- Networking
- Load Switch
- Synchronous Rectifier
- Quick Charger

Package type : PDFN 5X6

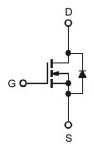
### Packing & Order Information

3,000/Reel

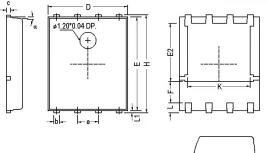


**RoHS** Compliant

#### **Graphic Symbol**



#### **Package Dimension**



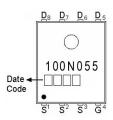


DETAIL "A" (3X:1)

A1

| REF. | Millimeter |           | REF. | Millimeter |            |      |      |
|------|------------|-----------|------|------------|------------|------|------|
| NEF. | Min.       | Nom.      | Max. | NEF.       | Min.       | Nom. | Max. |
| Α    | 0.85       | 1.00      | 1.15 | E          | 5.70       | -    | 5.90 |
| A1   | 0.00       | -         | 0.10 | е          | -          | 1.27 | -    |
| b    | 0.30       | -         | 0.51 | Н          | 5.90       | -    | 6.20 |
| С    | 0.20       | -         | 0.30 | L          | -          | 0.60 | -    |
| D    | 4.80       | -         | 5.00 | L1         | 0.06       | -    | 0.20 |
| F    | 1          | .10 Ref.  |      | α          | <b>0</b> ° | -    | 12°  |
| E2   | 3          | 3.50 Ref. |      | K          | 3.70       | 3.90 | 4.10 |

#### Marking





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### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

| Absolute Maximum Ratings |   |             |       |  |  |
|--------------------------|---|-------------|-------|--|--|
| Symbol                   | Parameter   | Value       | Units |  |  |
| V <sub>DS</sub>          | Drain-Source Voltage  | 100         | V     |  |  |
| V <sub>GS</sub>          | Gate-Source Voltage   | +20/-12     | V     |  |  |
| Ιp                       | Continuous Drain Current <sup>1</sup> (T <sub>c</sub> =25°C)  | 70          | А     |  |  |
| ID                       | Continuous Drain Current <sup>1</sup> (T <sub>c</sub> =100°C) | 44          | А     |  |  |
| IDM                      | Pulsed Drain Current <sup>1,2</sup>                           | 280         | А     |  |  |
| las                      | Single Pulse Avalanche Current, L =0.1mH <sup>3</sup>         | 80          | А     |  |  |
| E <sub>AS</sub>          | Single Pulse Avalanche Energy, L =0.1mH <sup>3</sup>          | 320         | mJ    |  |  |
| 5                        | Power Dissipation <sup>4</sup> (T <sub>c</sub> =25°C)         | 62.5        | W     |  |  |
| PD                       | Power Dissipation <sup>4</sup> (T <sub>A</sub> =25°C)         | 2           | W     |  |  |
| TJ/TSTG                  | Operating Junction and Storage Temperature                    | -50 to +150 | °C    |  |  |

| Thermal Resistance Ratings |  |         |       |  |  |  |
|----------------------------|--|---------|-------|--|--|--|
| Symbol                     | Parameter                                | Maximum | Units |  |  |  |
| Reja                       | Maximum Junction-to-Ambient <sup>1</sup> | 60      | °C/W  |  |  |  |
| Rejc                       | Maximum Junction-to-Case <sup>1</sup>    | 2       | °C/W  |  |  |  |

| Electrical Characteristics (T」=25°C unless otherwise specified) |  |  |      |      |      |       |
|---|--|--|------|------|------|-------|
| Symbol  | Parameter                                      | Test Conditions  | Min. | Тур. | Max. | Units |
| $V_{\text{GS}(\text{th})}$                                      | Gate Threshold Voltage                         | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA         | 1.0  | 1.6  | 2.5  | V     |
| BV <sub>DSS</sub>   | Drain-Source Breakdown Voltage                 | V <sub>GS</sub> =0V, I <sub>D</sub> =250µA                       | 100  | -    | -    | V     |
| <b>g</b> fs   | Forward Transconductance                       | V <sub>DS</sub> =10V, I <sub>D</sub> =5A                         | -    | 18   | -    | S     |
| I <sub>GSS</sub>  | Gate-Source Leakage Current                    | V <sub>DS</sub> =0V, V <sub>GS</sub> =20V                        | -    | -    | 100  | nA    |
| IDSS  | Drain-Source Leakage Current                   | V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C | -    | -    | 1    | μA    |
|   |  | V <sub>DS</sub> =80V, V <sub>GS</sub> =0V, T <sub>J</sub> =85°C  |      |      | 10   |       |
| RDS (on)  | Static Drain-Source On-Resistance <sup>2</sup> | V <sub>GS</sub> =10V, I <sub>D</sub> =20A                        | -    | 4.6  | 5.5  | mΩ    |
|   |  | V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A                       | -    | 6.2  | 7.8  |       |
| EAS   | Single Pulse Avalanche Energy <sup>5</sup>     | V <sub>DD</sub> =25V, L =0.1mH, I <sub>AS</sub> =40A             | 80   |      | -    | mJ    |
| Vsd   | Diode Forward Voltage <sup>2</sup>             | Is =1A, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C                | -    | -    | 1.0  | V     |
| ls  | Continuous Source Current <sup>1,6</sup>       |  | -    | -    | 70   | _     |
| I <sub>SM</sub>   | Pulsed Source Current <sup>2,6</sup>           | $V_G = V_D = 0V$ , Force Current                                 | -    | -    | 140  | A     |

#### Notes

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2. The data tested by pulsed, pulse width  $\leq$  300us, duty cycle  $\leq$  2%.

3. The EAS data shows maximum rating. The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH, I<sub>AS</sub>=80A.

4. The power dissipation is limited by 150  $^\circ\!\mathrm{C}$  junction temperature.

5. The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



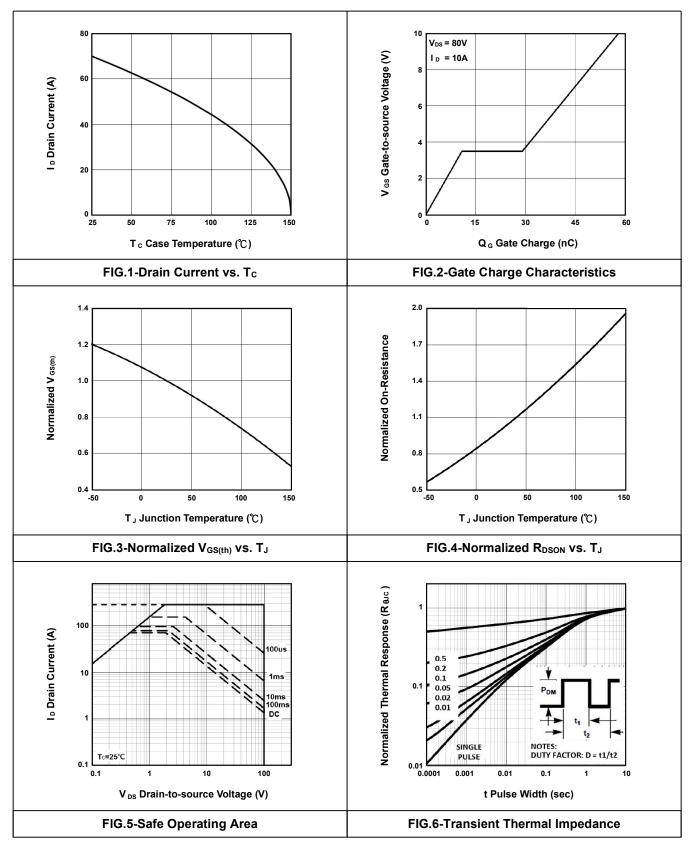
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| Dynamic |                                 |                                     |      |      |      |       |
|---------|---------------------------------|-------------------------------------|------|------|------|-------|
| Symbol  | Parameter                       | Test Conditions                     | Min. | Тур. | Max. | Units |
| Qg      | Total Gate Charge <sup>2</sup>  | V <sub>DS</sub> =80V                |      | 58.2 |      |       |
| Qgs     | Gate-Source Charge              | I <sub>D</sub> =10A                 |      | 9.2  |      | nC    |
| Qgd     | Gate-Drain Charge               | V <sub>GS</sub> =10V                |      | 20.8 |      |       |
| td(on)  | Turn-On Delay Time <sup>2</sup> | V <sub>DS</sub> =50V                |      | 24   |      |       |
| tr      | Rise Time                       | I <sub>D</sub> =1A                  |      | 19.8 |      |       |
| td(off) | Turn-Off Delay Time             | V <sub>GS</sub> =10V                |      | 46   |      | ns    |
| tf      | Fall Time                       | $R_G = 6\Omega$                     |      | 26   |      |       |
| CISS    | Input Capacitance               | V <sub>DS</sub> = 25V               |      | 4570 |      |       |
| Coss    | Output Capacitance              | V <sub>GS</sub> =0V                 |      | 1180 |      | pF    |
| CRSS    | Reverse Transfer Capacitance    | f=1.0MHz                            |      | 49   |      | 1     |
| Rg      | Gate Resistance                 | $V_{GS} = V_{DS} = 0V$ , f = 1.0MHz |      | 2    |      | Ω     |



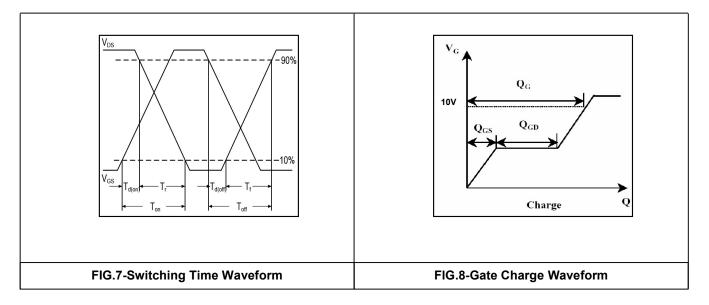
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• Typical Electrical Characteristics





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