

MSH30C16D

N & P-Channel 30-V (D-S) MOSFET

Description

The device is the highest performance trench N-ch and P-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Suit for 4.5V Gate Drive Applications
- N-ch Max $R_{on}@V_{GS}10V=12m\Omega$ $R_{on}@V_{GS}4.5V=16m\Omega$
- P-ch Max $R_{on}@V_{GS}-10V=17m\Omega$ $R_{on}@V_{GS}-4.5V=26m\Omega$
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

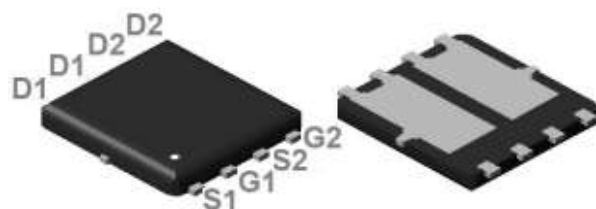
Typical Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

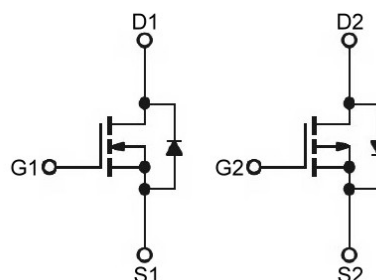
Package type : PDFN 5X6 Dual

Packing & Order Information

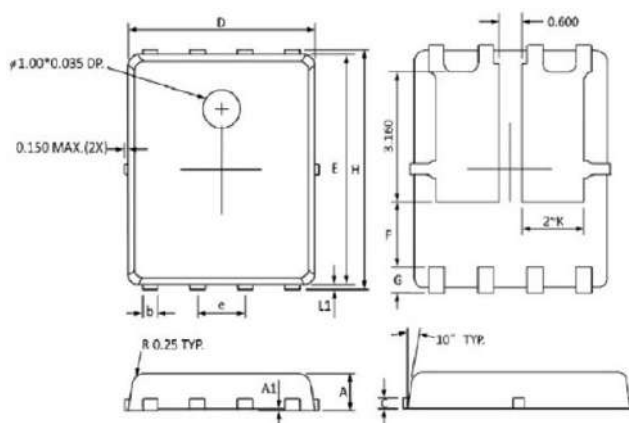
3,000/Reel



Graphic Symbol

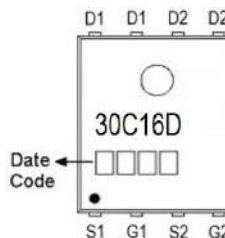


Package Dimension



REF.	Millimeter			REF.	Millimeter		
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.90	1.00	1.10	E	5.70	-	5.90
A1	0.00	-	0.05	e	-	1.27	-
b	0.33	-	0.51	H	5.90	-	6.20
c	0.20	-	0.30	G	0.50	-	0.70
D	4.80	-	5.00	L1	0.06	-	0.20
F	1.6 Ref.			K	-	1.60	-

Marking



RoHS Compliant

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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (unless otherwise specified)

Symbol	Parameter	Value		Units
		N-ch	P-ch	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
I_D	Continuous Drain Current ¹ ($T_C=25^\circ\text{C}$)	33	-31	A
	Continuous Drain Current ¹ ($T_C=70^\circ\text{C}$)	21	-20	A
I_{DM}	Pulsed Drain Current ² ($T_C=25^\circ\text{C}$)	132	-124	A
I_{AS}	Single Pulse Avalanche Current, $L=0.1\text{mH}^3$	22	-38	A
E_{AS}	Single Pulse Avalanche Energy, $L=0.1\text{mH}^3$	24	72	mJ
P_D	Power Dissipation ³ ($T_A=25^\circ\text{C}$)	2.5		W
T_J/T_{STG}	Operating Junction and Storage Temperature	-55 to +150		$^\circ\text{C}$

Thermal Resistance Ratings

Symbol	Parameter	Maximum	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ¹	55	$^\circ\text{C/W}$
$R_{\theta JC}$	Maximum Junction-to-Case ¹	5	$^\circ\text{C/W}$

Electrical Characteristics($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Ch	Min.	Typ.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	N	1.0	-	2.5	V
		$V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	P	-1.0	-	-2.5	V
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	N	30	-	-	V
		$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	P	-30	-	-	V
g_{fs}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=9\text{A}$	N	-	25	-	S
		$V_{DS}=-5\text{V}$, $I_D=-6\text{A}$	P	-	24	-	S
I_{GSS}	Gate-Source Leakage Current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$	N P	-	-	± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	N	-	-	1	μA
		$V_{DS}=24\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	N	-	-	5	
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	P	-	-	-1	
		$V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=55^\circ\text{C}$	P	-	-	-5	
$R_{DS(on)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10\text{V}$, $I_D=9\text{A}$	N	-	9	12	m Ω
		$V_{GS}=4.5\text{V}$, $I_D=5\text{A}$	N	-	12	16	
		$V_{GS}=-10\text{V}$, $I_D=-8\text{A}$	P	-	13	17	
		$V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	P	-	19	26	
E_{AS}	Single Pulse Avalanche Energy ⁵	$V_{DD}=25\text{V}$, $L=0.1\text{mH}$, $I_{AS}=15\text{A}$	N	11	-	-	mJ
		$V_{DD}=-25\text{V}$, $L=0.1\text{mH}$, $I_{AS}=-15\text{A}$	P	11	-	-	
V_{SD}	Diode Forward Voltage ²	$I_S=1\text{A}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	N	-	-	1.2	V
		$I_S=-1\text{A}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	P	-	-	-1.2	
I_S	Continuous Source Current ¹⁴ (Diode)	$V_G=V_D=0\text{V}$, Force Current	N P	-	-	6 -6	A
R_g	Gate Resistance	$V_{DS}=0\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	N P	-	1.8 8	-	Ω

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Dynamic and switching Characteristics

Symbol	Parameter	Test Conditions	Ch	Min.	Typ.	Max.	Units
Q_g	Total Gate Charge ²	N-Ch	N P	--	9.8 22	--	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=15V, I_D=9A, V_{GS}=4.5V$ P-Ch	N P	--	4.1 5.4	--	
Q_{gd}	Gate-Drain Charge	$V_{DS}=-15V, I_D=-8A, V_{GS}=-4.5V$	N P	--	3.5 7	--	
$t_{d(on)}$	Turn-On Delay Time ²	N-Ch	N P	--	4.1 32	--	ns
t_r	Rise Time	$V_{DS}=15V, I_D=1A, V_{GS}=10V,$ $R_G=1.5\Omega$	N P	--	8 34.5	--	
$t_{d(off)}$	Turn-Off Delay Time	P-Ch	N P	--	29 71	--	
t_f	Fall Time	$V_{DS}=-15V, I_D=-1A, V_{GS}=-10V,$ $R_G=3.3\Omega$	N P	--	3.8 10.2	--	
C_{ISS}	Input Capacitance	N-Ch	N P	--	940 2213	--	pF
C_{OSS}	Output Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0MHz$ P-Ch	N P	--	132 311	--	
C_{RSS}	Reverse Transfer Capacitance	$V_{DS}=-15V, V_{GS}=0V, f=1.0MHz$	N P	--	108 235	--	

Notes

1. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. The EAS data shows maximum rating. The test condition is N-ch $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=22A$,
P-ch $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-38A$.
4. The power dissipation is limited by 150°C junction temperature.
5. The Min. value is 100% EAS tested guarantee.
6. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

- Typical Electrical Characteristics N-Channel

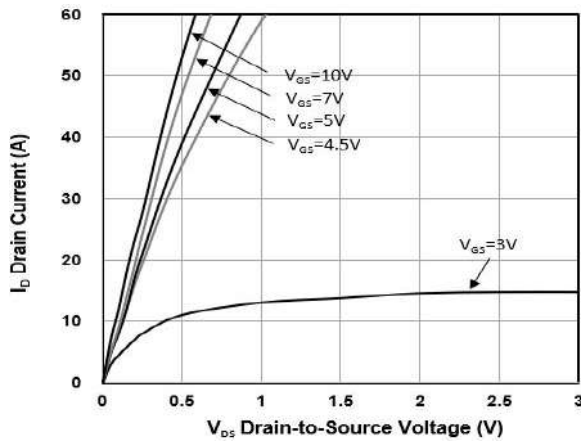


FIG.1-Typical Output Characteristics

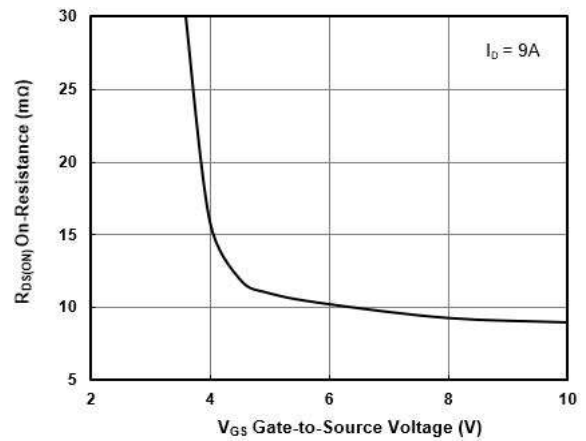


FIG.2-On-Resistance vs. G-S Voltage

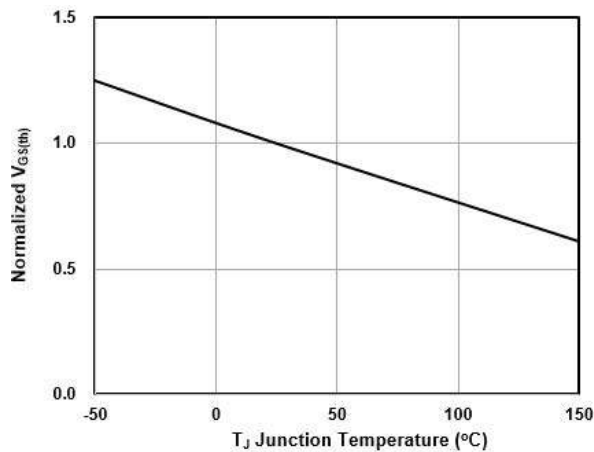


FIG.3-Normalized $V_{GS(th)}$ vs. T_J

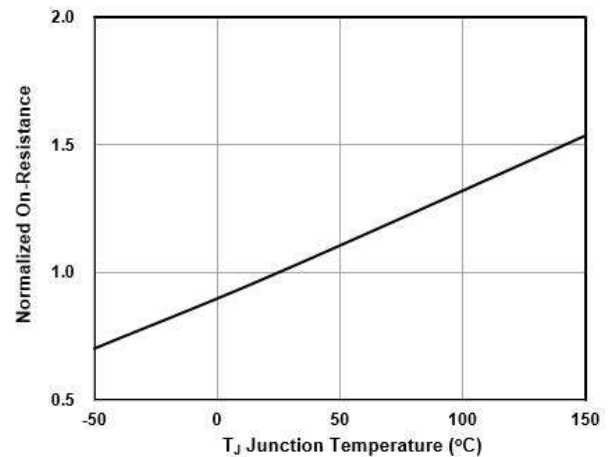


FIG.4-Normalized $R_{DS(on)}$ vs. T_J

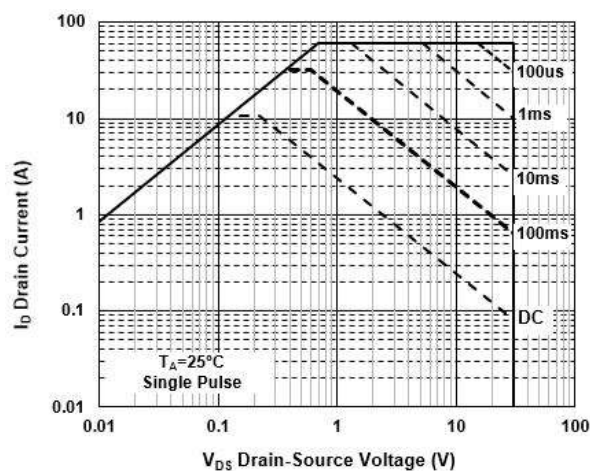


FIG.5-Safe Operating Area

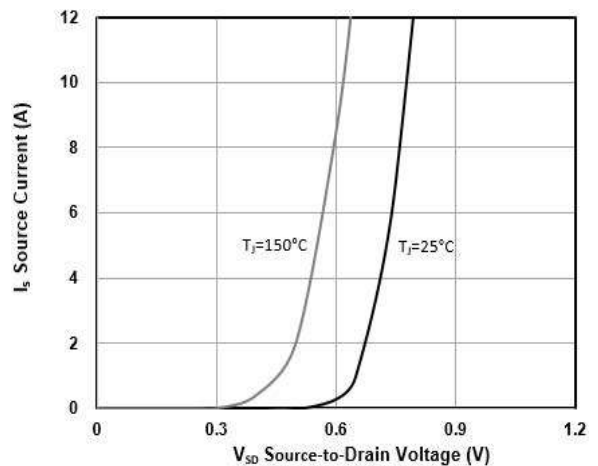


FIG.6-Forward Characteristics of Reverse

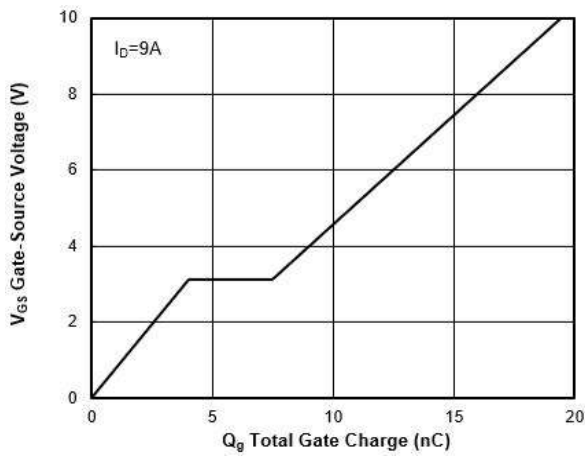


FIG.7-Gate Charge Characteristics

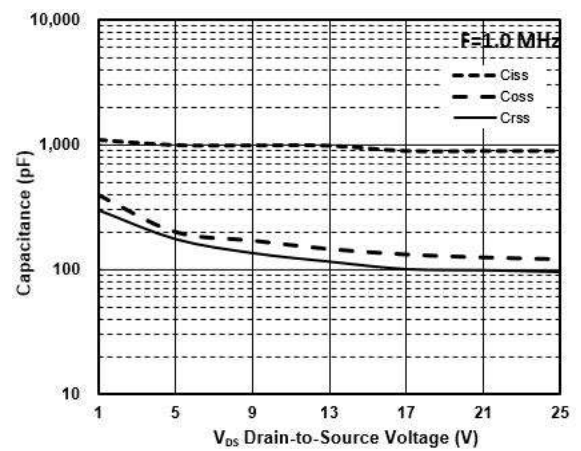


FIG.8-Capacitance Characteristics

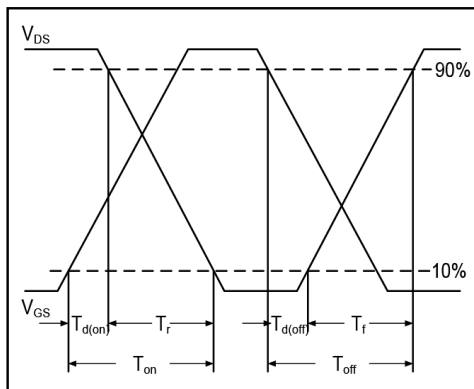


FIG.9-Switching Time Waveform

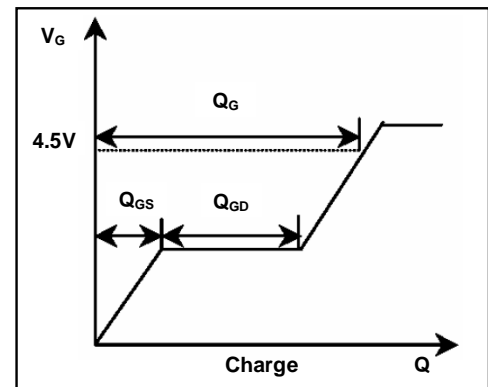


FIG.10-Gate Charge Waveform

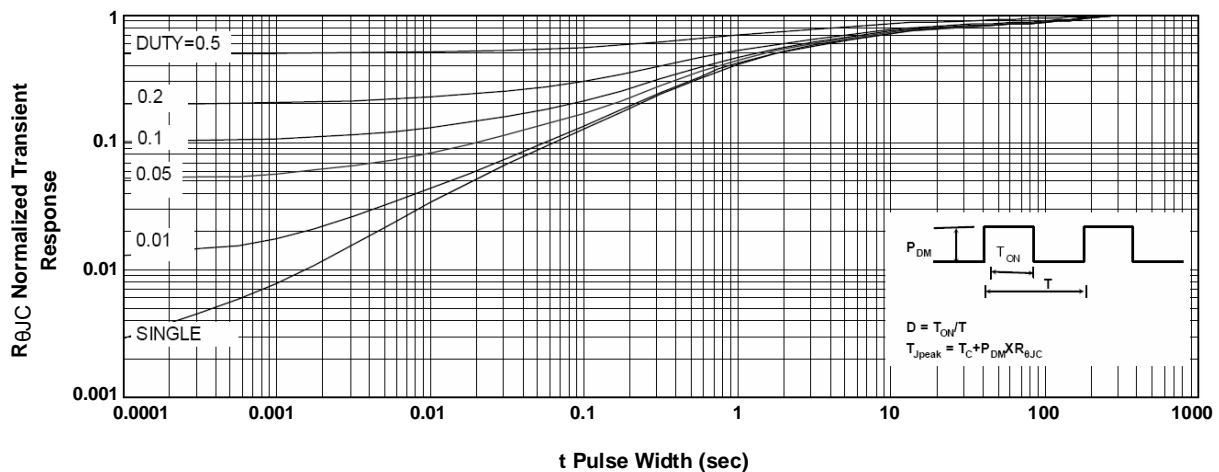
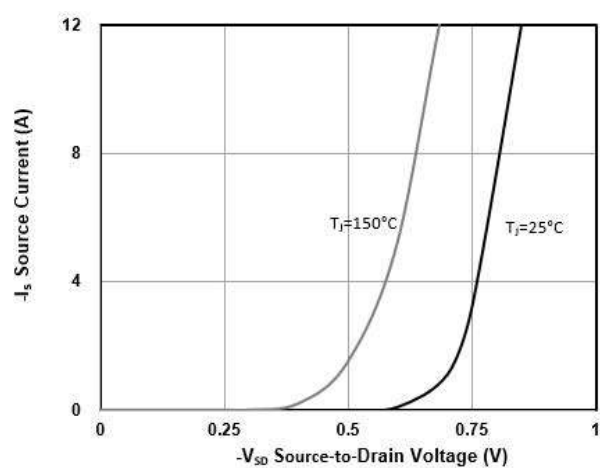
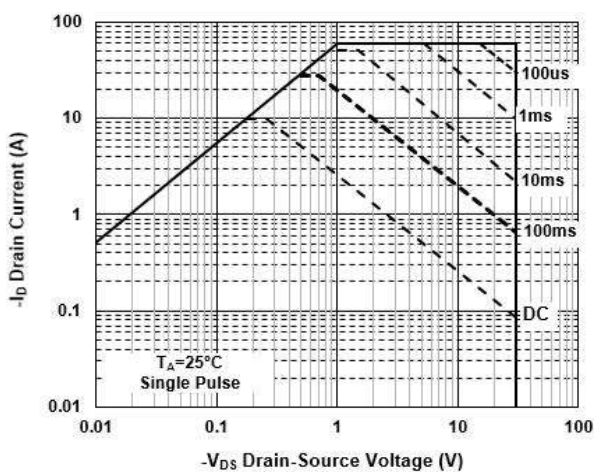
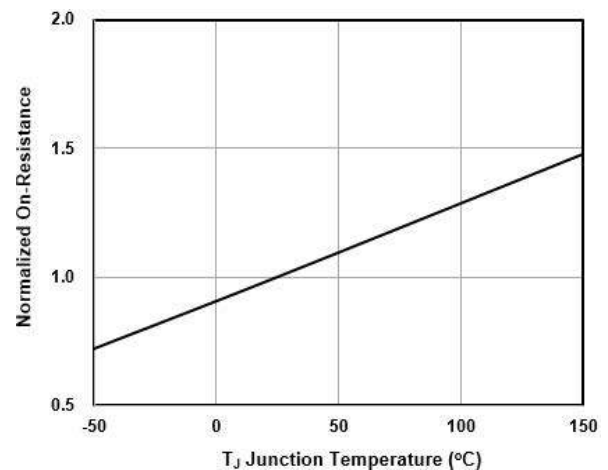
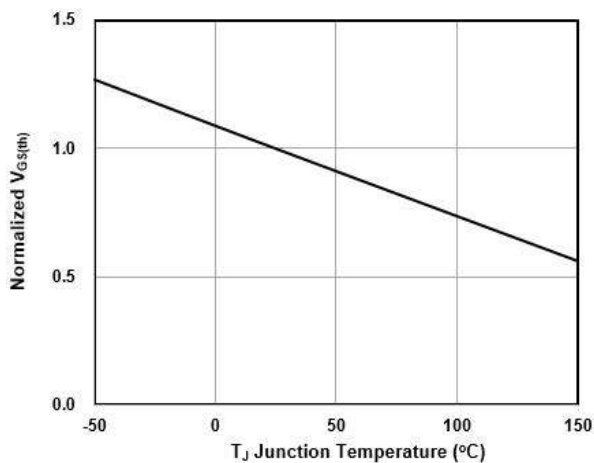
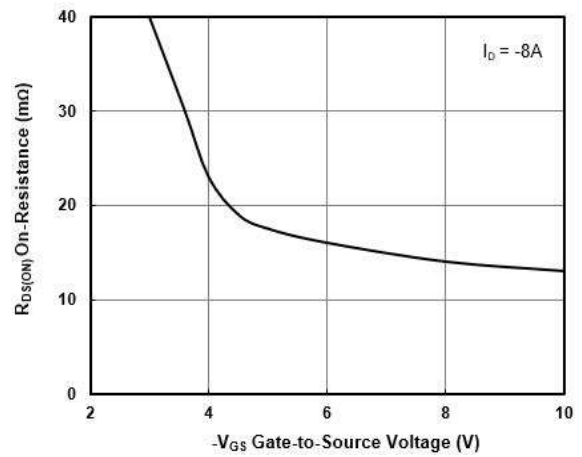
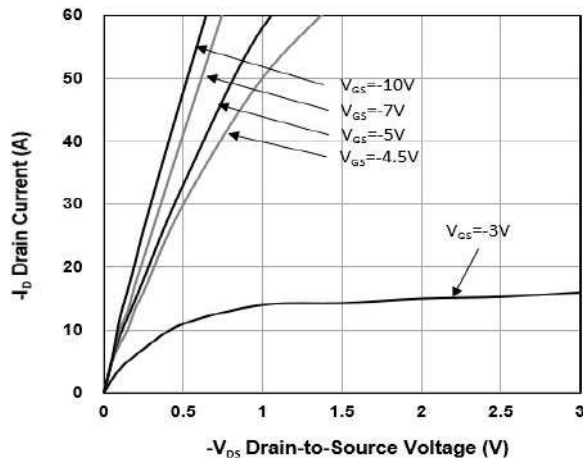


FIG.11-Normalized Maximum Transient Thermal Impedance

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- Typical Electrical Characteristics P-Channel



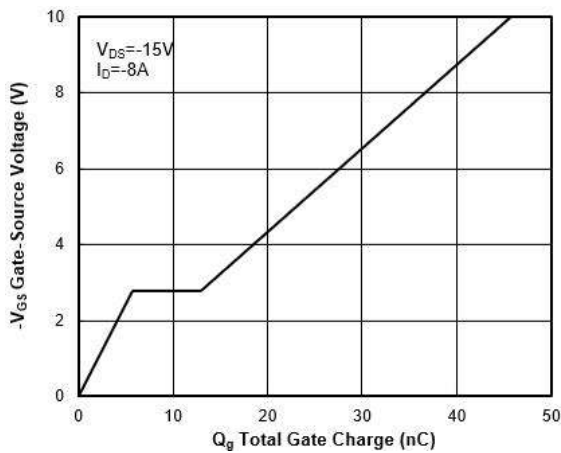


FIG.7-Gate Charge Characteristics

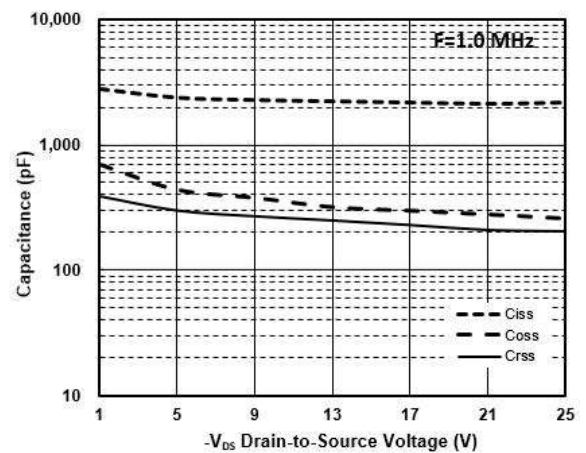


FIG.8-Capacitance Characteristics

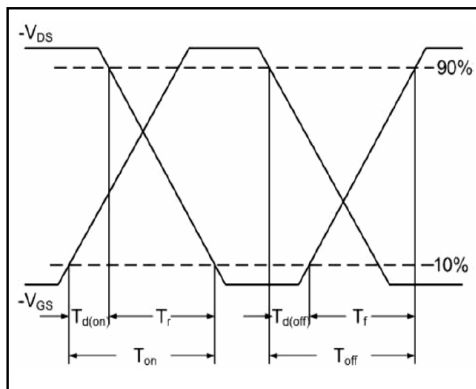


FIG.9-Switching Time Waveform

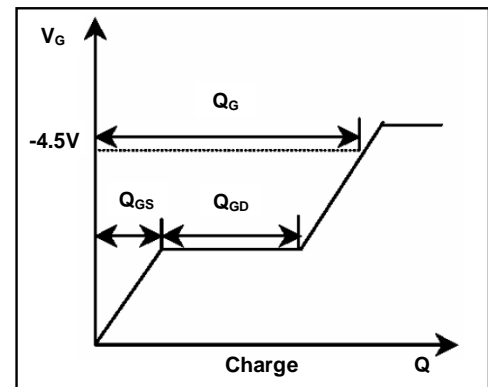


FIG.10-Gate Charge Waveform

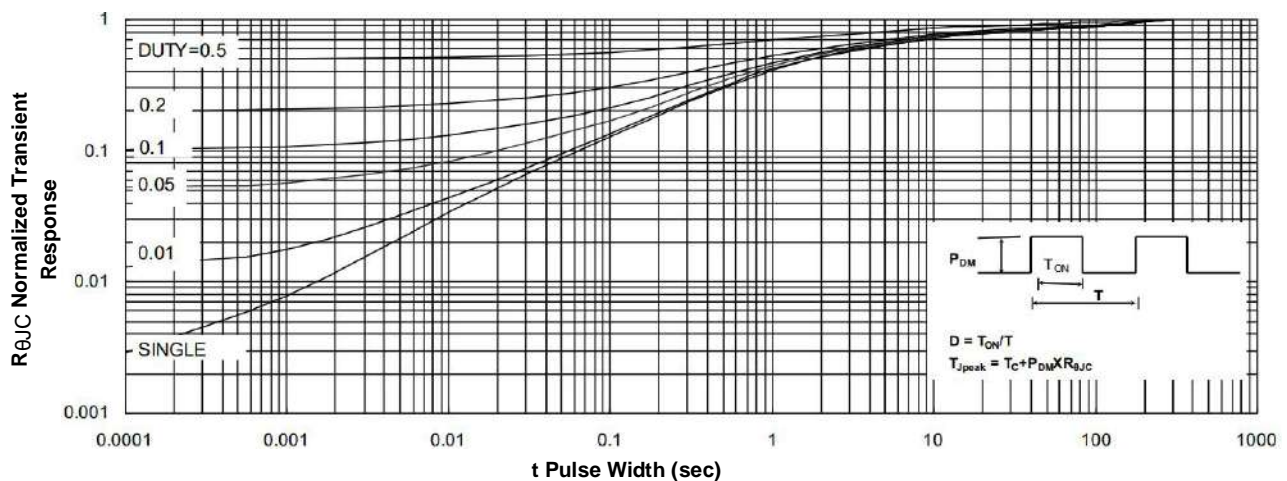


FIG.11-Normalized Maximum Transient Thermal Impedance

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