

P-Channel 60-V (D-S) MOSFET

Description

The MS60P03 is the highest performance P-ch

MOSFETs with super high dense cell design for low

 $R_{DS(ON)}$ and gate charge for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement with full function reliability approved.

Features

Low Reverse Transfer Capacitance

- High Switching Speed
- Improved dv/dt Capability
- Low Gate Charge

Green Device Available

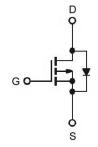
- **Typical Applications**
 - Motor Control
 - Net Working
 - LED Applications

Package type : SOT-23

Packing & Order Information 3,000/Reel

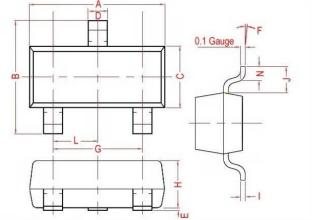


RoHS Compliant



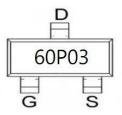
Graphic Symbol

Package Dimension



REF. A B C	Millimeter		DEE	Millimeter	
	Min.	Max.	REF.	Min.	Max.
A	2.70	3.10	G	1.90	Ref.
В	2.30	3.00	Н	0.90	1.30
С	1.20	1.75	L	0.05	0.21
D	0.30	0.50	J	0.58	Ref.
E	0.01	0.15	L	0.95 Typ.	
F	0°	10°	N	0.20 Min.	

Marking





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MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (unless otherwise specified)					
Symbol	Parameter	Value	Units		
V _{DS}	Drain-Source Voltage	-60	V		
V _{GS}	Gate-Source Voltage	±20	V		
T	Continuous Drain Current ¹ (T _A =25°C)	-3	А		
ID	Continuous Drain Current ¹ (T _A =70°C)	-2.3	А		
IDM	Pulsed Drain Current ^{1,2} ($T_A = 25^{\circ}C$)	-12	А		
PD	Power Dissipation ³ ($T_A = 25^{\circ}C$)	2	W		
TJ/TSTG	Operating Junction and Storage Temperature	-55 to +150	°C		

Thermal Resistance Ratings					
Symbol	Parameter	Maximum	Units		
R _{0JA}	Maximum Junction-to-Ambient ¹	125	°C/W		
R _θ JC	MaximumJunction-to-Case ¹	80	°C/W		

Electrical Characteristics ($T_J = 25^{\circ}$ C unless otherwise specified)							
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units	
$V_{GS\;(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	-1.0	-	-2.5	V	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250 \mu A$	-60	-	-	V	
I _{GSS}	Gate-Source Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA	
I _{DSS}	Drain-Source Leakage Current	$V_{DS} = -48V, V_{GS} = 0V, T_J = 25^{\circ}C$	-	-	-1	μΑ	
		$V_{DS} = -48V, V_{GS} = 0V, T_J = 125^{\circ}C$			-5		
RDS (on)	Static Drain-Source On-Resistance ²	$V_{GS} = -10V, I_D = -2A$	-	120	140	mΩ	
		$V_{GS} = -4.5 V, I_D = -1.5 A$	-	150	200	11152	
V _{SD}	Diode Forward Voltage ²	$I_S = -1.0A$, $V_{GS} = 0V$, $T_J = 25^{\circ}C$	-	-	-1.2	V	
Is	Continuous Source Current ^{1,4} (Diode)	$V_G = V_D = 0V$, Force Current	-	-	-3.2	Α	



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Dynamic and switching Characteristics						
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Qg	Total Gate Charge ²	$V_{DS} = -20V$		5.9		
Qgs	Gate-Source Charge	I _D =-2A		2.9		nC
Qgd	Gate-Drain Charge	V _{GS} =-4.5V		1.8		
td(on)	Turn-On Delay Time ²	$V_{DD}=-12V$		10		
tr	Rise Time	I _D =-1A		17		
td(off)	Turn-Off Delay Time	$V_{GS} = -10V$		22		ns
tf	Fall Time	$R_G = 3.3\Omega$		21		
C _{ISS}	Input Capacitance	V _{DS} =-15V		715		
Coss	Output Capacitance	$V_{GS} = 0V$		51		pF
Crss	Reverse Transfer Capacitance	f =1.0MHz		34		

Notes

1. Surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2. The data tested by pulsed, pulse width \leq 300us, duty cycle \leq 2%.

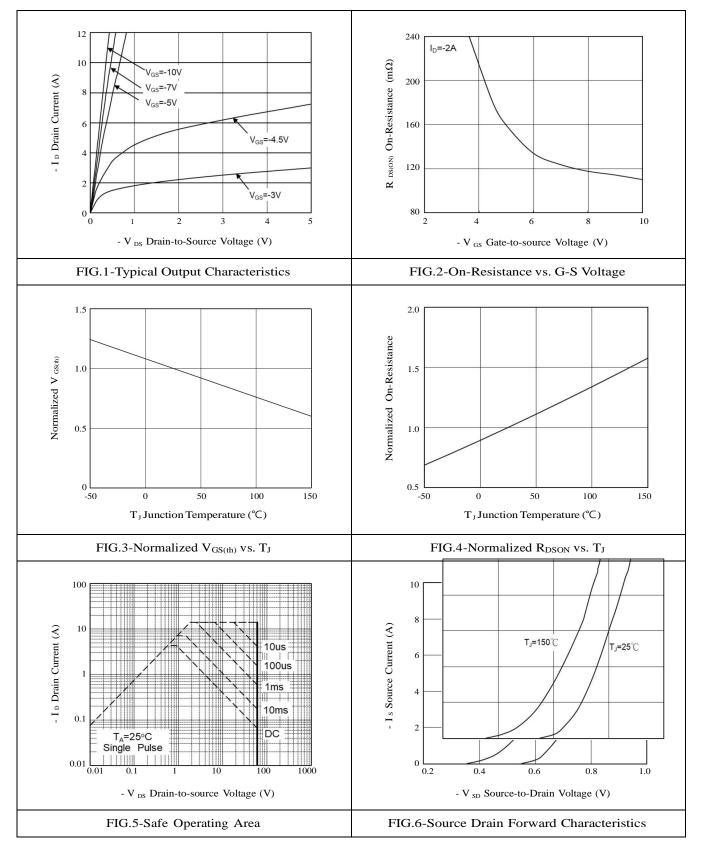
3. The power dissipation is limited by 150°C junction temperature.

 $4. \quad \mbox{The data is theoretically the same as } I_D \mbox{ and } I_{DM} \mbox{, in real applications, should be limited by total power dissipation.}$



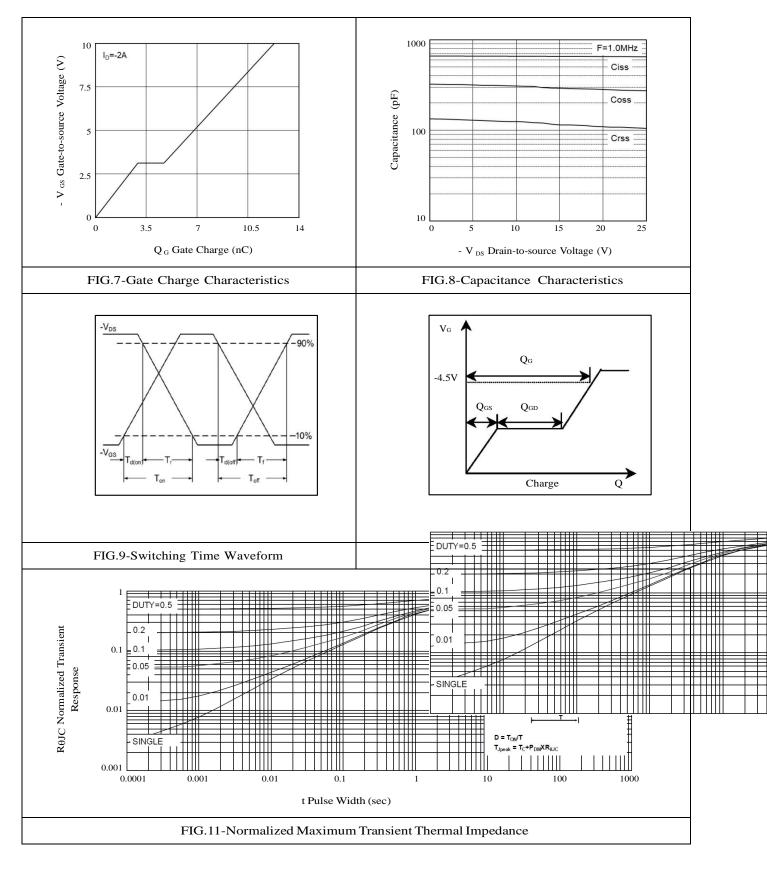
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Typical Electrical Characteristics





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