

Features

- Uses MOT advanced double trench technology
- Low On-Resistance ($R_{DS(on)} \leq 2.4\text{m}\Omega$)
- Low Gate Charge
- Low Reverse transfer capacitances
- 100% avalanche tested
- Pb-free plating; RoHS compliant

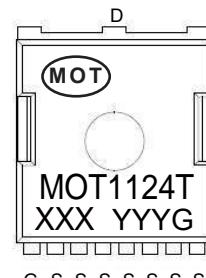
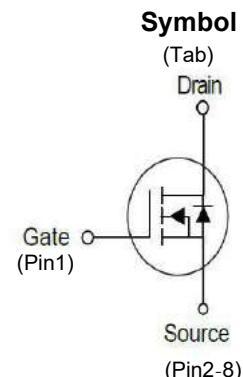
Applications

- Battery management
- Motor control and drive
- Synchronous rectification
- Switching applications

Pin configuration (Top view)



TOLL-8



G S S S S S S
 XXX=Lot Number
 YYY=Year Week
 G=V_{th} Range

Marking

Parameter	Value	Unit
V_{DS}	100	V
$R_{DS(on)}$,typ.	2.2	$\text{m}\Omega$
I_D	246	A

Ordering information

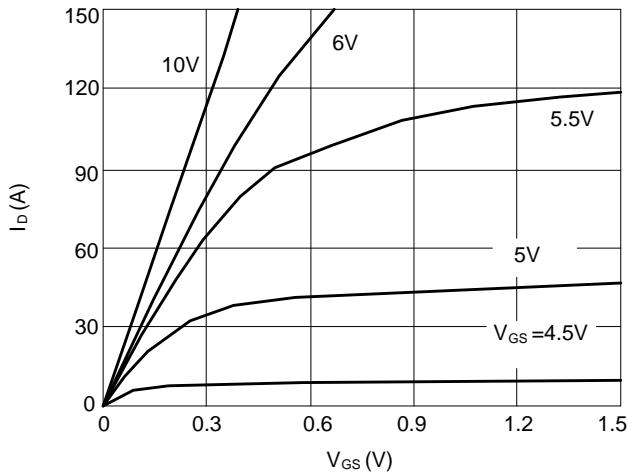
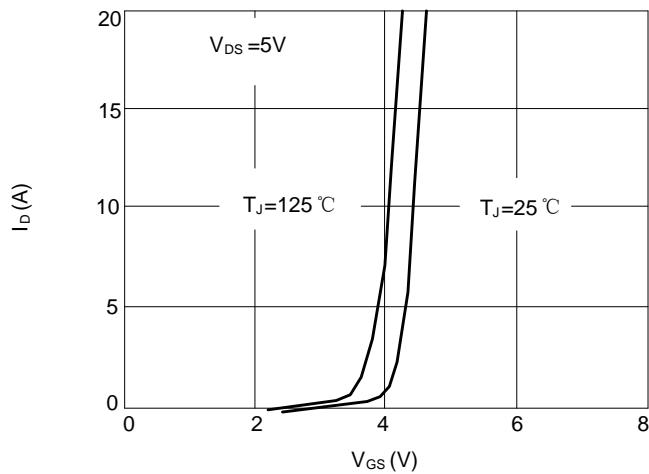
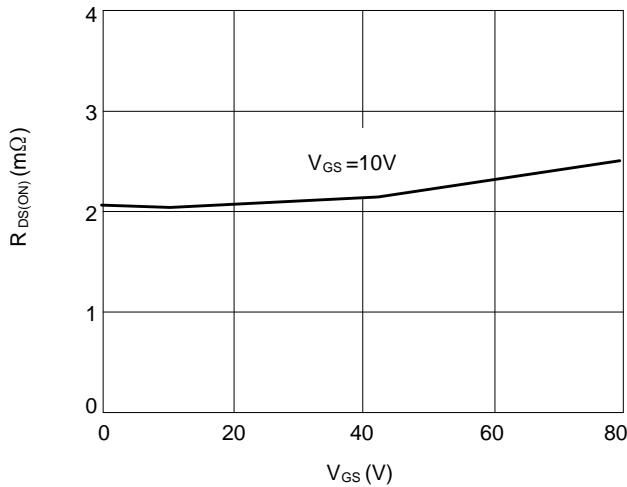
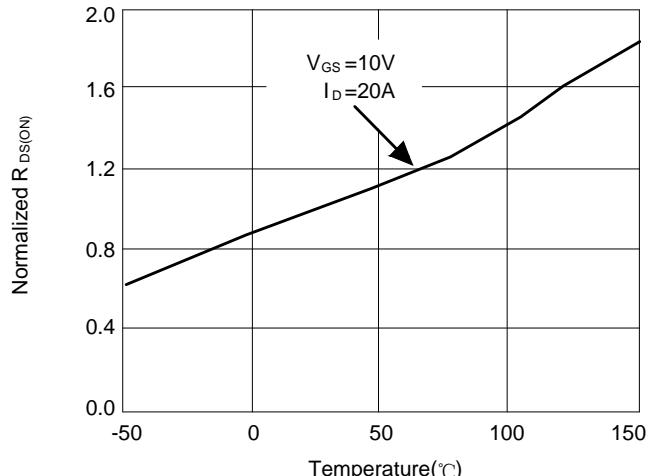
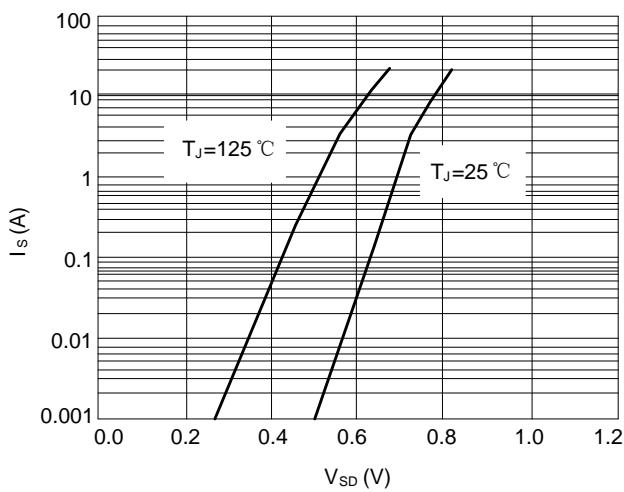
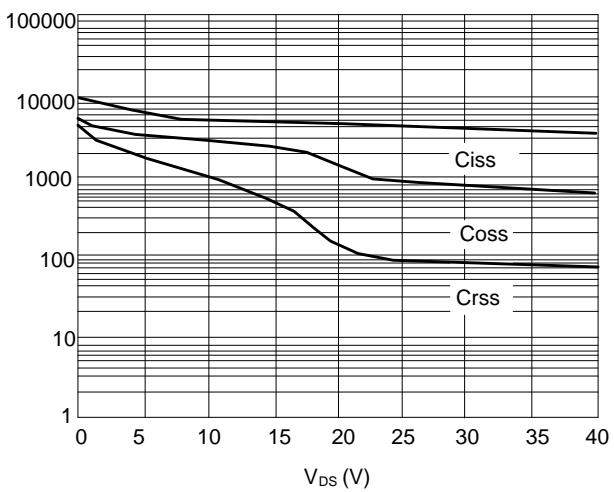
Type/Ordering Code	Package	Marking	Packing&Qty.(pcs)
MOT1124T	TOLL-8	MOT1124T	2000/Reel

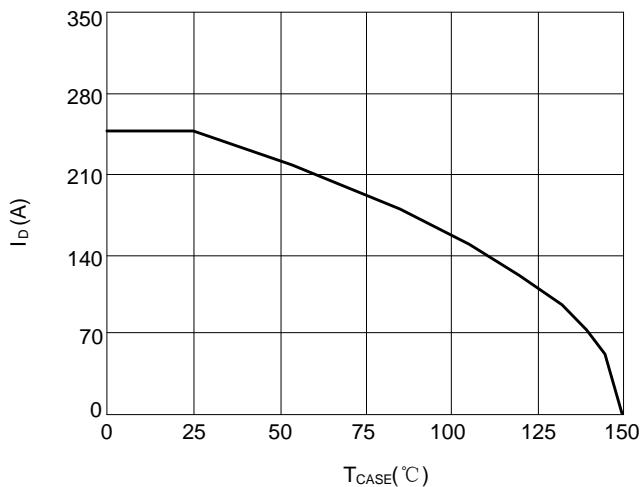
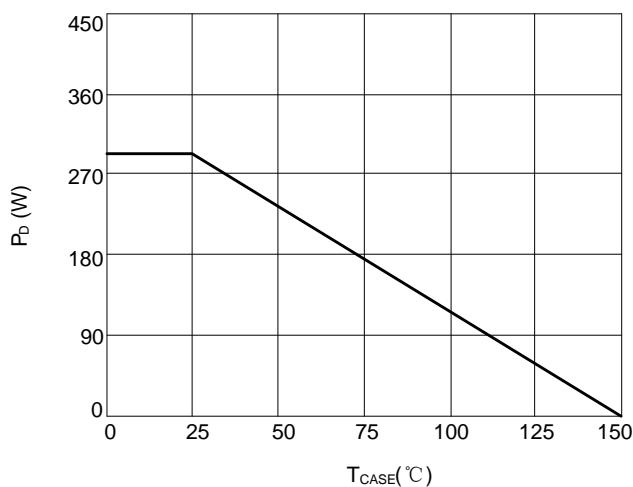
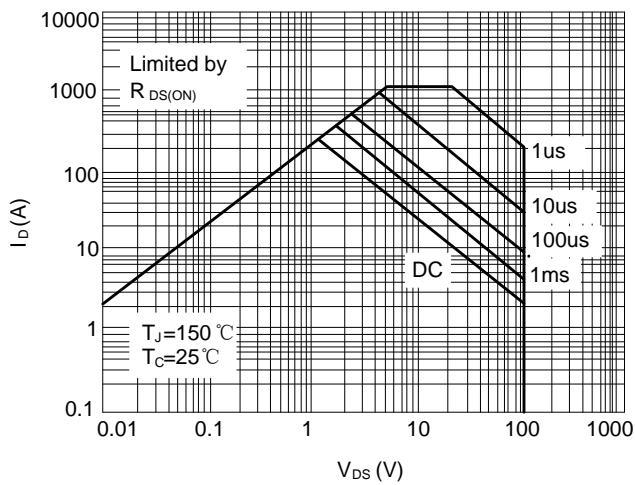
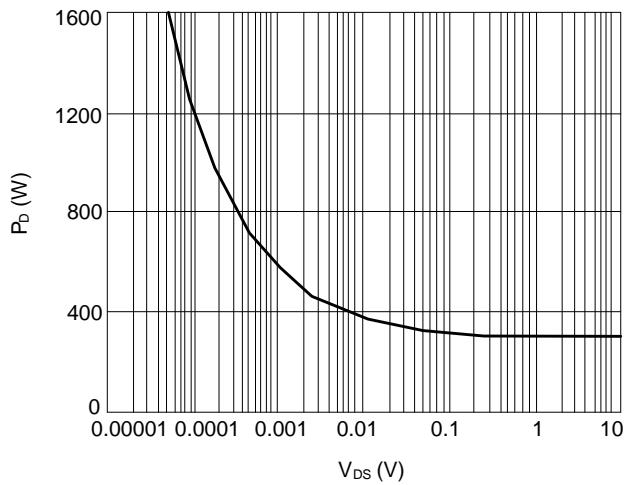
■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

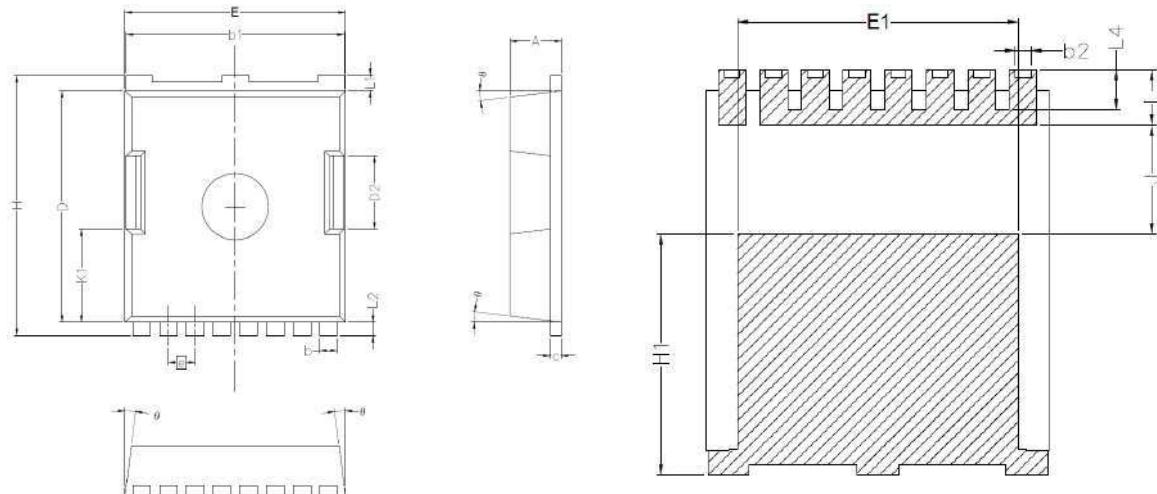
Parameter		Symbol	Value	Unit
Drain-to-Source Voltage		V _{DS}	100	V
Gate-to-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _C = 25°C	I _D	246	A
	T _C = 100°C	I _D	156	A
Pulsed Drain Current		I _{DM}	984	A
Avalanche Energy		E _{AS}	1560	mJ
Power Dissipation	T _C = 25°C	P _D	290	W
	T _C = 100°C	P _D	116	W
Thermal Resistance, Junction-to-Case		R _{θJC}	0.43	°C/W
Junction & Storage Temperature Range		T _J , T _{STG}	-55 to 150	°C

■ ELECTRICAL CHARACTERISTICS ($T_C=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Static parameter						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ$	-	-	1.0	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2.0	2.8	4.0	V
Static Drain-Source ON-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	2.2	2.4	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{DS} = 5\text{V}, I_D = 5\text{A}$	10	-	-	S
Diode Forward Voltage	V_{SD}	$I_S = 1\text{A}, V_{GS} = 0\text{V}$	-	0.66	1.0	V
Diode Continuous Current	I_S	$T_C = 25^\circ\text{C}$	-	-	246	A
Dynamic parameter						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 20\text{V}, f = 1\text{MHz}$	-	9100	-	pF
Output Capacitance	C_{oss}		-	2900	-	pF
Reverse Transfer Capacitance	C_{rss}		-	284	-	pF
Gate Resistance	R_g	$V_{GS} = 0\text{V}, V_{DS} = 0\text{V}, f = 1\text{MHz}$	-	0.7	-	Ω
Switching parameter						
Total Gate Charge (@ $V_{GS} = 10\text{V}$)	Q_g	$V_{GS} = 0 \text{ to } 10\text{V}$ $V_{DS} = 50\text{V}, I_D = 20\text{A}$	-	131	-	nC
Total Gate Charge (@ $V_{GS} = 6.0\text{V}$)	Q_g		-	83	-	nC
Gate Source Charge	Q_{gs}		-	46	-	nC
Gate Drain Charge	Q_{gd}		-	27	-	nC
Turn-On DelayTime	$t_{D(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 50\text{V}$ $R_L = 2.5\Omega, R_{\text{GEN}} = 3\Omega$	-	33	-	ns
Turn-On Rise Time	t_r		-	33	-	ns
Turn-Off DelayTime	$t_{D(off)}$		-	63	-	ns
Turn-Off Fall Time	t_f		-	23	-	ns
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	-	91	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	250	-	nC

■ TYPICAL CHARACTERISTICS

Figure 1: Saturation characteristics

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Figure 3: $R_{DS(ON)}$ vs. drain current

Figure 4: $R_{DS(ON)}$ vs. junction temperature

Figure 5: Body diode characteristics

Figure 6: Capacitance characteristics

■ TYPICAL CHARACTERISTICS(Cont.)

Figure 7: Current de-rating

Figure 8: Power de-rating

Figure 9: Maximum safe operating area

Figure 10: Single pulse power rating junction-Case

■TOLL-8L PACKAGE OUTLINE DIMENSIONS


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	2.20	2.40
b	0.70	0.90
b1	9.70	9.90
b2	0.42	0.50
c	0.40	0.60
D	10.28	10.58
D2	3.10	3.50
E	9.70	10.10
E1	7.90	8.30
e	1.20BSC	
H	11.48	11.88
H1	6.75	7.15
N	8	
J	3.00	3.30
K1	3.98	4.38
L	1.40	1.80
L1	0.60	0.80
L2	0.50	0.70
L4	1.00	1.30
θ	4°	10°

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