

Airvu Filter Series

TAOGLAS. DBP5200XA60

Part No: DBP.5200.X.A.60

Description:

Dielectric Band Pass Filter for 5.2 GHz Bandwidth 100MHz

Features:

Center Frequency 5200MHz Bandwidth 100MHz Low Insertion Loss Band Pass-Band Ripple High Ultimate Attenuation Dimensions: 8.7*4*3 mm RoHS & Reach Compliant



1.	Introduction	3
2.	Specifications	4
3.	Characteristics	5
4.	Mechanical Drawing	6
5.	Recommended Reflow Solder Profile	7
6.	Environmental Reliability	8
7.	Other	10
8.	Packaging	11
	Changelog	13

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1. Introduction



Taoglas are utilizing their deep understanding of the RF component design and manufacturing process to provide high-quality, small-form-factor, cost-effective and easy to implement RF filters, known as the Airvu Filter Series. The Taoglas Filters Division will feature a range of off- the-shelf filters for a variety of applications, including filters for emerging license free bands used for WLAN 802.11 5.2 GHz applications. We can also work with customers to develop bespoke filter solutions.

Taoglas dielectric filters are designed to be used in wireless transmitters or receivers. These filters are designed to protect the Wi-Fi Module from noisy out of band emissions originated from nearby transmitters.

By selecting the proper Taoglas filter you can eliminate unnecessary out of band noise while maintaining minimal in-band insertion loss. The filter is manufactured as a single ceramic block [monoblock] which provides high reliability, low insertion loss and high attenuation in a simple compact SMD package.

The DBP.5200.X.A.60 is a standard Taoglas product but can be customized for specific customer needs. For further information please contact your regional Taoglas customer support team.

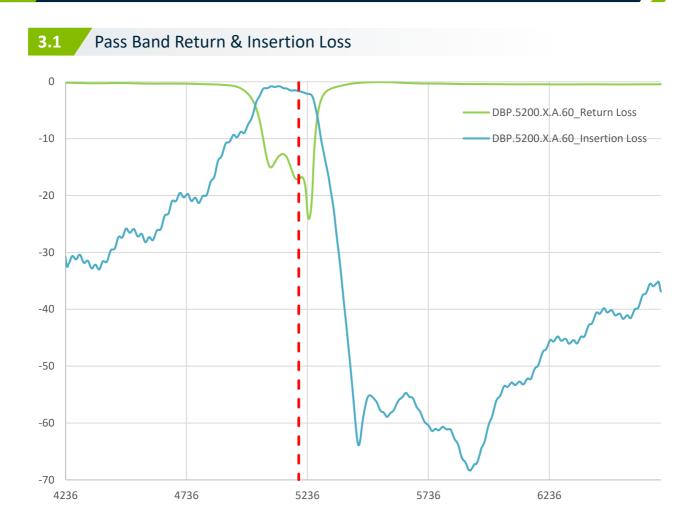


Specifications

	Electrical
Centre Frequency (MHz)	5200MHz
Bandwidth	fo ± 50 [5150~5250] 100MHz
Insertion Loss	2.5 dB max
Passband Ripple	1.3 dB max
Return Loss	< -11 dB
Attenuation	≥30dB (at 5376 MHz) ≥20dB at 5350~5370 MHz ≥30dB at 5370~5850 MHz ≥20dB (at 4600 MHz)
In/Out Impedance	50 Ω
Power Dissipation	1.0 W min.
	Mechanical
Dimensions	8.6*3.9*2.85 (L*W*H)
Material	Ceramic
Finish	Ag plated
	Environmental
Operating Temperature	-40°C to 85°C
Storage Temperature	-40°C to 85°C
Moisture Sensitivity Level (MSL)	3 (168 Hours)



Characteristics

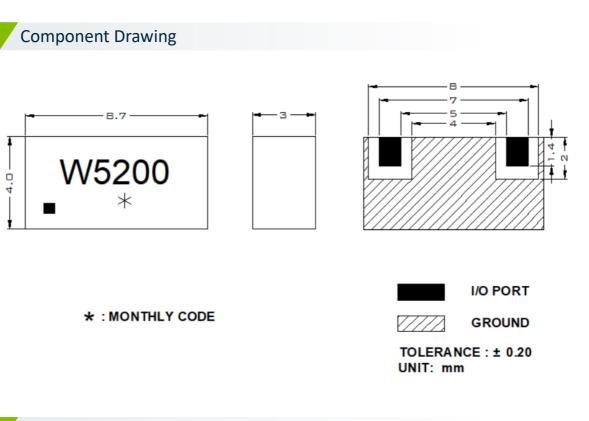






4.1

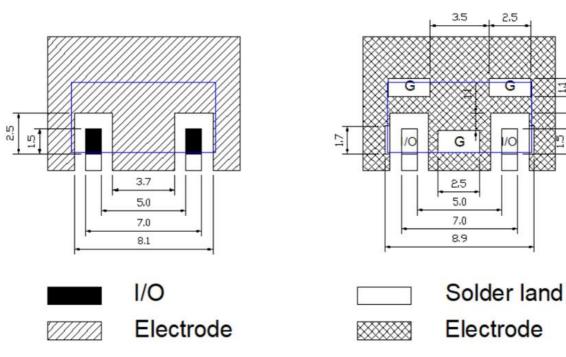
Mechanical Drawing



4.2 Recommended PCB Layout

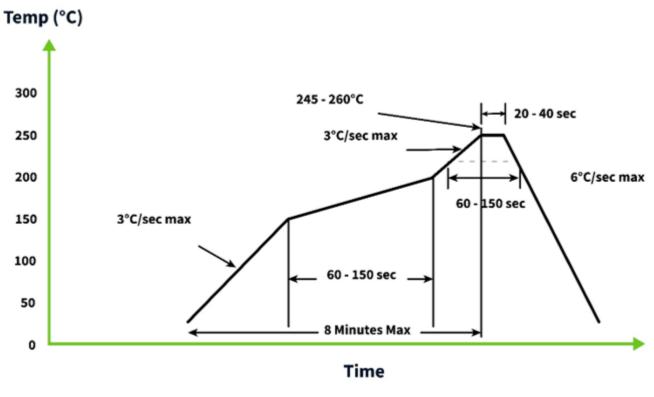
Note: Test PCB material: FR4 ε r 4.4;

T=0.6mm





The DBP.5200.X.A.60 can be assembled by following the recommended soldering temperatures are as follows:



*Temperatures listed within a tolerance of +/- 10º C

Smaller components are typically mounted on the first pass, however, we do advise mounting the DBP.5200.X.A.60 when placing larger components on the board during subsequent reflows.

Note: Soldering flux classified ROLO under IPC J-STD-004 is recommended.

SPE-20-8-047-C



6.1. STORAGE CONDITIONS:

6.

- 6.1.1. Use the product of former delivery first.
- 6.1.2. Temperature: 0°C to 40 °C Humidity: 80%RH or less
- 6.1.3. The product should not be stored exceeding six months (as packed by Taoglas) or one month (once unpacked). Use the product within that period.

6.2. STANDARD CONDITION

This standard shall satisfy the condition of Table 1 after the following test 6.2.

6.3. TEST METHOD

The filter shall withstand the following test condition.

6.3.1. Low Temperature Hold Test :-40°C

Unit shall be subjected to the above condition for 500 hours and then be left for more than 2 hours at room temperature.

6.3.2. High Temperature Hold Test:+100°C

Unit shall be subjected to the above condition for 500 hours and then be left for more than 2 hours at room temperature.

6.3.3. Humidity Resistance Test:

Unit shall be subjected to the $85\pm2^{\circ}$ C, $85^{\circ}90\%$ relative humidity for 500 hours and then be left for more than 2 hours at $25\pm5^{\circ}$ C in less than 85% relative humidity.

6.3.4. Vibration Resistance Test:

The vibration frequency shall be varied within 10-60 Hz with the amplitude of 1.5mm for 20 minutes. Devices are applied to vibration in each of three mutually perpendicular planes for 2 hours.

6.3.5. Mechanical Shock Resistance Test:

A half sine wave shock with a maximum acceleration of 30 G's and duration of 11 msec. Unit is applied in six directions at right angles to each other by three shocks in each direction.

6.3.6. Thermal Shock Resistance Test:

After the unit is applied to thermal shock $-40^{\circ}C(30min) \Leftrightarrow +25^{\circ}C(within 5min) \Leftrightarrow +100^{\circ}C(30min) \Leftrightarrow +25^{\circ}C(within 5min)$ soak at each temperature with transition time less than 10 seconds for 350 cycles and then be left for more than 1 to 2 hour at $25\pm5^{\circ}C$ in less than 65% relative humidity.

6.3.7. Lead Pins Heat Resistance Test: (L type and M type series)

After the lead pins of the unit are soaked in solder bath at $270 \pm 10^{\circ}$ C for 5 seconds and then be left for more than 1 hour at $25\pm5^{\circ}$ C in less than 65% relative humidity.



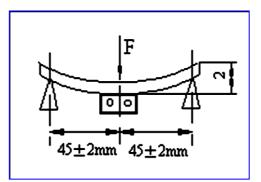
6.3.8. Adhesion Test:

The device is subjected to be soldered on test PCB. Then apply 0.5Kg(5N) of force for 10 ± 1 seconds in the direction of arrow. (the soldering should be done by reflow and be conducted with care so that the soldering is uniform and free of defect by stress such as heat shock)



6.3.9. Bending Resist Test

Weld the product to the center part of the PCB with the thickness 1.6±0.2mm as the illustration shows, and keep exerting force arrow-ward on it at speed of :1mm/S, and hold for 5±1S at the position of 1.5mm





Other



7.1. IN CASE OF ANY PROBLEM REGARDING THIS SPECIFICATION, BOTH CUSTOMER AND Taoglas SHALL DISCUSS AND SOLVE IT.

7.2. INSTALLING A DIELECTRIC FILTER / DUPLEXER:

7.2.1. How to install dielectric filter/duplexer:

Taoglas recommend reflow process for the installation of dielectric filter/duplexer onto PCB. Soldering the dielectric filter/duplexer onto a PCB with a hand soldering iron is not recommended.

7.2.2. Connecting I/Os:

The edge of metal shield, if any, shall be soldered to the electrode of PCB board by reflow process. Tx, Ant, and Rx portion of the PCB shall have either via hole or through hole.

I/Os of the filter/duplexer shall be connected to 50- ohm strip lines that pass through these holes to the backside of the PCB. The 50- ohm strip lines from the duplexer shall be connected to the next circuit element.

It is important to place the filter/duplexer on one side and the connecting strip lines on the other side in order to avoid the interferences by the signal radiation from the strip lines. Such interferences may change the duplexer performance.

7.2.3. Solder and resist area:

Solder and resist area shall be prepared according to referenced Land pattern for each part number provided by Taoglas. The Referenced Land Pattern may be found in Taoglas's Product Catalog. The solder area under the filter/duplexer should be divided into smaller patches with narrow strips of resist area. This is to help the evacuation of the gasses generated from the solder paste during reflow process.

7.3. FILTER REPLACEMENT:

7.3.1. Removal of filters:

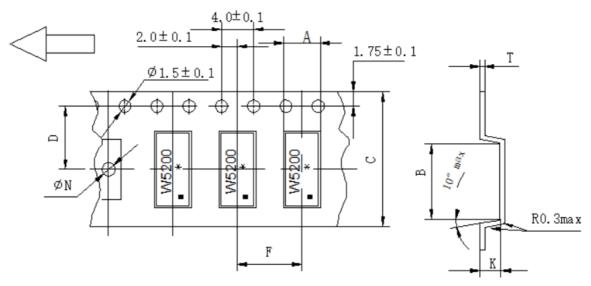
If filter replacement is necessary, it is recommended that a directed source hot air repair station be used. Use tweezers to handle the filter. Do not expose the filter to temperatures in excess of 220 °C as damage may occur making postmortem difficult.

7.3.2 Installing Replacement Filter:

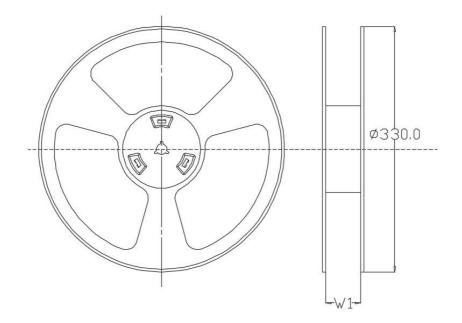
It is helpful that the filter be preheated to 180 °C before reflow. Pretinning the filter with a soldering iron is not recommended due to possible damage generated by a concentrated heat source. Use tweezers to handle the filter. Do not place material handling devices inside of the resonator holes as filter damage may occur. The use of the same silver bearing solder paste is recommended for filter rework or replacement. As with the initial reflow, replacement filters should not be exposed to temperatures in excess of 215 °C. Disclaimer: The center of this section is presented only as the recommendation or the reference for the use and installation of Taoglas dielectric ceramic filter/duplexer. Since Taoglas does not know any particular conditions, circumstances, and environment of each customer, Taoglas does not take any responsibility for the consequences caused by use out of this specification document. The readers of this document should examine all possible technical issues regardless of being discussed in this document.



Packaging



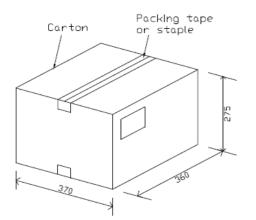
				J	尺寸标	记			
编	뮥	Α	В	С	D	F	ØN	К	Т
16mmD	FC5G-A	4.6±0.1	9.0±0.2	16.0 ± 0.2	7.5±0.1	8.0±0.1	1.55±0.05	3.3±0.1	0.3±0.05



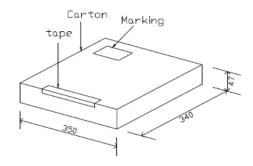
帶寬	$\vee 1$
16mm	16.4±1.0mm



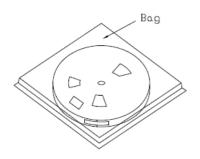
1. Duter Carton Quanyity:5000PCS



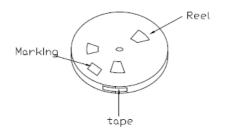
2. Inner Carton Quanyity:1000PCS



3. Bag Quanyity:1000PCS



4. Taping Quanyity:1000PCS



Unitimm



Changelog for the datasheet

SPE-20-8-047 - DBP.5200.X.A.60

Revision: C (Current	Release)
Date:	2023-09-05
Notes:	Updated Solder Reflow Information
Author:	Cesar Sousa

Previous Revisions

R	Revision: B	
	Date:	2021-09-07
	Notes:	Added MSL rating and fixed table fonts.
	Author:	Erik Landi

Revision: A (Orig	al First Release)
Dat	2020-08-12
Note	
Autho	Jack Conroy



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