



栅极驱动光耦 Gate Drive Optocoupler

ACPL-P341(AT)

Product Data Sheet

AOTE DCC
RELEASE

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概述 Description

ACPL-P341(AT) 是一种 输出电流为 3.0A 的栅极驱动光电耦合器，具有一个砷化铝镓 (AlGaAs) 的发光二极管，通过红外光耦合到光敏集成电路。这种光电耦合器可驱动大多数的小功率 IGBTs 和 MOSFETs。在电机控制逆变器以及高性能电力系统应用中，其非常适用于快速切换驱动功率 IGBTs 和 MOSFETs。

The ACPL-P341(AT) is a gate driven optocoupler with an output current of 3.0A, with an AlGaAs LED, which is coupled to a photosensitive integrated circuit through infrared light. This optocoupler can drive most low-power IGBTs and MOSFETs. In the motor control inverter and high-performance power system applications, it is very suitable for fast switching drive power IGBTs and MOSFETs

特性 Features

- 35kV/ μ s 最小共模抑制 ($V_{CM} = 1500V$)
35kV/ μ s minimum Common Mode Rejection ($V_{CM} = 1500V$)
- 宽工作电压范围：15V 至 30V
Wide operating V_{CC} Range: 15V ~ 30V
- 工作温度范围：-40°C to +105°C
Operating temperature range: -40°C ~ +105°C
- 最大传播延迟时间为 200 ns
200ns maximum propagation delay
- 100ns 最大传播延迟差
100ns maximum propagation delay difference
- 3.0A 最大峰值输出电流
3.0A maximum peak output current
- 2.5A 最小峰值输出电流
2.5A minimum peak output current
- 符合加强绝缘标准
Meet reinforced insulation standards

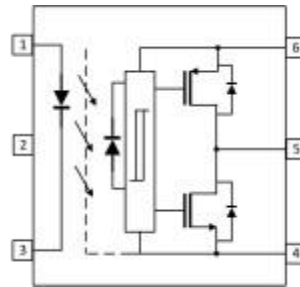
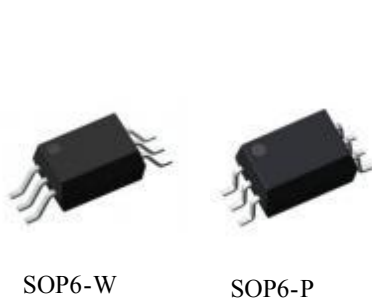
应用 Applications

- 开关电源
Switching power supplies
- IGBT/MOSFET 栅极驱动器
IGBT/MOSFET gate drive
- 交流和无刷直流电机驱动器
AC and Brushless DC motor drives
- 可再生能源逆变器
Renewable energy inverters
- 工业逆变器
Industrial inverters

真值表 Truth table

LED	$V_{CC}-V_{EE}$ " POSITIVE GOING" (TURN-ON)	$V_{CC}-V_{EE}$ "NEGATIVE GOING" (TURN-OFF)	VO
OFF	0 ~ 30V	0 ~ 30V	LOW
ON	0 ~ 12.1V	0 ~ 11.1V	LOW
ON	12.1 ~ 13.5V	11.1 ~ 12.4V	TRANSITION
ON	13.5 ~ 30V	12.4 ~ 30V	HIGH

封装和原理图 Package and Schematic Diagram



Pin Configuration

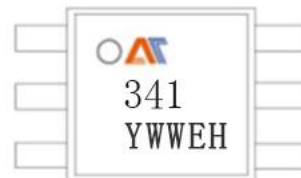
1. Anode
2. NC
3. Cathode
4. VSS
5. VO
6. VDD

注：在引脚 4 和 6 之间必须连接一个 0.1uF 的旁路电容器。

Note: A 0.1uF bypass capacitor must be connected between pins 4 and 6.

印字信息 Marking Information

- 印字中 “” 为奥特品牌 LOGO
“” denotes LOGO
- 印字中 “Y” 代表年份：A(2018), B(2019), C(2020)
“Y” denotes YEAR：A(2018), B(2019), C(2020)
- 印字中 “WW” 代表周号
“WW” denotes Week’ s number
- 印字中 “E” 代表内部代码
“E” denotes Internal code
- 印字中的 “H” 代表无卤，而当产品有卤/无铅时，此处空白
“H” denotes Halogen-free, when the product has halogen/lead-free, leave this blank.



绝缘和安规信息 Insulation and Safety related specifications

项目 Item	符号 Symbol	数值 Value	单位 Unit	备注 Note
爬电距离 Creepage Distance	L	≥ 8	mm	从输入端到输出端，沿本体最短距离路径 Measured from input terminals to output terminals, shortest distance path along body.
电气间隙 Clearance Distance	L	≥ 8	mm	从输入端到输出端，通过空气的最短距离 Measured from input terminals to output terminals, shortest distance through air.
绝缘距离 Insulation Thickness	DTI	≥ 0.4	mm	发射器和探测器之间的绝缘厚度 Insulation thickness between emitter and detector.
峰值隔离电压 Peak Isolation Voltage	V_{IORM}	1500	V_{peak}	DIN/EN/IEC EN60747-5-5.
瞬态隔离电压 Transient Isolation Voltage	V_{IOTM}	7000	V_{peak}	DIN/EN/IEC EN60747-5-5.
隔离电压 Isolation Voltage	V_{ISO}	5000	V_{rms}	$T_A = 25^{\circ}C$, RH < 50% for 1 min.

极限参数 Absolute Maximum Ratings ($T_A = 25^{\circ}C$)

参数 Parameter		符号 Symbol	额定值 Rating	单位 Unit
输入 Input	平均输入电流 Average Input Current	$I_{F(AVG)}$	25	mA
	峰值瞬态输入电流 (<1 μs 脉冲宽度, 300 pps) Peak Transient Input Current (<1 μs Pulse Width, 300 pps)	$I_{F(TRAN)}$	1.0	A
	反向电压 Reverse Voltage	V_R	5	V
	输入功率 Power Dissipation	P_I	45	mW
输出 Output	高峰值输出电流 High Peak Output Current	$I_{OH(PEAK)}$	3	A
	低峰值输出电流 Low Peak Output Current	$I_{OL(PEAK)}$	3	A
	电源电压 Supply Voltage	$V_{CC} - V_{EE}$	0 ~ 35	V
	输出电压 Output Voltage	$V_{O(PEAK)}$	-0.5 ~ V_{CC}	V
	输出功率 Power Dissipation	P_O	700	mW
总功耗 Total Power Dissipation		P_{tot}	745	mW
隔离电压 Isolation Voltage		V_{ISO}	5000	V_{rms}
工作温度 Operating Temperature		T_{opr}	-40 ~ +105	$^{\circ}C$
储存温度 Storage Temperature		T_{STG}	-55 ~ +125	$^{\circ}C$
焊接温度 Soldering Temperature		T_{sol}	260	$^{\circ}C$

推荐操作条件 Recommended Operating Conditions

参数 Parameter	符号 Symbol	最小值 Min	最大值 Max	单位 Unit
电源电压 Power Supply Voltage	$V_{CC}-V_{EE}$	15	30	V
开启电流 Input Current (ON)	$I_{F(ON)}$	7	16	mA
关断电压 Input Voltage (OFF)	$V_{F(OFF)}$	-3.6	0.8	V
工作温度 Operating Temperature	T_A	-40	+105	°C

产品特性参数 Electro-optical Characteristics ($T_A = 25^\circ\text{C}$)

除非另有说明，在推荐的工作条件下测试最小值和最大值。所有典型值在 $T_A = 25^\circ\text{C}$ 、 $V_{DD} = 30\text{V}$ 、 $V_{SS} = \text{GND}$ 测得。
All minimum and maximum specifications are at recommended operating conditions, unless otherwise noted.
All typical values are at $T_A = 25^\circ\text{C}$, $V_{DD} = 30\text{V}$, $V_{SS} = \text{GND}$.

参数 Parameter	符号 Symbol	条件 Condition	最小 Min.	典型 Typ.	最大 Max.	单位 Unit
正向电压 Forward Voltage	V_F	$I_F = 10\text{mA}$	1.2	1.55	1.95	V
反向电压 Reverse Voltage	B_{VR}	$I_R = 10\mu\text{A}$	5	-	-	V
阈值电流从低到高 Threshold Input Current Low to High	I_{FLH}	$R_g = 10\Omega$, $C_g = 25\text{nF}$, $V_O > 5\text{V}$	-	1.5	4	mA
阈值电压从高到低 Threshold Input Voltage High to Low	V_{FHL}	$R_g = 10\Omega$, $C_g = 25\text{nF}$, $V_O > 5\text{V}$	0.8	-	-	V
发射端 Input	低电压锁定阈值 UVLO Threshold	$I_F = 10\text{mA}$, $V_O > 5\text{V}$	12.1	12.8	13.5	V
			11.1	11.8	12.4	
	低电压锁定阈值延迟 UVLO Hysteresis	-	-	1.0	-	V
输入正向电压的温度系数 Temperature Coefficient of Input Forward Voltage	$\Delta V_F / \Delta T_A$	$I_F = 10\text{mA}$	-	-1.7	-	mV/°C
输入电容 Input Capacitance	C_{IN}	$V = 0$, $F = 1\text{kHz}$	-	70	-	pF
接收端 Output	高电平输出电流 High Level Output Current	$V_O = V_{CC} - 4\text{V}$	-1.0	-2.3	-	A
		$V_{CC} - V_O \leq 15\text{V}$	2.5	-	-	
	低电平输出电流 Low Level Output Current	$V_O = V_{EE} + 2.5\text{V}$	1.0	3.0	-	A
		$V_O - V_{EE} \leq 15\text{V}$	2.5	-	-	
	高输出晶体管电阻 (开启) High Output Transistor Resistance	$I_{OH} = -2.5\text{A}$	-	1.7	3.0	Ω

参数 Parameter		符号 Symbol	条件 Condition	最小 Min.	典型 Typ.	最大 Max.	单位 Unit
	低输出晶体管电阻 (开启) Low Output Transistor Resistance	$R_{DS(OL)}$	$I_{OL} = 2.5A$	-	0.8	1.8	V
	高电平输出电压 High Level Output Voltage	V_{OH}	$I_o = -100mA$	$V_{CC}-0.3$	$V_{CC}-0.1$	-	
			$I_f = 10mA, I_o = 0mA$	-	V_{CC}	-	
	低电平输出电压 Low Level Output Voltage	V_{OL}	$I_o = 100mA$	-	0.1	0.2	A
	高电平电源电流 High Level Supply Current	I_{CCH}	$R_g = 10\Omega$ $C_g = 25nF, I_f = 10mA$	-	1.9	3.0	
	低电平电源电流 Low Level Supply Current	I_{CCL}	$R_g = 10\Omega$ $C_g = 25nF, V_f = 0V$	-	1.9	3.0	
传输特性 Transfer Characteristics	逻辑低电平传输延迟 Propagation Delay Time to Low Output Level	T_{PHL}	$V_{CC} = 15V \text{ to } 30V$ $I_f = 7mA \text{ to } 16mA$, $R_g = 10\Omega$ $C_g = 25nF$ $f = 20kHz$ Duty Cycle = 50%	50	95	200	ns
	逻辑低电平传输延迟 Propagation Delay Time to High Output Level	T_{PLH}		50	98	200	ns
	脉冲失真 Pulse Width Distortion	PWD		-	22	70	ns
	传播延迟差 Propagation Delay Difference Between Any Two Parts	PDD		-100	-	100	ns
	上升时间 (10% ~90%) Rise Time(10% ~90%)	T_R		-	43	-	ns
	下降时间 (90% ~10%) Fall Time(90% ~10%)	T_F		-	40	-	ns
	输出高电平共模抑制 Output High Level Common Mode Transient Immunity	$ CM_H $	$T_A = 25^\circ C$, $V_{CM} = 1500V$	35	50	-	kV/ μs
	输出低电平共模抑制 Output Low Level Common Mode Transient Immunity	$ CM_L $		35	50	-	kV/ μs
	隔离电阻 Isolation Resistance	R_{ISO}	$V_{IO} \leq 500V$	10^{12}	-	-	Ω
	隔离电容 Isolation Capacitance	C_{ISO}	$V_{IO} = 0V$, $f = 1MHz$	-	1	-	pF

典型光电特性曲线 Typical Electro-Optical Characteristics Curves

Fig.1 High Output Rail Voltage vs. Ambient Temperature

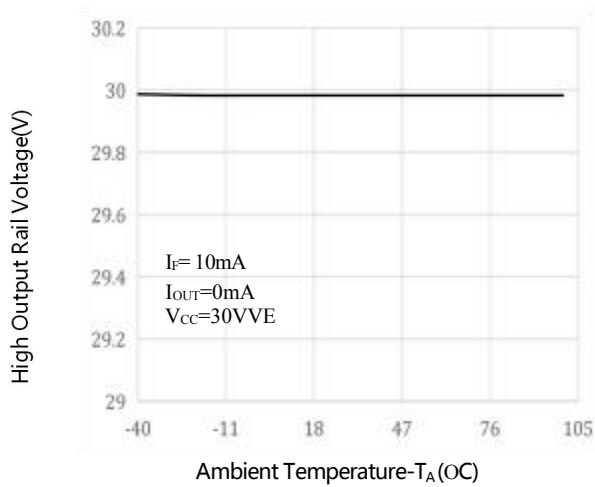


Fig.2 High Level Output Voltage vs. Ambient Temperature

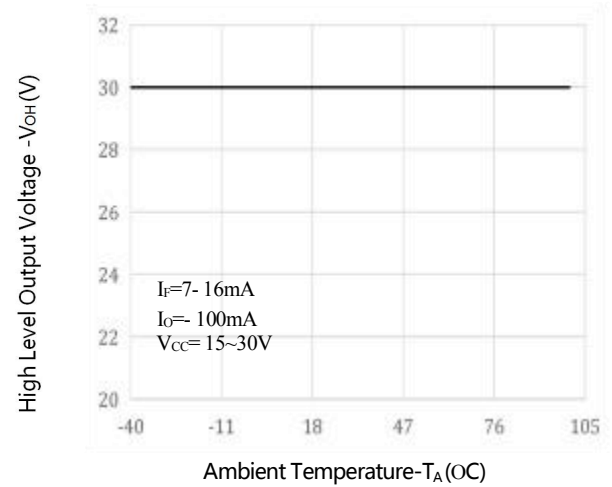


Fig.3 Low Level Output Voltage vs. Ambient Temperature

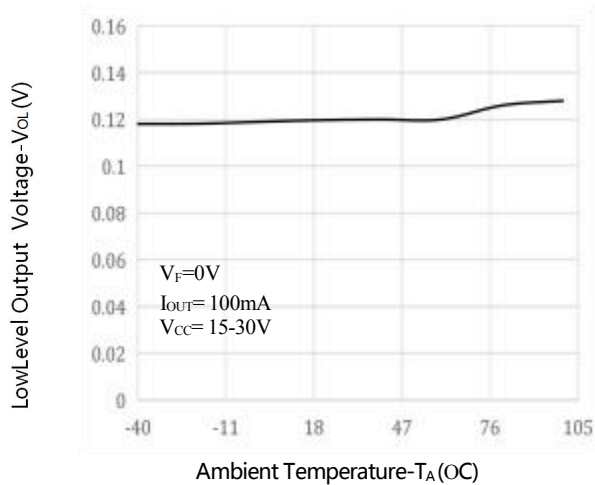


Fig.4 Supply Current vs. Ambient Temperature

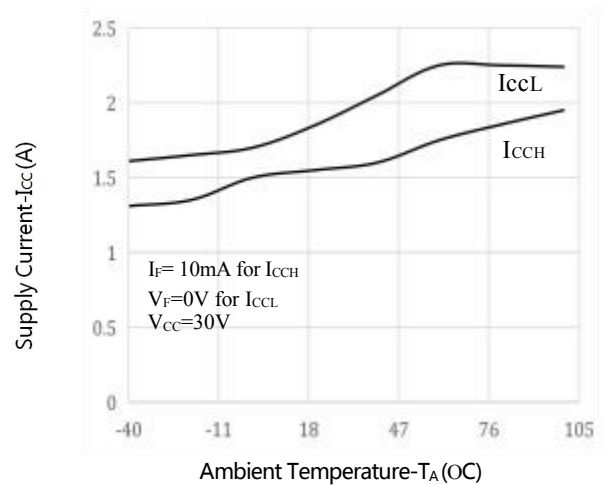


Fig.5 Supply Current vs Supply Voltage

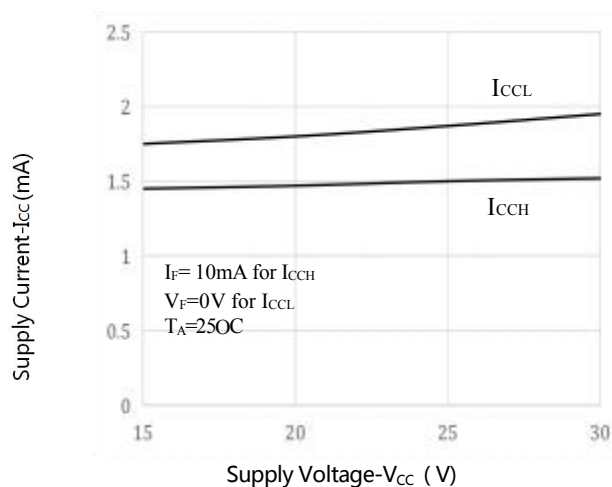


Fig.6 Output Voltage vs Threshold Input Current Low to High

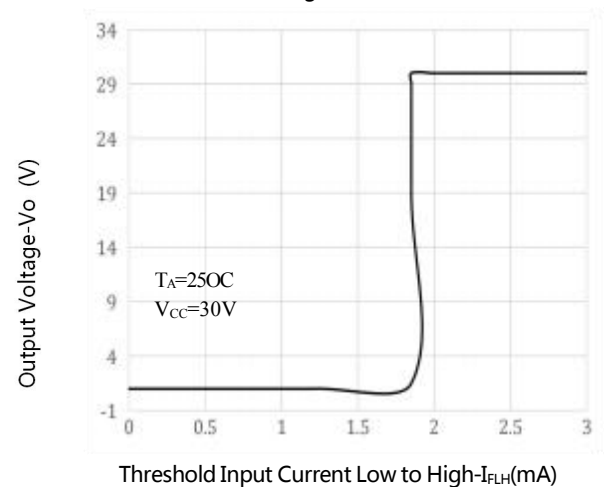


Fig.7 Threshold Input Current Low to High vs Ambient Temperature

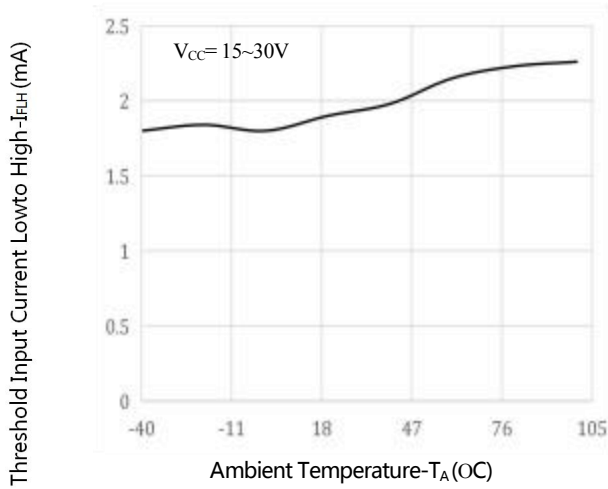


Fig.8 Propagation Delay vs. Supply Voltage

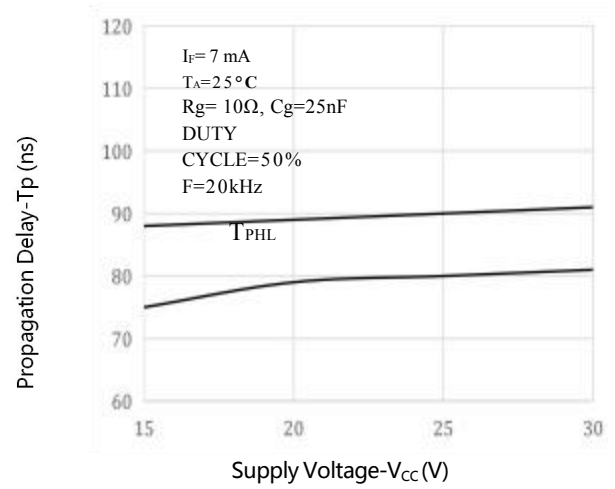


Fig.9 Propagation Delay vs Input Forward Current

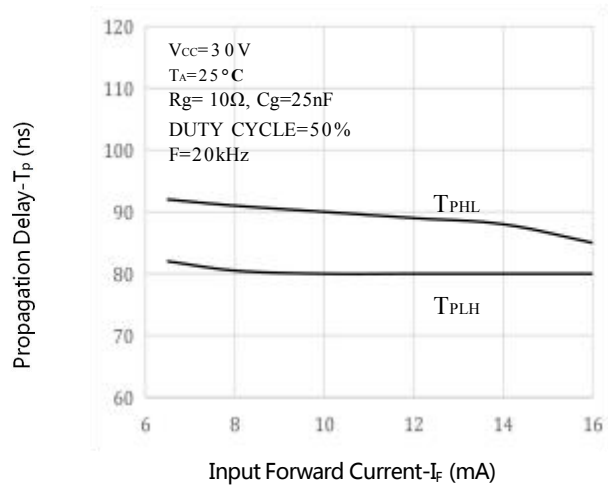


Fig.10 Propagation Delay vs Ambient Temperature

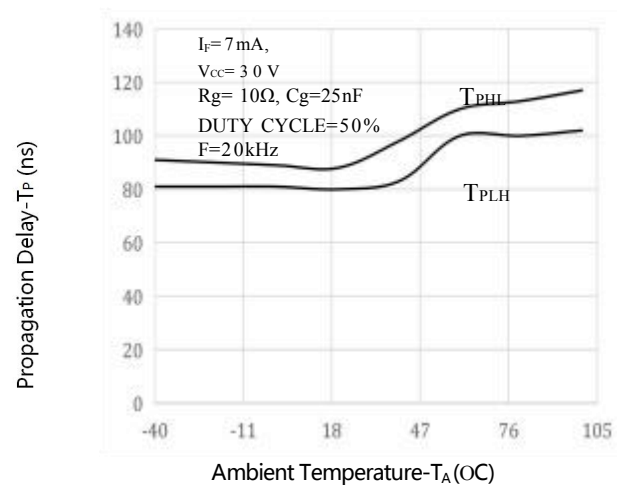


Fig.11 Propagation Delay vs. Load Resistance

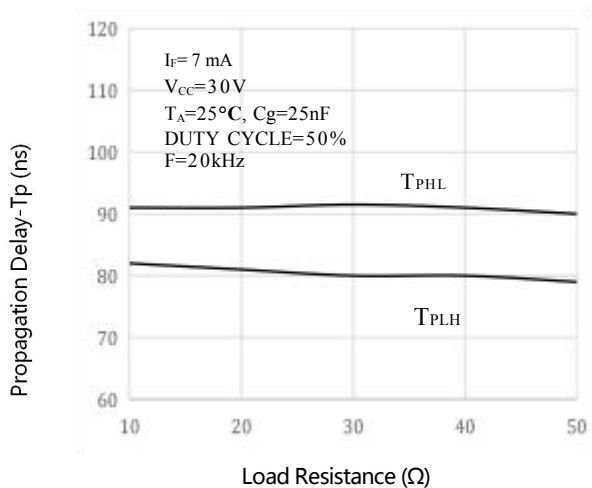


Fig.12 Propagation Delay vs. Load Capacitance

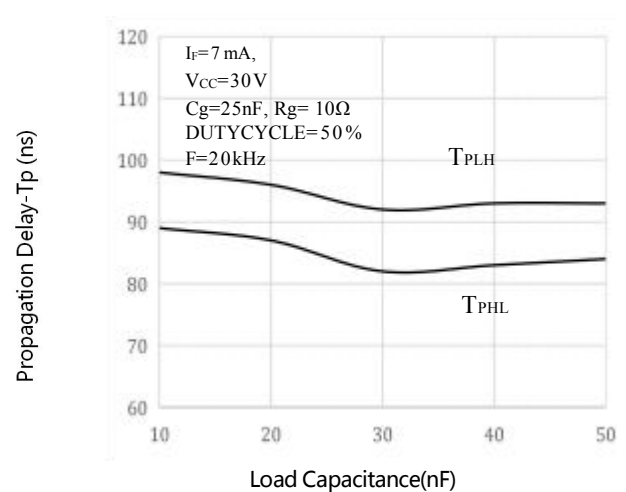
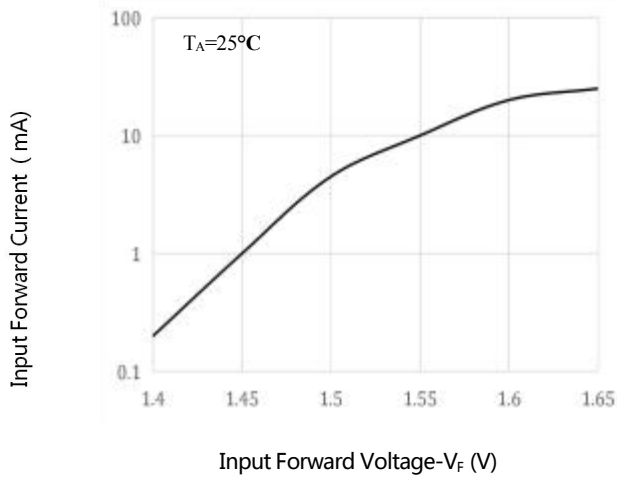


Fig.13 Input Forward Current vs Input Forward Voltage



测试电路 Test Circuits

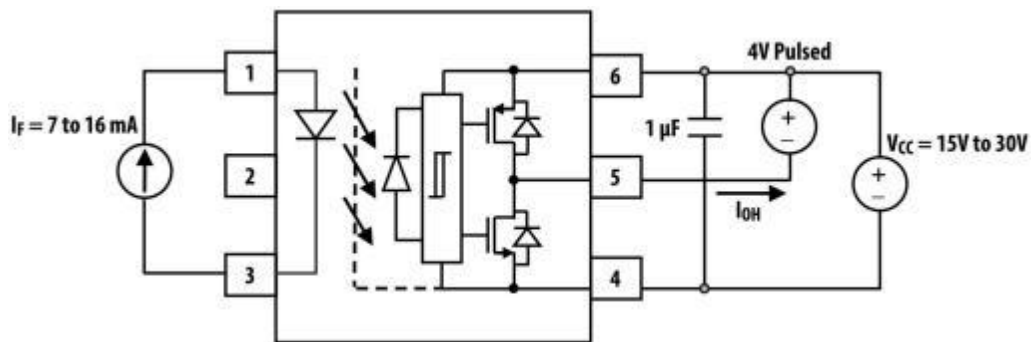


Figure.14 I_{OH} Pulsed Test Circuit

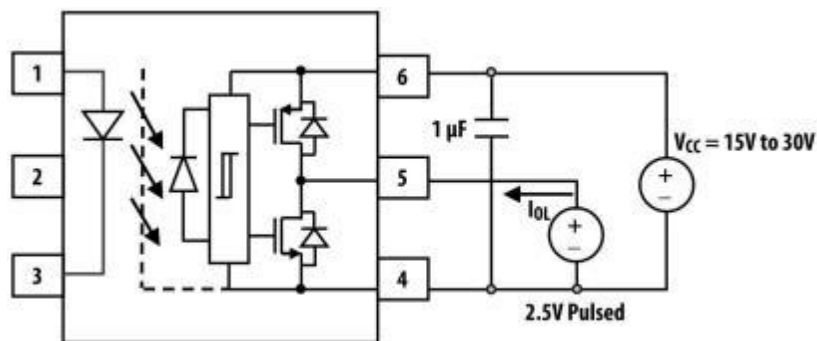


Figure.15 I_{OL} Pulsed Test Circuit

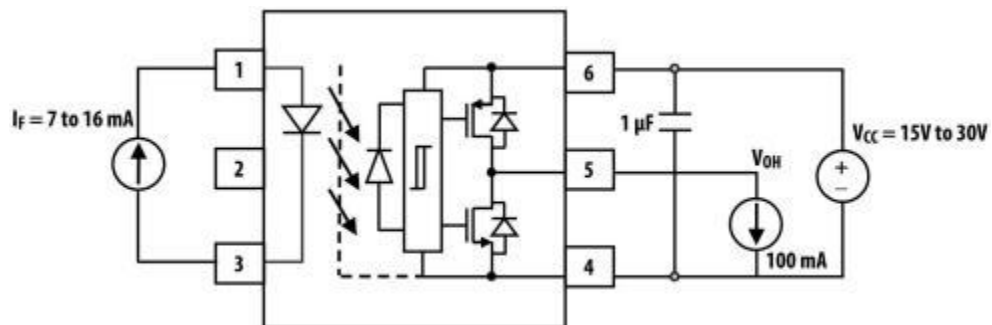


Figure.16 V_{OH} Pulsed Test Circuit

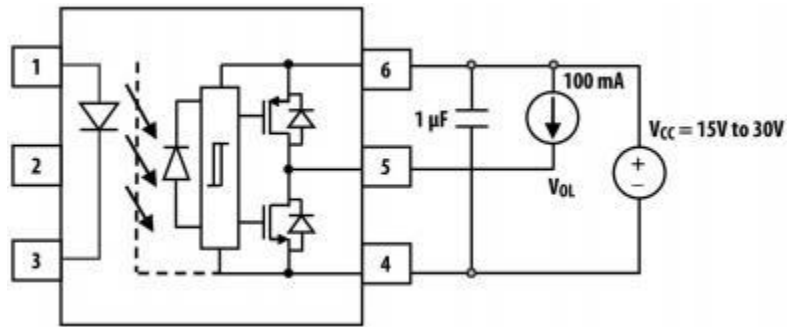


Figure.17 V_{OL} Pulsed Test Circuit

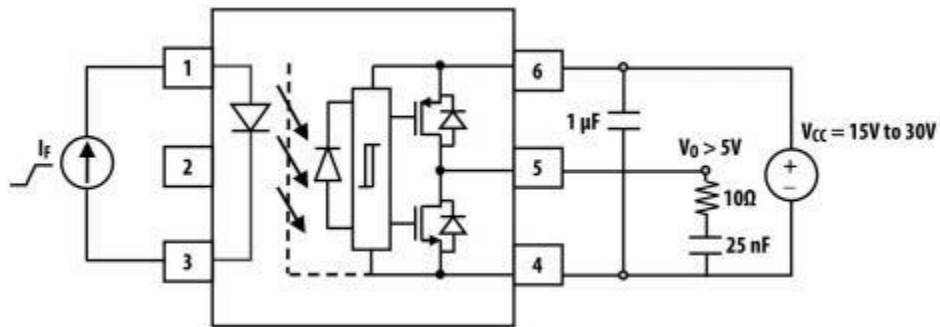


Figure.18 I_{FLH} Test Circuit

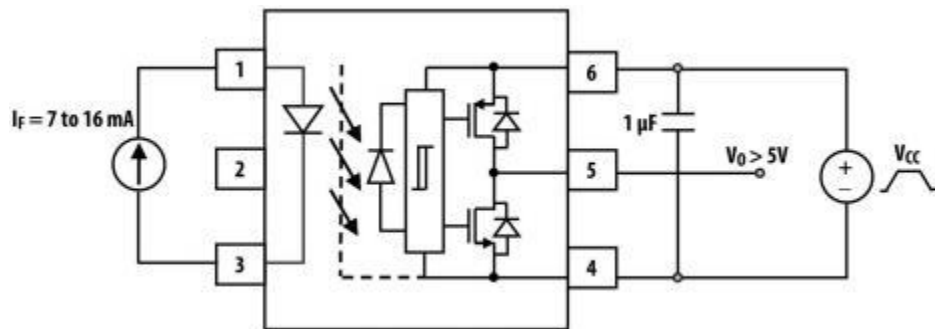


Figure.19 UVLO Test Circuit

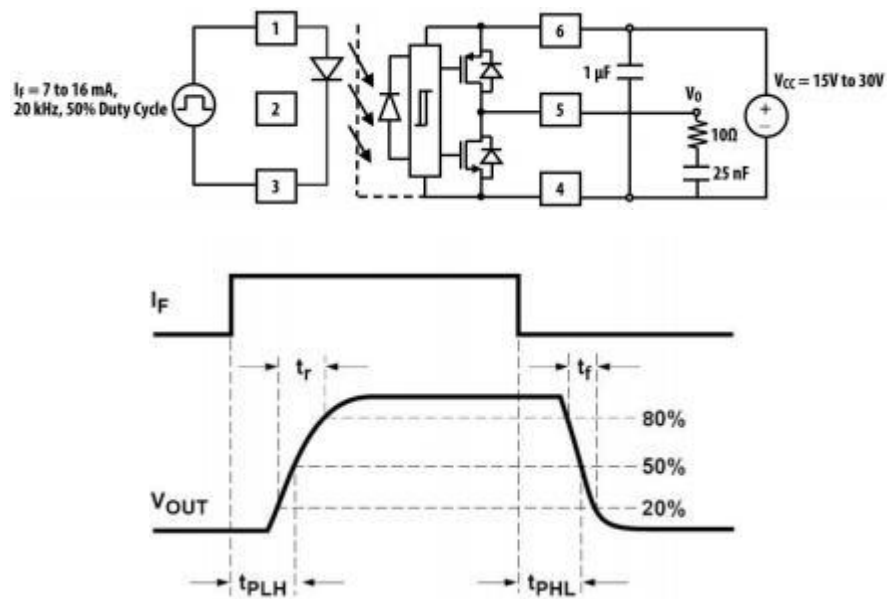


Figure.20 T_{PHL} 、 T_{PLH} 、 T_R 、 T_F Test Circuit

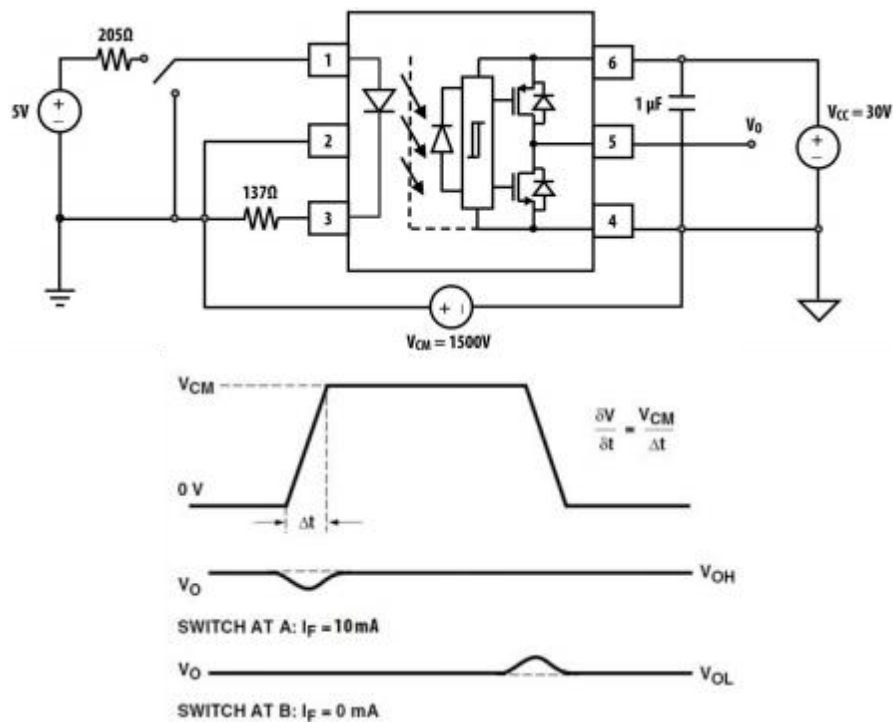
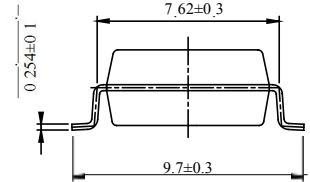
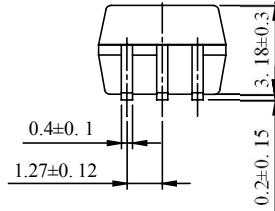
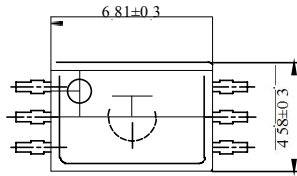


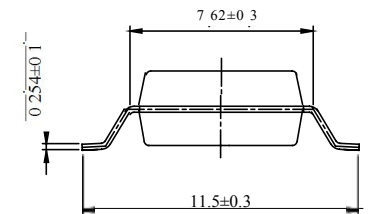
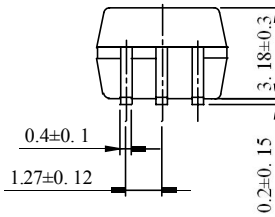
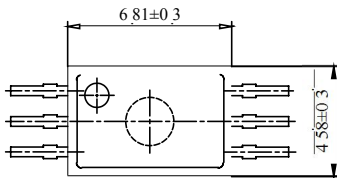
Figure.21 CMR Test Circuit

外形尺寸 Outline Dimensions

SOP6-P

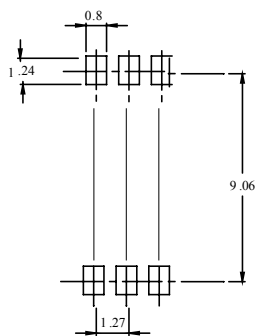


SOP6-W

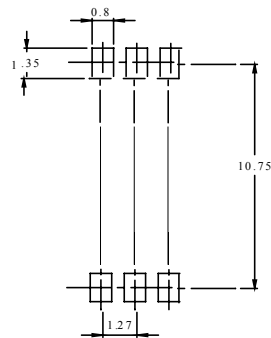


单位 Unit: mm

建议焊盘布局 Recommended Pad Layout



SOP6-P



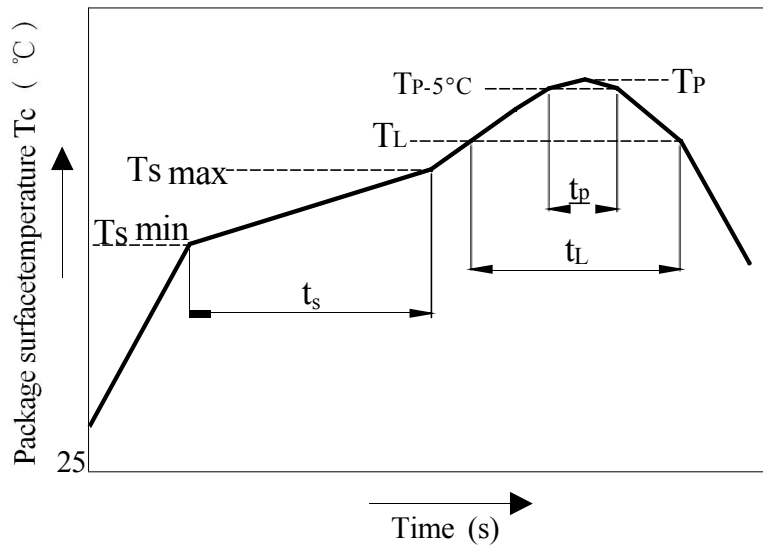
SOP6-W

单位 Unit: mm

注意：上图为产品正视图。

Note: The picture above is the front view of the product.

回流焊温度曲线图 Solder Reflow Profile

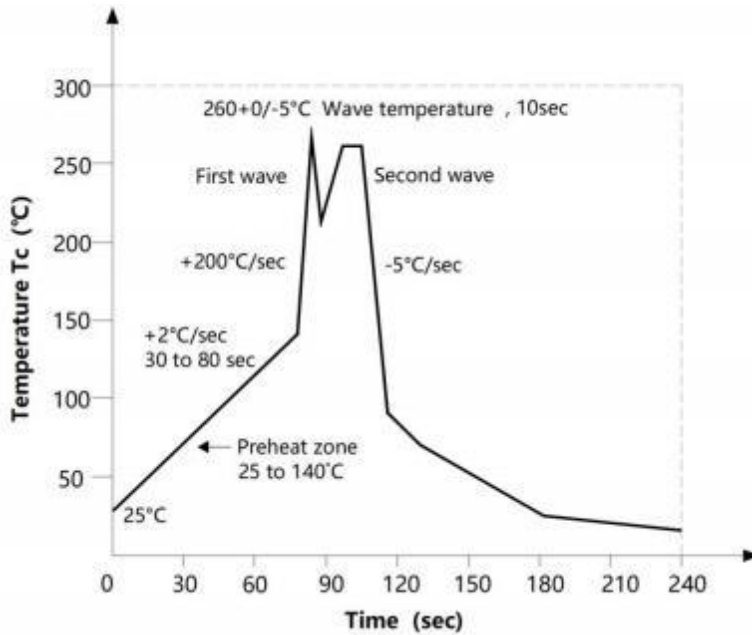


项目 Item	符号 Symbol	最小值 Min.	最大值 Max.	单位 Unit
预热温度 Preheat Temperature	T_s	150	200	$^\circ\text{C}$
预热时间 Preheat Time	t_s	60	120	s
升温速率 Ramp-Up Rate (T_L to T_P)	-	-	3	$^\circ\text{C/s}$
液相线温度 Liquidus Temperature	T_L	217		$^\circ\text{C}$
时间高于 T_L Time Above T_L	t_L	60	150	s
峰值温度 Peak Temperature	T_P	-	260	$^\circ\text{C}$
T_c 在 $(T_P - 5)$ 和 T_P 之间的时间 Time During Which T_c Is Between $(T_P - 5)$ and T_P	t_p	-	30	s
降温速率 Ramp-down Rate (T_P to T_L)	-	-	6	$^\circ\text{C/s}$

注：建议在所示的温度和时间条件下进行回流焊，最多不能超过三次。

Note: Reflow soldering is recommended at the temperatures and times shown, no more than three times.

波峰焊温度曲线图 Wave Soldering Profile



手工烙铁焊接 Soldering with hand soldering iron

- A. 手工烙铁焊仅用于产品返修或样品测试；
Hand soldering iron is only used for product rework or sample testing;
- B. 手工烙铁焊要求：温度 $360^{\circ}\text{C} \pm 5^{\circ}\text{C}$ ，时间 $\leq 3\text{s}$ 。
Manual soldering method Temperature: $360^{\circ}\text{C} \pm 5^{\circ}\text{C}$, within 3s.

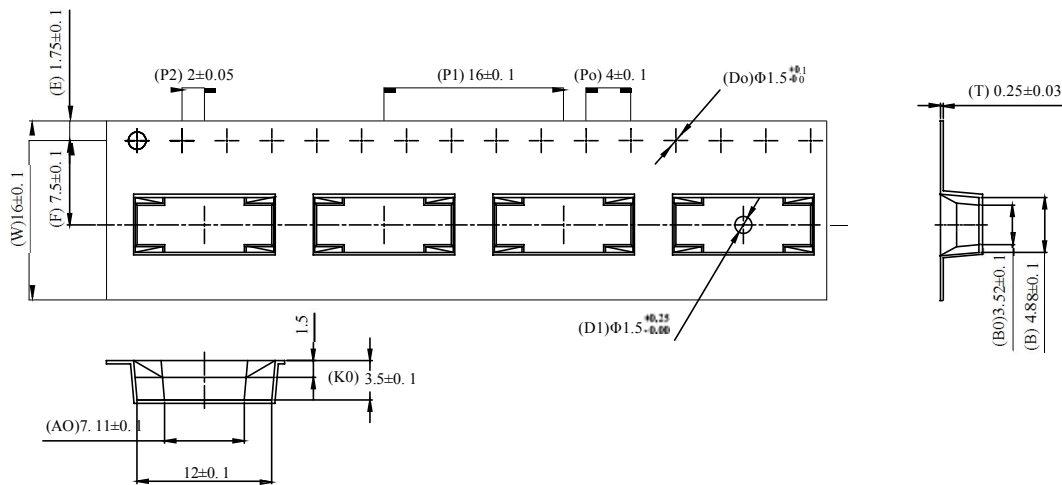
包装 Packing

■ 汇总表 Summary table

封装形式	包装方式	盘数量	盒数量	箱数量	静电袋规格	盒规格	箱(双瓦楞)规格	备注
SOP6	卷盘 ($\phi 330\text{mm}$ 蓝盘)	1000 只/盘	2 盘/盒	10 盒/箱	380*380mm	340*60*340 mm	620*360*365mm	首尾端空至少 200mm
Package Type	Packing Form	Quantity per Reel	Quantity per Box	Quantity per Carton	Antistatic Bag Specification	Box Specification	Carton Specification	Note
SOP6	Reel ($\phi 330\text{mm}$ Blue)	1k pcs/reel	2Reel/box	10box/ctn	380*380mm	340*60*340 mm	620*360*365mm	Leave at least 200mm of blank space at both ends

■ 编带包装 Tape & Reel

- 1) 每卷数量：1000 只。
Qty/reel：1000 pcs.
- 2) 每箱数量：20000 只。
Qty/ctn：20000 pcs.
- 3) 内包装：每盒 2 盘。
Inner packing：2 reels/box.
- 4) 示意图 Schematic：



单位 Unit：mm

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