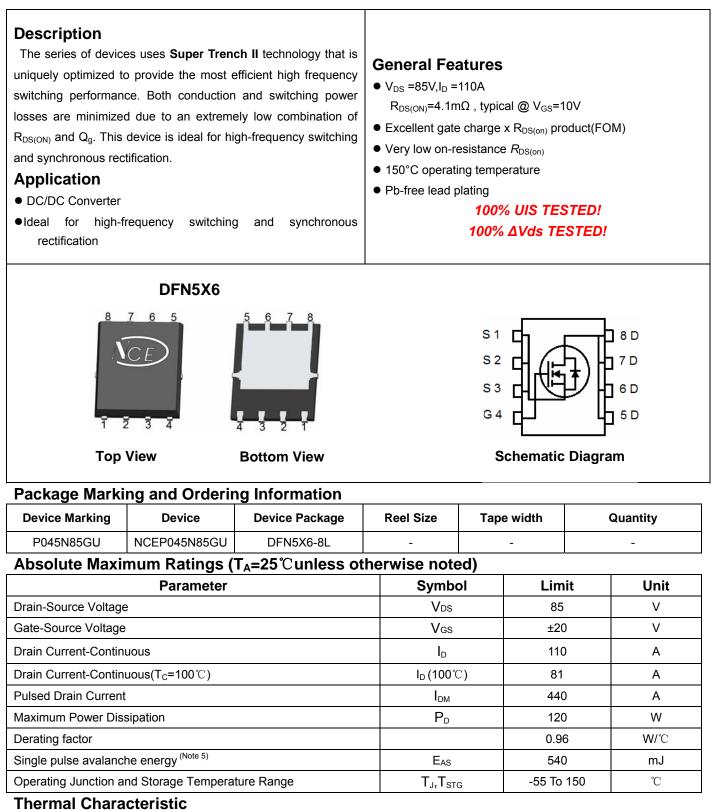




## NCE N-Channel Super Trench II Power MOSFET





#### Electrical Characteristics (T<sub>c</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	85		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =85V, $V_{GS}$ =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)	····		·			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.0	3.0	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, I <sub>D</sub> =55A	-	4.1	4.5	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =55A		62	-	S
Dynamic Characteristics (Note4)			·			
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V, F=1.0MHz	-	3528	-	PF
Output Capacitance	C <sub>oss</sub>		-	635	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	11	-	PF
Switching Characteristics (Note 4)	· · ·		·			
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{DD}$ =40V,I <sub>D</sub> =55A $V_{GS}$ =10V,R <sub>G</sub> =1.6 $\Omega$	-	18	-	nS
Turn-on Rise Time	tr		-	17	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	44	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	11	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =40V,I <sub>D</sub> =55A, V <sub>GS</sub> =10V	-	52	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	14.0		nC
Gate-Drain Charge	Q <sub>gd</sub>		-	13.2		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =55A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	110	Α
Reverse Recovery Time	t <sub>rr</sub>	$T_J$ = 25°C, $I_F$ = $I_S$	-	61	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/ $\mu$ s <sup>(Note3)</sup>	-	110	-	nC

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board,  $t \le 10$  sec.

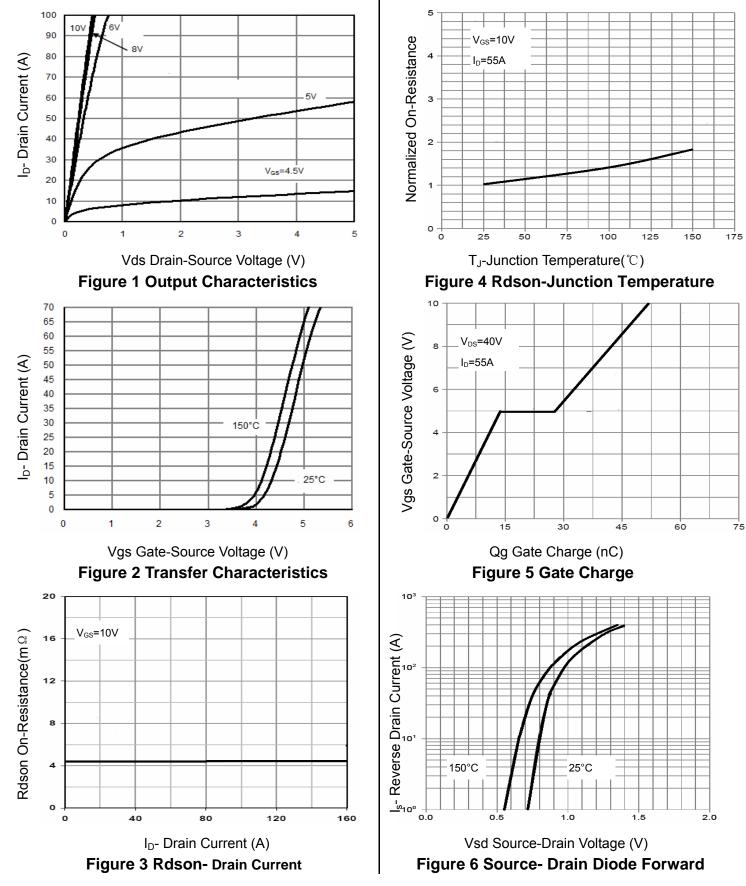
3. Pulse Test: Pulse Width ≤ 300 $\mu$ s, Duty Cycle ≤ 2%.

4. Guaranteed by design, not subject to production

5. EAS condition : Tj=25  $^\circ \!\! \mathbb{C}$  ,V\_{DD}=40V,V\_G=10V,L=0.5mH,Rg=25  $\Omega$ 

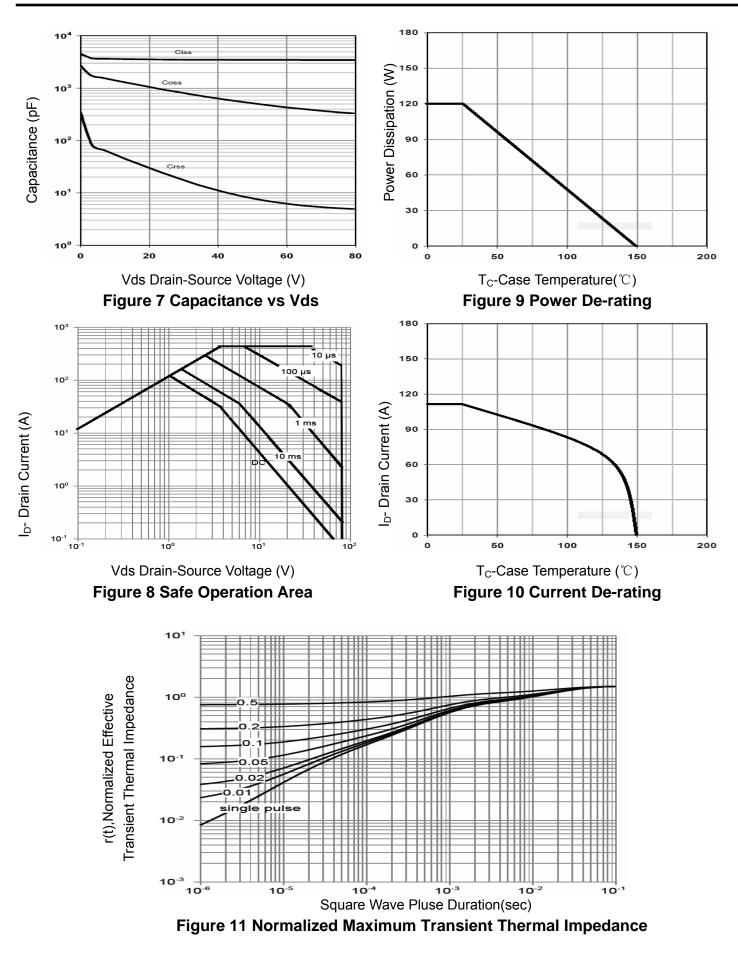


#### **Typical Electrical and Thermal Characteristics**





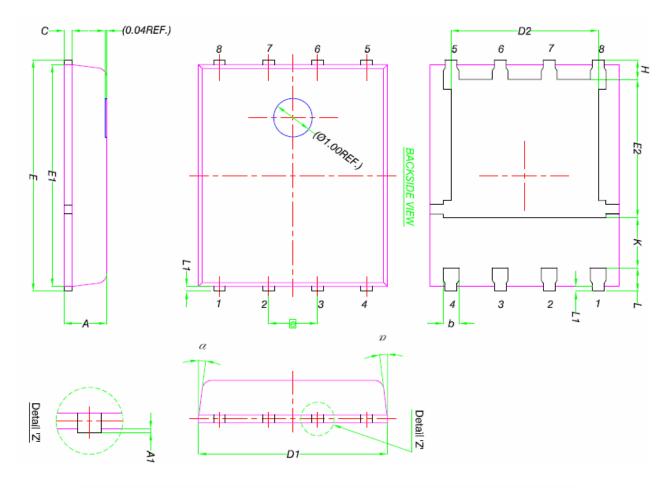
# NCEP045N85GU



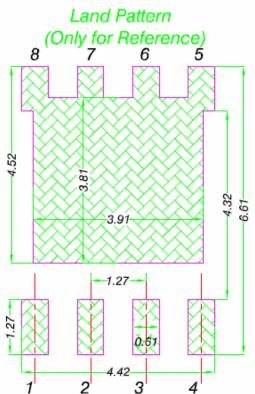




### DFN5X6-8L Package Information



DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	0.90	1.00	1.10		
A1	0	-	0.05		
b	0.33	0.41	0.51		
С	0.20	0.25	0.30		
D1	4.80	4.90	5.00		
D2	3.61	3.81	3.96		
Е	5.90	6.00	6.10		
E1	5.70	5.75	5.80		
E2	3.38	3.58	3.78		
е	1.27 BSC				
Н	0.41	0.51	0.61		
к	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.06	0.13	0.20		
α	0°	-	12°		





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