

NCE N&P-Channel complementary Power MOSFET

Description

The NCE60NP4035K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

N channel

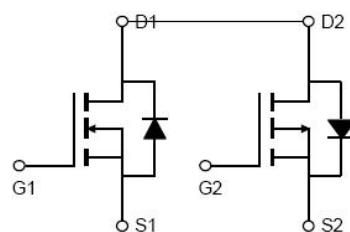
- $V_{DS} = 60V, I_D = 40A$
- $R_{DS(ON)} < 15.5m\Omega @ V_{GS} = 10V$
- $R_{DS(ON)} < 22m\Omega @ V_{GS} = 4.5V$

p channel

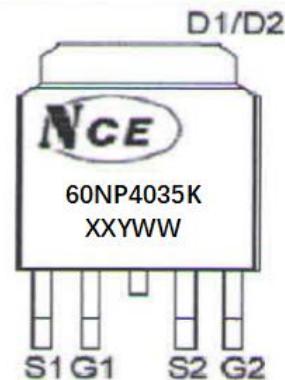
- $V_{DS} = -60V, I_D = -35A$
- $R_{DS(ON)} < 35m\Omega @ V_{GS} = -10V$
- High density cell design for ultra low $R_{DS(on)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- H-bridge
- Inverters



Schematic diagram



Marking and pin assignment

100% UIS TESTED!

100% ΔV_{ds} TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
60NP4035K	NCE60NP4035K	TO-252-4L	-	-	-

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V_{DS}	60	-60	V
Gate-Source Voltage		V_{GS}	± 20	± 20	V
Continuous Drain Current	$T_c=25^\circ C$	I_D	40	-35	A
	$T_c=100^\circ C$		28	-24.5	
Pulsed Drain Current (Note 1)		I_{DM}	160	-140	A
Maximum Power Dissipation	$T_c=25^\circ C$	P_D	80		W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 To 175		°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	1.88	°C/W
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N-Channel Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.3	1.6	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=20\text{A}$	-	13.5	15.5	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=20\text{A}$		18.5	22	
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=20\text{A}$	18	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $F=1.0\text{MHz}$	-	1620	-	PF
Output Capacitance	C_{oss}		-	112	-	PF
Reverse Transfer Capacitance	C_{rss}		-	96	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{R}_L=6.7\Omega$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_G=3\Omega$	-	7.4	-	nS
Turn-on Rise Time	t_r		-	5.1	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	28.2	-	nS
Turn-Off Fall Time	t_f		-	5.5	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=20\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	-	38.5	-	nC
Gate-Source Charge	Q_{gs}		-	7	-	nC
Gate-Drain Charge	Q_{gd}		-	8.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=20\text{A}$	-		1.2	V
Diode Forward Current (Note 2)	I_s		-	-	40	A
Reverse Recovery Time	t_{rr}	$\text{TJ} = 25^\circ\text{C}, \text{IF} = 20\text{A}$ $\text{di/dt} = 100\text{A}/\mu\text{s}$ (Note 3)	-	28	-	nS
Reverse Recovery Charge	Q_{rr}		-	40	-	nC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition: $\text{Tj}=25^\circ\text{C}, \text{VDD}=30\text{V}, \text{VG}=10\text{V}, \text{L}=0.5\text{mH}, \text{Rg}=25\Omega$

N-Channel Typical Electrical and Thermal Characteristics (Curves)

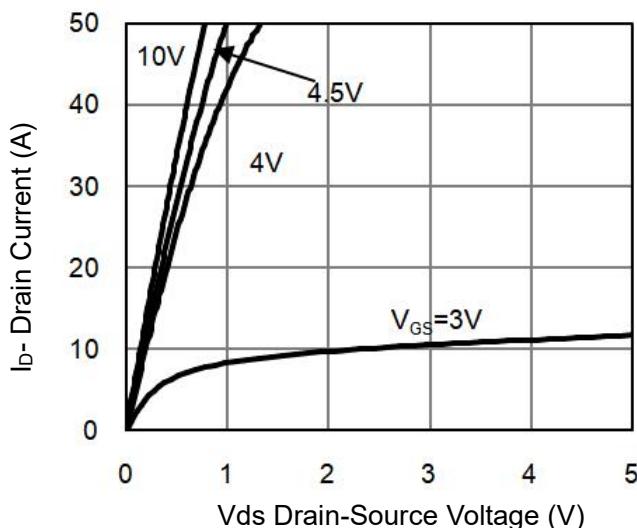


Figure 1 Output Characteristics

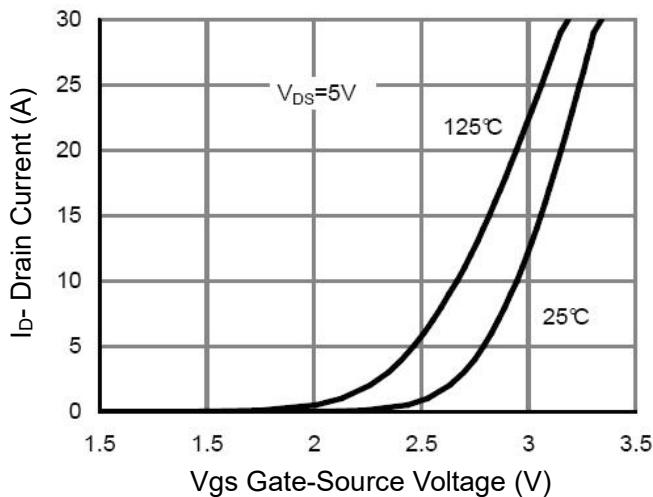


Figure 2 Transfer Characteristics

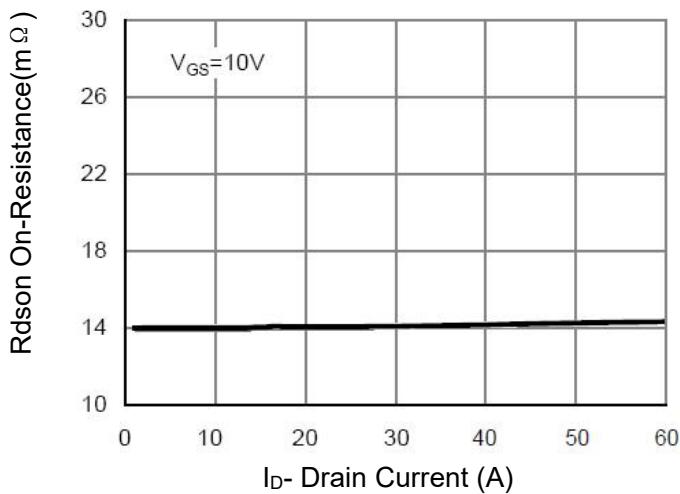


Figure 3 Rdson- Drain Current

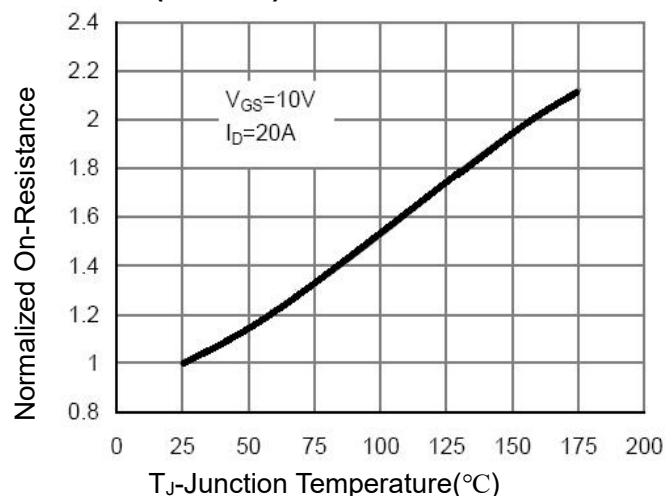


Figure 4 Rdson-Junction Temperature

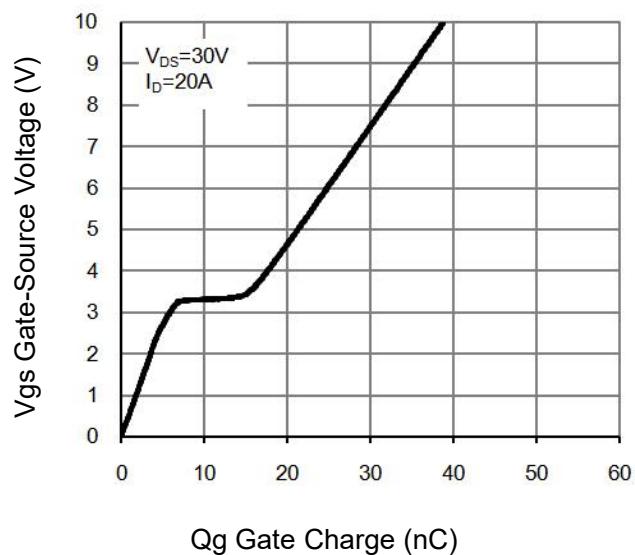


Figure 5 Gate Charge

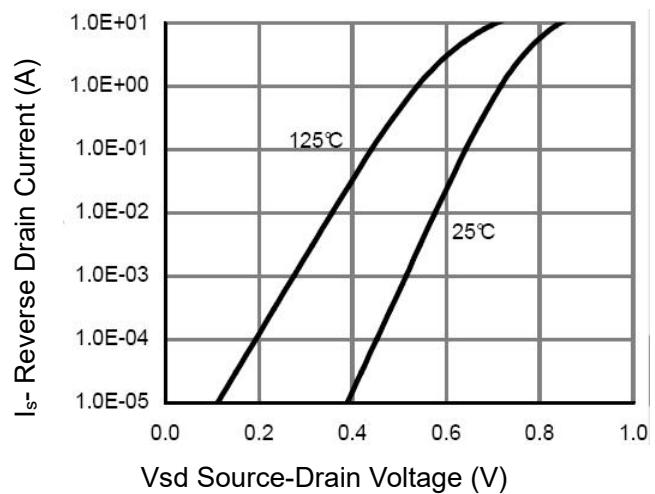


Figure 6 Source- Drain Diode Forward

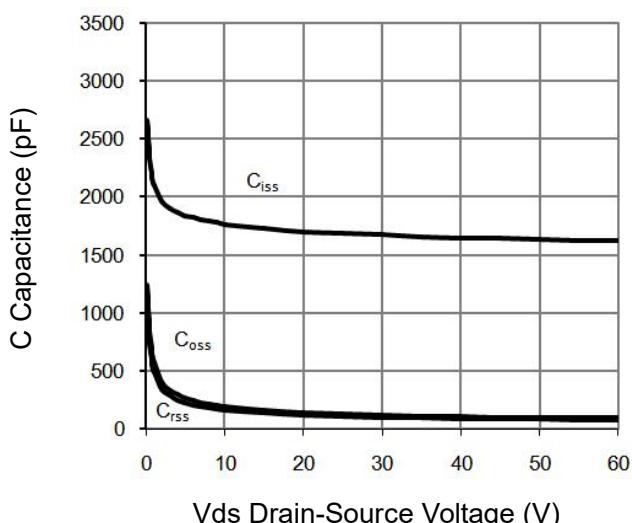


Figure 7 Capacitance vs Vds

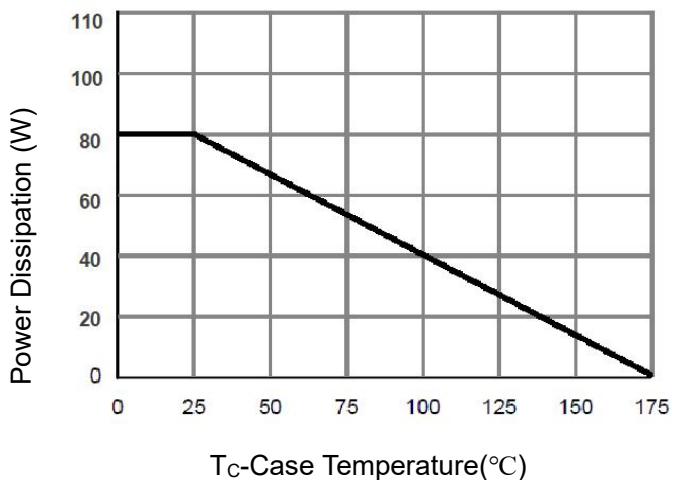


Figure 9 Power De-rating

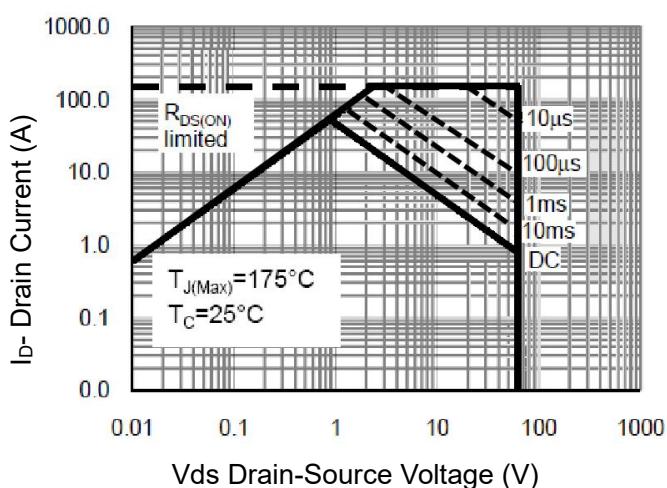


Figure 8 Safe Operation Area

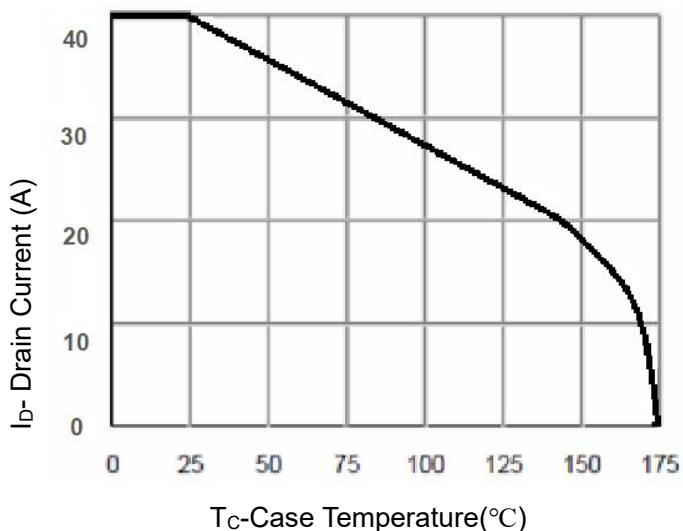


Figure 10 ID Current De-rating

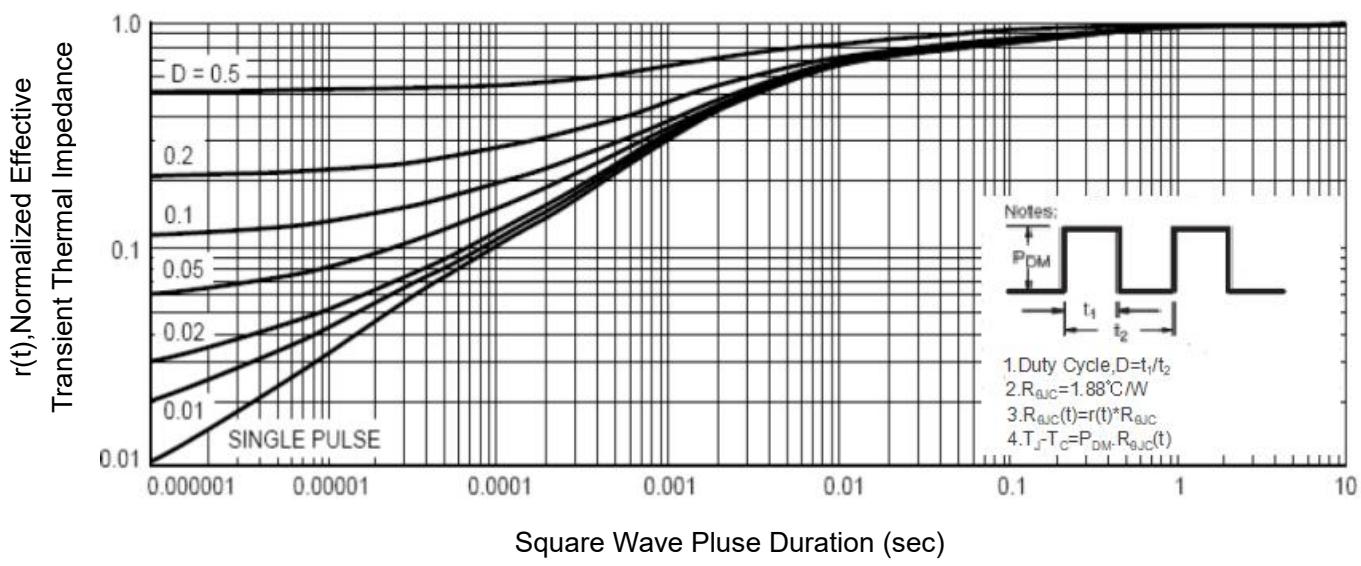


Figure 11 Normalized Maximum Transient Thermal Impedance

P-Channel Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	±100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-2.0	-2.6	-3.5	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-20\text{A}$	-	31	35	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-20\text{A}$	-	20	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	2220	-	PF
Output Capacitance	C_{oss}		-	119	-	PF
Reverse Transfer Capacitance	C_{rss}		-	97.5	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-30\text{V}, I_{\text{D}}=-20\text{A}, V_{\text{GS}}=-10\text{V}, R_{\text{G}}=3\Omega$	-	13	-	nS
Turn-on Rise Time	t_{r}		-	14	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	39	-	nS
Turn-Off Fall Time	t_{f}		-	15	-	nS
Total Gate Charge	Q_{g}	$V_{\text{DS}}=-30, I_{\text{D}}=-20\text{A}, V_{\text{GS}}=-10\text{V}$	-	40.5	-	nC
Gate-Source Charge	Q_{gs}		-	9	-	nC
Gate-Drain Charge	Q_{gd}		-	9.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-20\text{A}$	-		1.2	V
Diode Forward Current ^(Note 2)	I_{s}		-	-	-45	A
Reverse Recovery Time	t_{rr}	$T_{\text{J}} = 25^\circ\text{C}, I_{\text{F}} = -20\text{A}$ $dI/dt = -100\text{A}/\mu\text{s}$ ^(Note 3)	-	-	40	nS
Reverse Recovery Charge	Q_{rr}		-	-	70	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

P-Channel Typical Electrical and Thermal Characteristics (Curves)

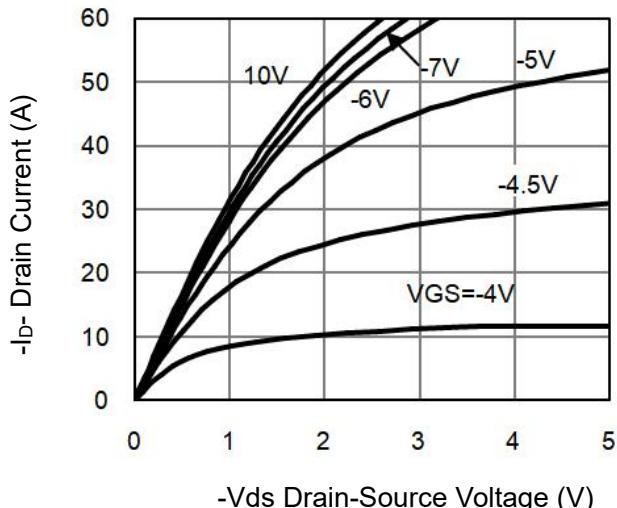


Figure 1 Output Characteristics

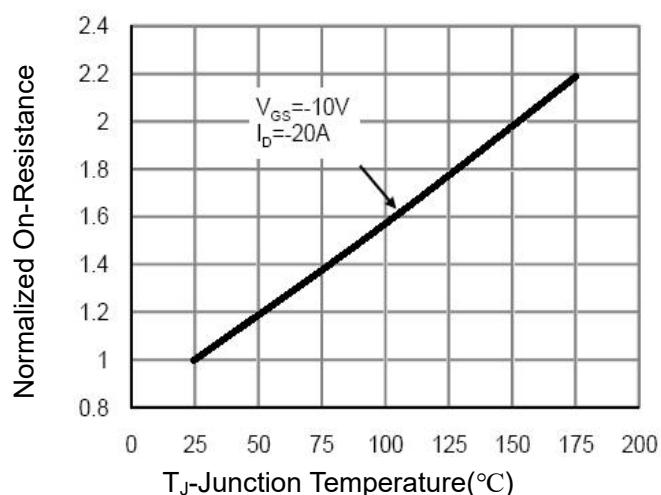


Figure 4 Rdson-Junction Temperature

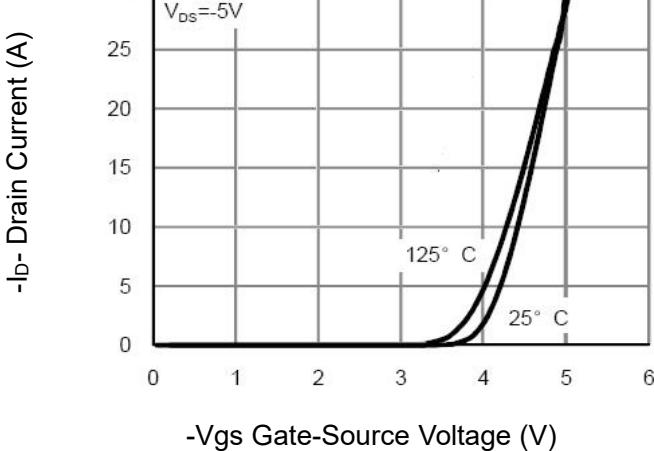


Figure 2 Transfer Characteristics

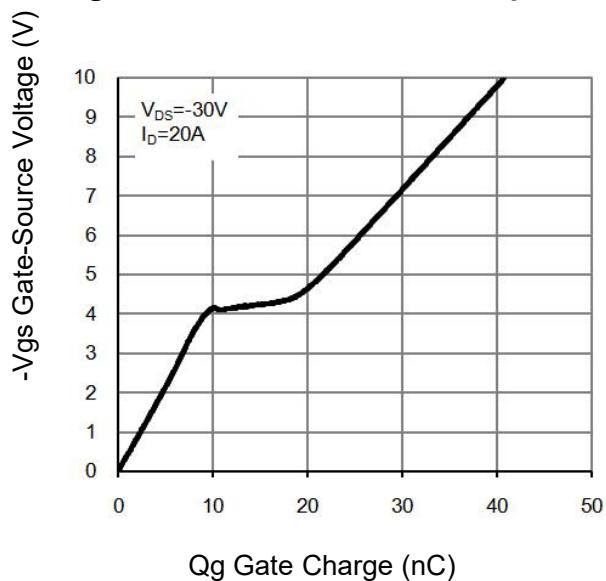


Figure 5 Gate Charge

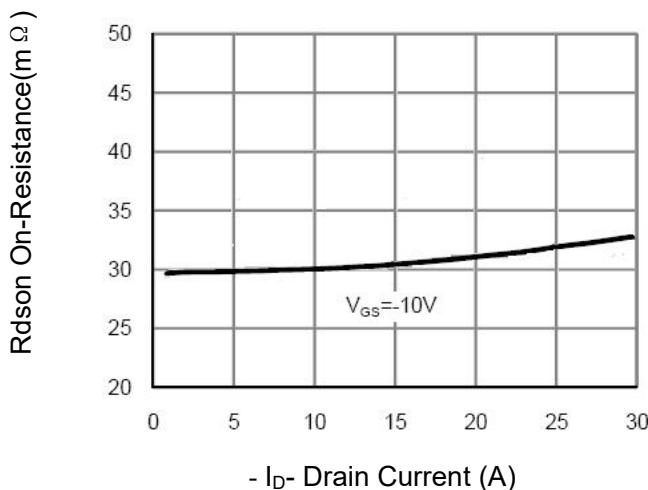


Figure 3 Rdson- Drain Current

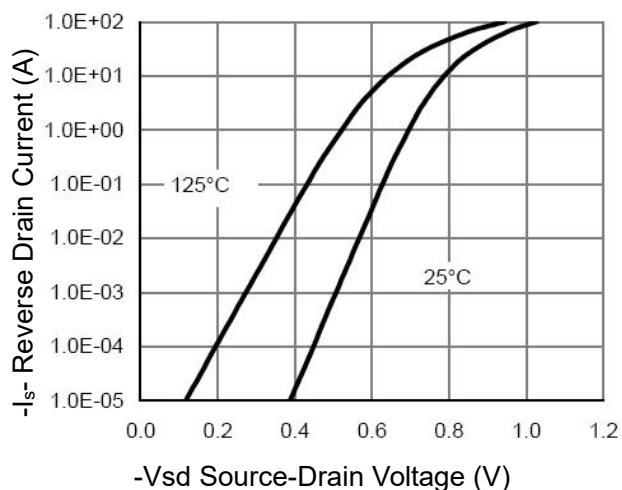


Figure 6 Source- Drain Diode Forward

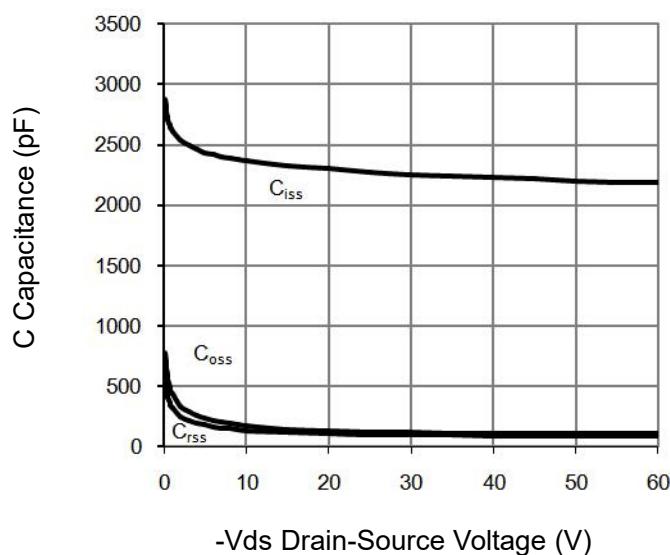


Figure 7 Capacitance vs Vds

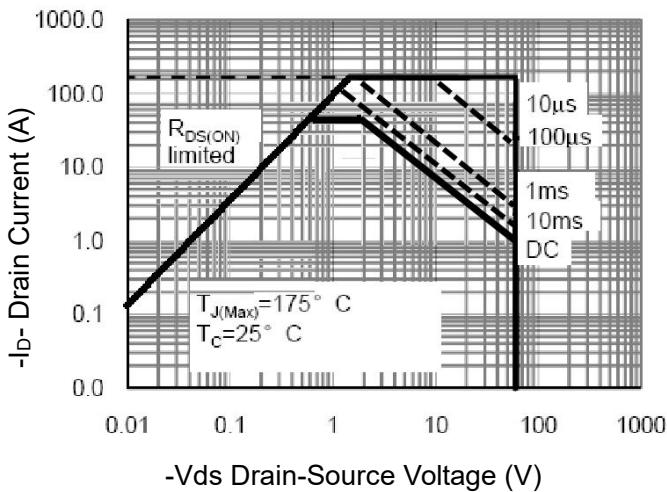


Figure 8 Safe Operation Area

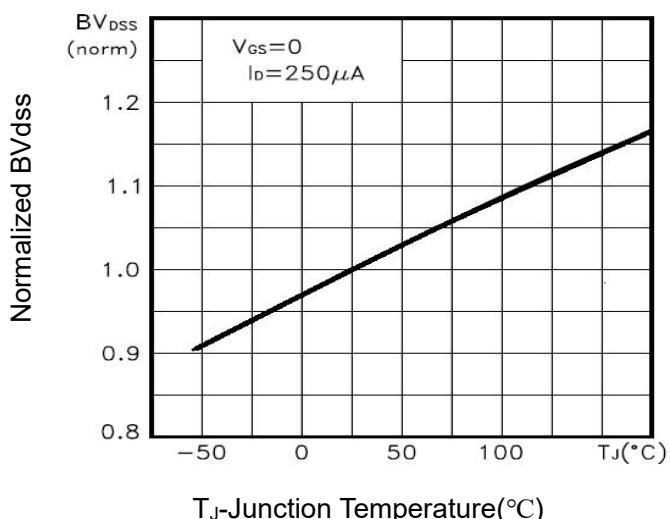


Figure 9 BV_{dss} vs Junction Temperature

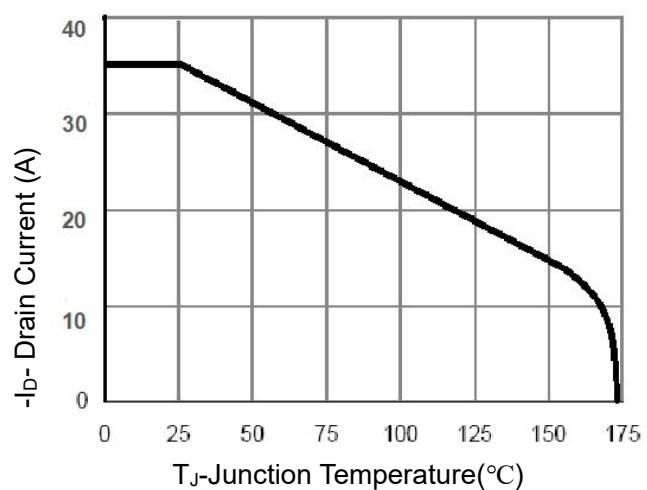


Figure 10 ID Current De-rating

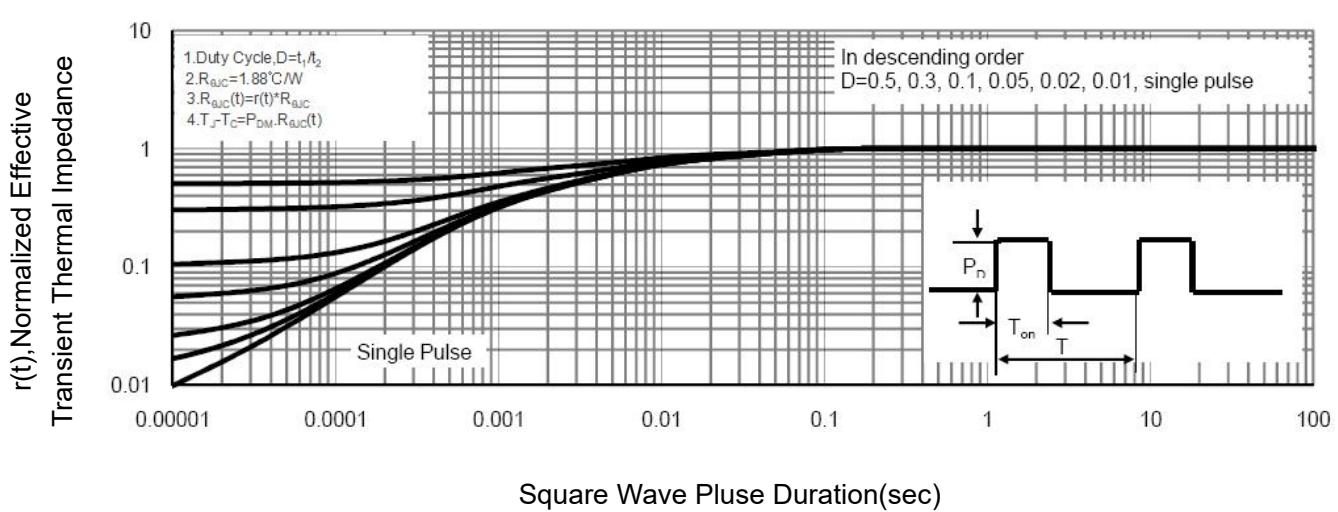
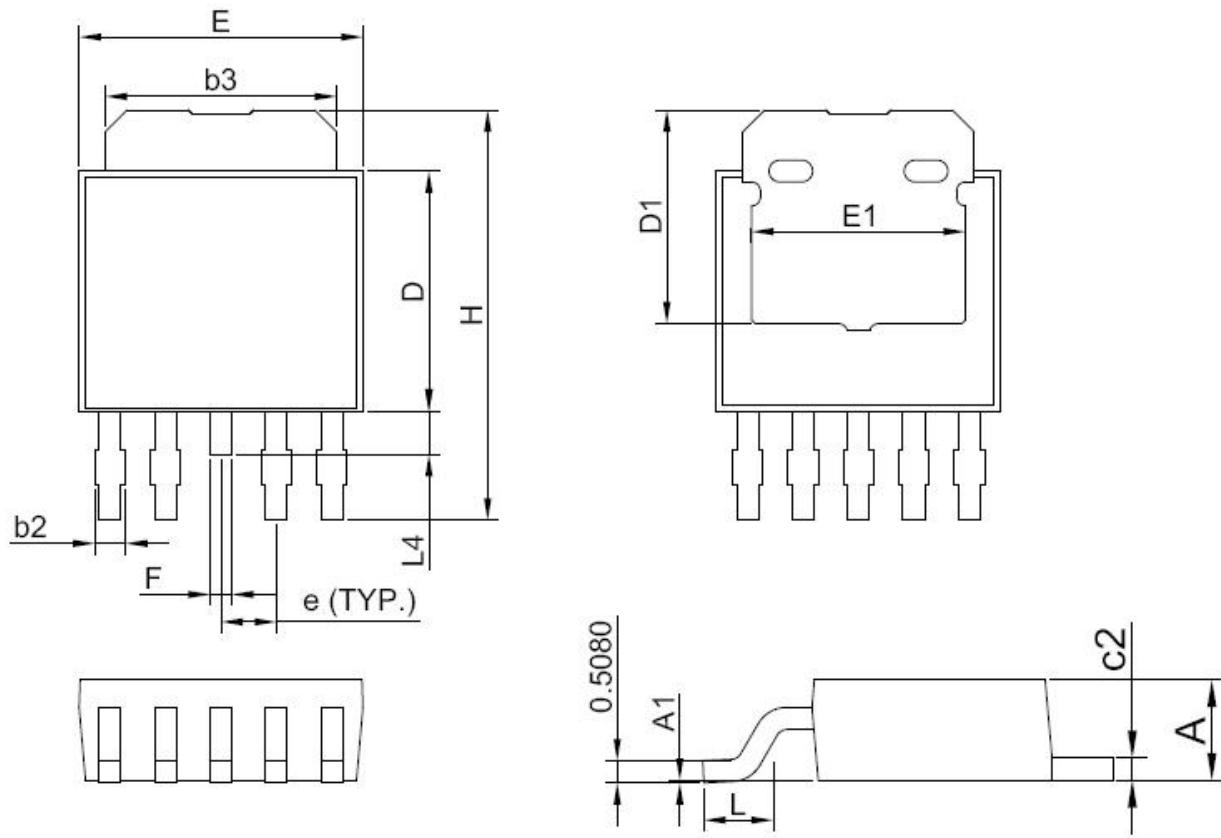


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252-4L Package Information



COMMON DIMENSIONS
UNITS OF MEASURE=MILLIMETER

SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.00	0.08	0.15
b	0.45	0.53	0.60
b2	0.50	0.65	0.80
b3	5.20	5.35	5.50
c2	0.45	0.50	0.55
D	5.40	5.60	5.80
D1	4.57	-	-
E	6.40	6.60	6.80
E1	3.81	-	-
e	1.27 REF.		
F	0.40	0.50	0.60
H	9.40	9.80	10.20
L	1.40	1.59	1.77
L1	2.40	2.70	3.00
L2	0.80	1.00	1.20

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