

ALPHA-TOP TECHNOLOGY CORP.

# APPROVAL SHEET

MODEL NO.:	nSMD003	
CUSTOMER:		
CUSTOMER'S APPRO	/AL:	
AUTHORIZED SIGNAT	JRE/STAMP:	
DATE		

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Submitted by: Approved by: DATE:	Chen YC Lin 27-Apr-22

SEA & LAND ELECTRONIC CORP.

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# Features Surface Mount Devices Lead free device Size 3.2\*1.6 mm/0.12\*0.06 inch Surface Mount packaging for automated assembly

#### Applications Almost anywhere there is a low voltage power supply, up to 60V and a load to be protected, including: Computer mother board, Modem. USB hub PDAs & Charger, Analog & digital line card Digital cameras, Disk drivers, CD-ROMs,

Alpha-Top (Sea&Land Alliance)

# nSMD003

Performance Specification														
	Model	Marking	V <sub>max</sub>	max	I <sub>hold</sub>	I <sub>trip</sub>	$\mathbf{P}_{d}$		Maximum Time To Trip		Resistance		Agency Approval	
	Model	warking	(Vdc)		@25°C	@25°C	Max.	Current	Time	Ri <sub>min</sub>	R1max	UL	TUV	
	nSMD003	αT	(Vac) 60	<b>(A)</b> 20	(A) 0.03	( <b>A</b> ) 0.10	<b>(W)</b> 0.4	( <b>A</b> ) 0.20	(Sec) 1.20	<b>(Ω)</b> 8.000	<b>(Ω)</b> 80.000			
lhold	= Hold Current.	Maximum cu	irrent device	will not trip	in 25°C still a	air.								
Itrip	= Trip Current. M	Ainimum curr	ent at which	the device	will always tri	p in 25°C stil	l air.							
Vmax	= Maximum ope	erating voltag	e device car	n withstand w	without dama	ge at rated c	urrent (Ima	x).						
Imax	= Maximum fau	It current dev	ice can with	stand witho	ut damage at	t rated voltag	e (Vmax).							
Pd	= Power dissipat	tion when dev	vice is in the	tripped stat	e in 25°C stil	l air environn	nent at rate	d voltage.						
Rimin/max = Minimum/Maximum device resistance prior to tripping at 25°C.														
R1 <sub>max</sub>														
CAUT	CAUTION : Operation beyond the specified ratings may result in damage and possible arcing and flame.													

#### **Environmental Specifications**

Test	Conditions					
Passive aging	+85°C, 1000 hrs.					
Humidity aging	+85°C, 85% R.H. , 168 hours					
Thermal shock	+85°C to -40°C, 20 times					
Resistance to solvent	MIL-STD-202, Method 215					
Vibration	MIL-STD-202, Method 201					
Ambient operating conditions :	- 40 °C to 85 °C					
Maximum surface temperature of the device in the tripped state is 125 °C						
In case of special use, please contact our engineer						

## Agency Approvals :

Regulation/Standard:



#### 2015/863/EU

EN14582

#### Ihold Versus Temperature

Madal	Model	Maximum ambient operating temperature (T <sub>mao</sub> ) vs. hold current (I <sub>hold</sub> )									
MOUEI		-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C	
nSMD003	3	0.045	0.040	0.035	0.030	0.026	0.023	0.021	0.018	0.015	

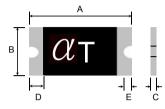


## nSMD003

Alpha-Top (Sea&Land Alliance)

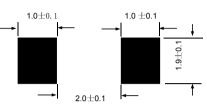
Construction And Dimension (Unit:mm)									
Model	Α			В		С		E	
Model	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Min.	
nSMD003	3.00	3.50	1.50	1.80	0.60	1.10	0.15	0.10	

#### **Dimensions & Marking**



 $\alpha$  = Trademark Z = Part identification

#### **Recommended Pad Layout (mm)**



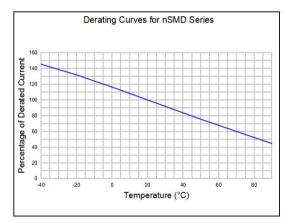
#### **Termination Pad Characteristics**

Terminal pad materials : Terminal pad solderability : Rework

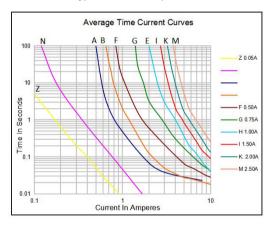
Tin-plated Nickel-Copper Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3.

Use standard industry practices, the removal device must be replaced with a fresh one.

#### Thermal Derating Curve



#### Typical Time-To-Trip At 25°C



# WARNING:

Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.

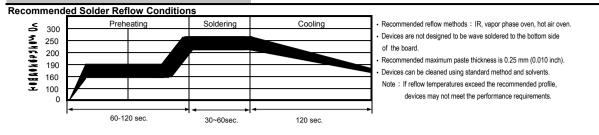
PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated. Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.

Use PPTC with a large inductance in circuit voltes at maintee in themself with inconsistent with recommended ecclosing, inclinate a proceedings for eccelosing components. Use PPTC with a large inductance in circuit voltes (L. di/dt) above the rated voltage of the PPTC. Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space. Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods. Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.

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## nSMD003

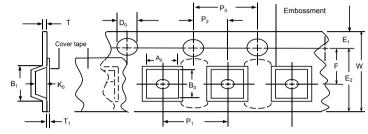
#### Alpha-Top (Sea&Land Alliance)



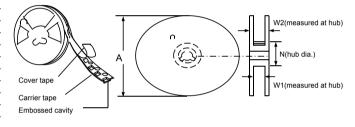
#### Tape And Reel Specifications (mm)

Governing Specifications	EIA 481-1
W	8.15 ± 0.3
_P0	4.0 ± 0.10
_P1	4.0 ± 0.10
P2	2.0 ± 0.05
_A0	1.95 ± 0.10
B0	3.45 ± 0.10
B1max.	4.35
_D0	1.5 + 0.1, -0
F	3.5 ± 0.05
_E1	1.75 ± 0.10
E2min.	6.25
Tmax.	0.6
T1max.	0.1
K0	1.04 ± 0.1
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	60
W1	9 ± 0.5
W2	12.6 ± 0.5

## EIA Tape Component Dimensions



#### **EIA Reel Dimensions**



## Storage And Handling

• Storage conditions : 40°C max, 70% R.H.

· Devices may not meet specified performance

if storage conditions are exceeded.

Order Information	Packaging				
nSMD	003	Tape & Reel Quantity			
Product name	Hold				
Size 3216 mm / 1206 inch	Current	3,500 pcs/reel			
SMD : surface mount device	0.03A				

Tape & reel packaging per EIA481-1

Labeling Information

