

PART NUMBER 54FCT244DMB-ROCS

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All re-creations are done with the approval of the Original Component Manufacturer. (OCM)

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceeds the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-38535
 - Class Q Military
 - Class V Space Level

Qualified Suppliers List of Distributors (QSLD)

 Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OCM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

54FCT244

54FCT244 Octal Buffer/Line Driver with -TRISTATE Outputs



Literature Number: SNOS430



54FCT244

Octal Buffer/Line Driver with TRI-STATE® Outputs

General Description

The 'FCT244 is an octal buffer and line driver with TRI-STATE outputs designed to be employed as a memory and address driver, clock driver, or bus-oriented transmitter/receiver.

Features

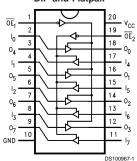
- Non-inverting buffers
- Output sink capability of 48 mA, source capability of 12 mA
- TRI-STATE outputs drive lines or buffer memory address registers
- TTL input and output level compatible
- CMOS power consumption
- Standard Microcircuit Drawing (SMD) 5962-8763001

Ordering Code

Military	Package	Package Description
	Number	
54FCT244DMQB	J20A	20-Lead Ceramic Dual-In-Line
54FCT244FMQB	W20A	20-Lead Cerpack
54FCT244LMQB	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C

Connection Diagrams

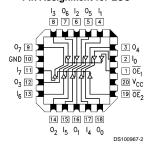
Pin Assignment for DIP and Flatpak



Pin	Description			
Names				
$\overline{OE}_1,\overline{OE}_2$	Output Enable Input			
	(Active Low)			
I ₀ -I ₇	Inputs			
O ₀ -O ₇	Outputs			

OE ₁	I ₀₋₃	O ₀₋₃	OE ₂	I ₄₋₇	0 ₄₋₇
Н	Х	Z	Н	Х	Z
L	Н	Н	L	Н	Н
L	L	L	L	L	L

Pin Assignment for LCC



H = HIGH Voltage Level L = LOW Voltage Level

X = Immaterial

Z = High Impedance

TRI-STATE® is a registered trademark of National Semiconductor Corporation.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Storage Temperature -65°C to $+150^{\circ}\text{C}$ Ambient Temperature under Bias -55°C to $+125^{\circ}\text{C}$

Junction Temperature under Bias

Ceramic -55°C to +175°C

 V_{CC} Pin Potential to Ground Pin \$-0.5V\$ to +7.0V Input Voltage (Note 2) -0.5V to +7.0V

Input Current (Note 2)

-0.5v to +7.0v

-30 mA to +5.0 mA

Voltage Applied to Any Output

in the Disabled or

Power-Off State $\begin{array}{cc} -0.5 \text{V to } 5.5 \text{V} \\ \text{in the HIGH State} \end{array}$

Current Applied to Output

in LOW State (Max) twice the rated I_{OL} (mA) DC Latchup Source Current -500 mA

Recommended Operating Conditions

Free Air Ambient Temperature

Military -55°C to +125°C

Supply Voltage

DC Electrical Characteristics for 'FCT Family Devices

Symbol	Parameter		FCT244		Units	V _{cc}	Conditions	
			Min	Max	1			
V _{IH}	Input HIGH Voltage		2.0		V		Recognized HIGH Signal	
V _{IL}	Input LOW Voltage			0.8	V		Recognized LOW Signal	
V _{CD}	Input Clamp Diode Voltage			-1.2	V	Min	I _{IN} = -18 mA	
V _{OH}	Output HIGH Voltage	54FCT	4.3		V	Min	I _{OH} = -300 μA	
		54FCT	2.4				I _{OH} = -12 mA	
V _{OL}	Output LOW Voltage	54FCT		0.2	V	Min	I _{OL} = 300 μA	
		54FCT		0.55			I _{OL} = 48 mA	
I _{IH}	Input HIGH Current			5	μΑ	Max	V _{IN} = V _{CC}	
I _{IL}	Input LOW Current			-5	μA	Max	V _{IN} = 0.0V	
I _{OZ}	Maximum TRI-STATE Current F LOW	IIGH or		±10	μA	Max	$V_{IN} = 0.0V$ or $V_{IN} = V_{CC}$	
I _{os}	Output Short-Circuit Current			-60	mA	Max	V _{OUT} = 0.0V	
I _{ccq}	Quiescent Power Supply Curren	t		1.5	mA	Max	V_{IN} < 0.2V or V_{IN} 5.3V, V_{CC} = 5.5V	
Δl _{CC}	Quiescent Power Supply Current			2.0	mA	Max	$V_{I} = 3.4V, V_{CC} = 5.5V$	
I _{CCD}	Dynamic I _{cc}			0.4	mA/ MHz	Max	Outputs Open, V_{CC} = 5.5V, V_{IN} 5.3V or V_{IN} < 0.2V, One Bit Toggling, 50% Duty Cycle, \overline{OE} = GND, LE = V_{CC}	
I _{CCT}	Total Power Supply Current			6.0	mA	Max	Outputs Open, f_{CP} = 10 MHz, V_{CC} = 5.5V, V_{IN} 5.3V or V_{IN} < 0.2V, One Bit Toggling, 50% Duty Cycle, \overline{OE} = GND, LE = V_{CC}	

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

Note 3: All outputs loaded; thresholds on input associated with output under test.

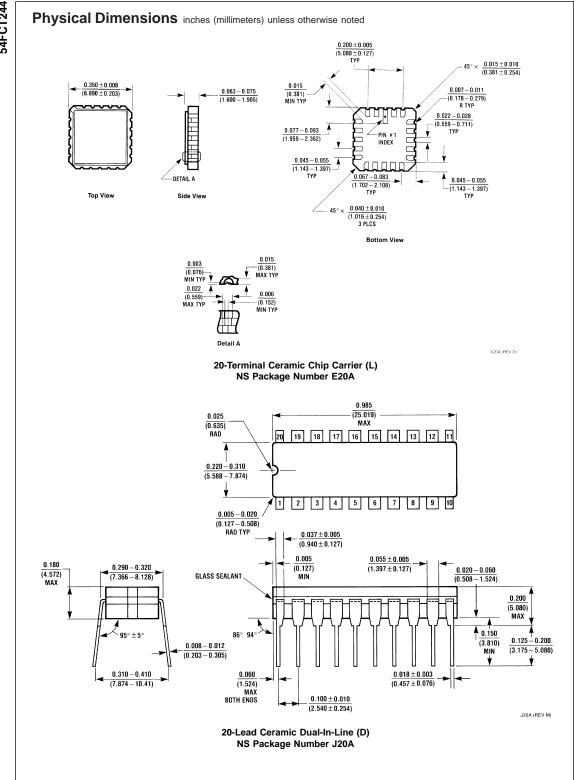
Note 4: Maximum test duration 2.0 ms, one output loaded at a time.

Symbol	Parameter	54	FCT	Units	Fig.
		$T_A = -55^{\circ}C \text{ to } +125^{\circ}C$ $V_{CC} = 4.5V - 5.5V$ $C_L = 50 \text{ pF}$			No.
		Min	Max		
t _{PLH}	Propagation Delay	1.5	7.5	ns	
t_{PHL}	Data to Outputs	1.5	7.5		
t _{PZH}	Output Enable	1.5	10.5	ns	
t_{PZL}	Time	1.5	10.5		
t _{PHZ}	Output Disable	1.5	8.0	ns	
t_{PLZ}	Time	1.5	8.0		

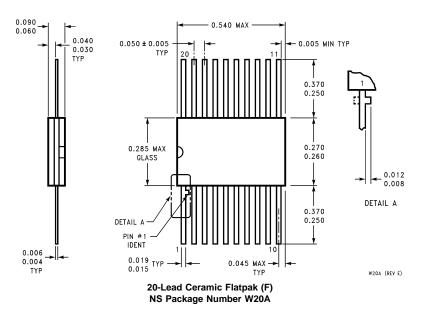
Capacitance

Symbol	Parameter	Max	Units	Conditions T _A = 25°C
C _{IN}	Input Capacitance	10.0	pF	V _{CC} = 0V
C _{OUT} (Note 5)	Output Capacitance	12.0	pF	V _{CC} = 5.0V

Note 5: C_{OUT} is measured at frequency f = 1 MHz, per MIL-STD-883B, Method 3012.



Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



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