

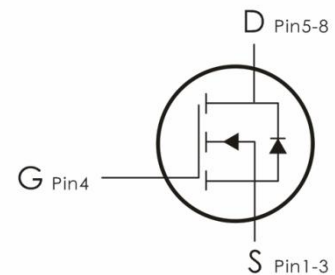
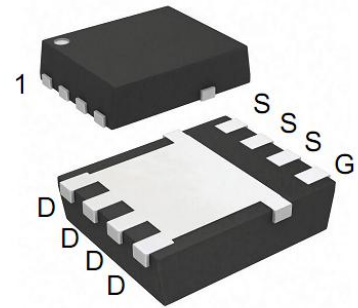
N-Channel Enhancement Mode MOSFET

Description:

This N-Channel MOSFET uses advanced SGT technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=100V, I_D=55A, R_{DS(ON)} < 10.8m\Omega @ V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹⁾	55	A
$I_{D, pulse}$	Pulsed Drain Current ²⁾	165	
I_S	Continuous diode forward current ¹⁾	55	A
$I_{S, Pulse}$	Diode pulsed current ²⁾	165	A
E_{AS}	Single Pulse Avalanche Energy ⁵⁾	20	mJ
P_D	Power Dissipation ³⁾	75	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.67	$^\circ C/W$

R_{θJA}	Thermal Resistance Junction to mbient ⁽⁴⁾	62	°C/W
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Package Marking and Ordering Information:

Part NO.	Marking	Package
BSC109N10NS3G	109N10	DFN5*6-8

Electrical Characteristics: (T_C=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	100	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =100V	---	---	1	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	1	---	2.5	V
R _{DS(ON)}	Drain-Source On Resistance	V _{GS} =10V, I _D =30A	---	9.9	10.8	m Ω
		V _{GS} =4.5V, I _D =12A	---	12.5	15	
R _G	Gate resistance	f=1 MHz, Open drain	---	2.8	---	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	2300	---	pF
C _{oss}	Output Capacitance		---	1550	---	
C _{rss}	Reverse Transfer Capacitance		---	120	---	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =50V, I _D =25A, R _G =2Ω, V _{GS} =10V	---	19	---	ns
t _r	Rise Time		---	5.4	---	ns
t _{d(off)}	Turn-Off Delay Time		---	39.2	---	ns
t _f	Fall Time		---	7.4	---	ns
Q _g	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _D =25A	---	27	---	nC
Q _{gs}	Gate-Source Charge		---	4.4	---	nC
Q _{gd}	Gate-Drain “Miller” Charge		---	6.5	---	nC

V _{plateau}	Gate plateau voltage		---	3.2	---	V
Drain-Source Diode Characteristics						
Symbol	Parameter	Conditions	Min	Typ	Max	Units
V _{SD}	Source-Drain Diode Forward Voltage	V _{GS} =0V,I _S =30A	---	---	1.3	V
I _{rrm}	Peak reverse recovery current	V _R =50V,I _S =25A, di/dt=100A/us	---	3	---	A
Q _{rr}	Reverse Recovery Charge		---	130	---	nC
T _{rr}	Reverse Recovery Time		---	70.8	---	ns

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a=25^\circ\text{C}$.
- 5) $V_{\text{DD}}=50\text{V}, V_{\text{GS}}=10\text{V}, L=0.3\text{mH}$, starting $T_j=25^\circ\text{C}$.

Typical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

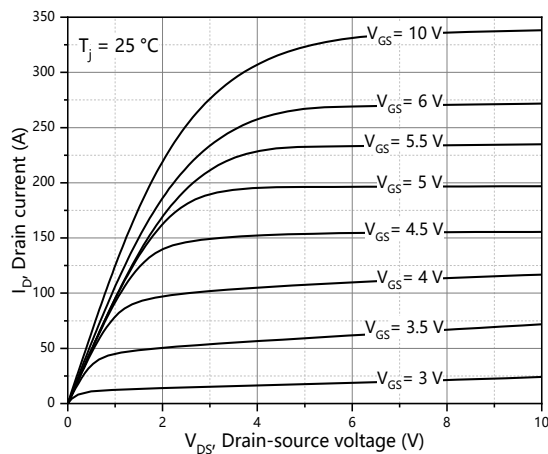


Figure 1. Typ. output characteristics

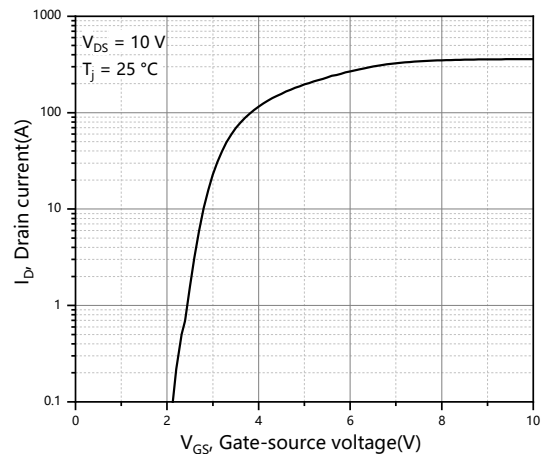
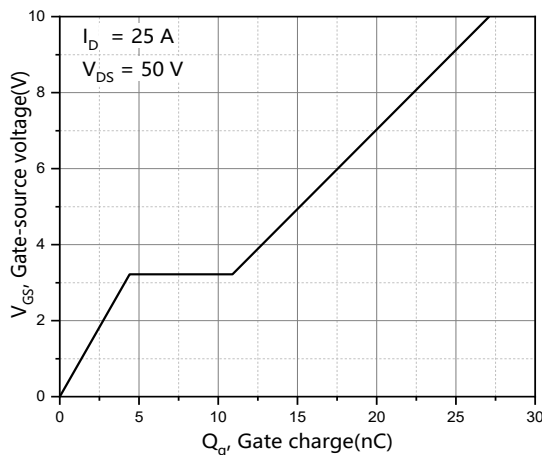
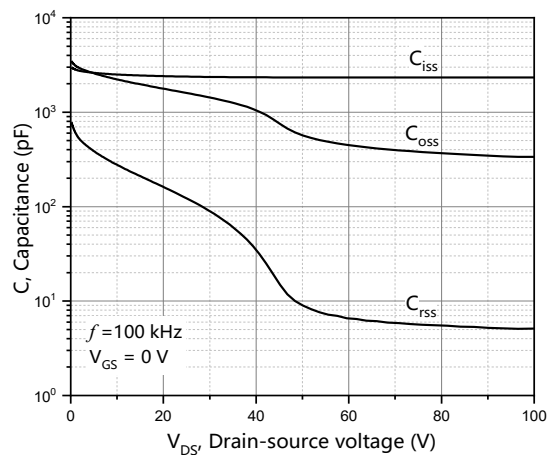


Figure 2. Typ. transfer characteristics



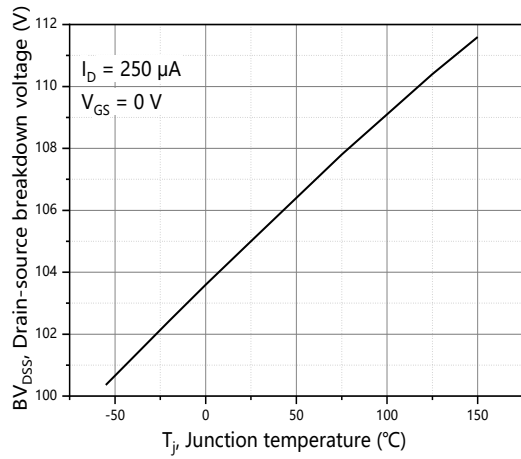


Figure 5. Drain-source breakdown voltage

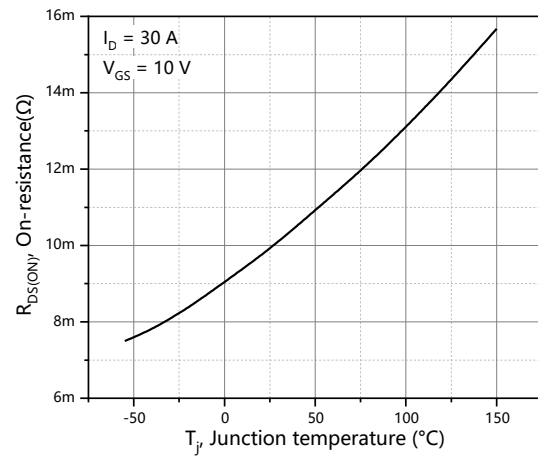


Figure 6. Drain-source on-state resistance

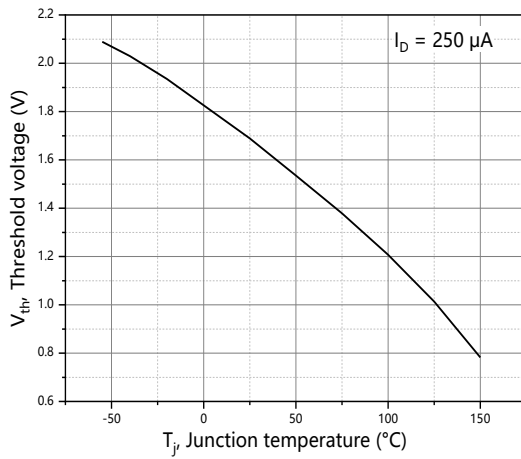


Figure 7. Threshold voltage

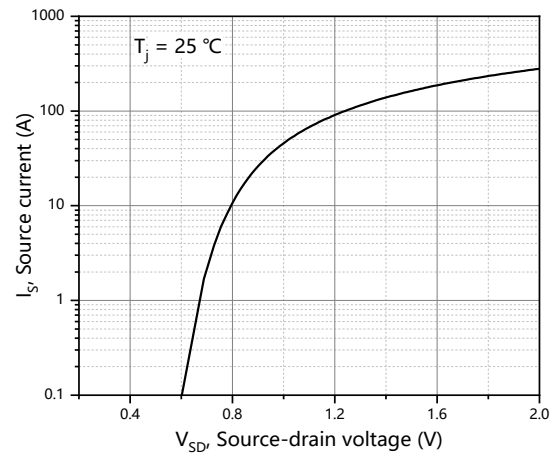


Figure 8. Forward characteristic of body diode

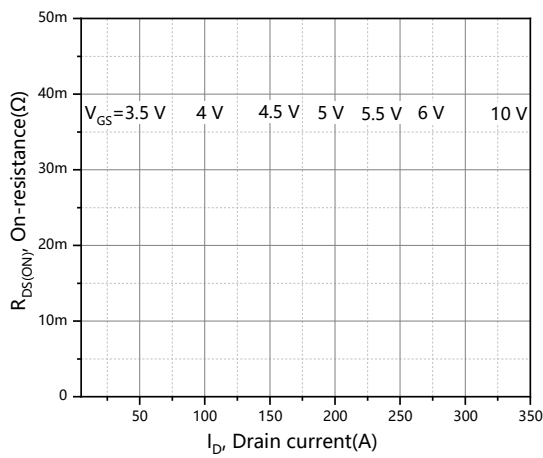


Figure 9. Drain-source on-state resistance

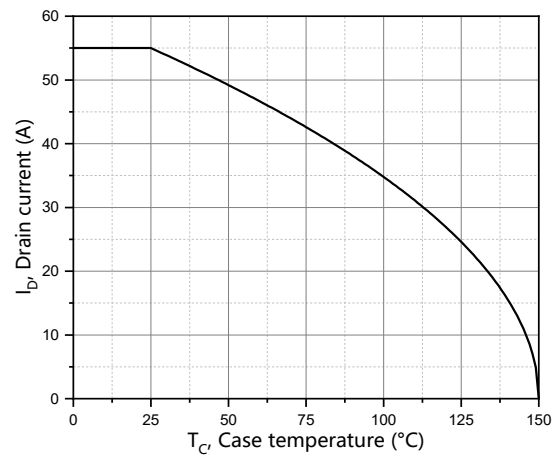


Figure 10. Drain current

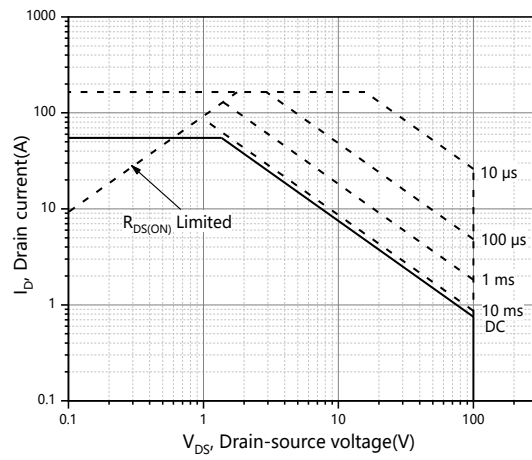


Figure 11. Safe operation area $T_c=25^\circ\text{C}$

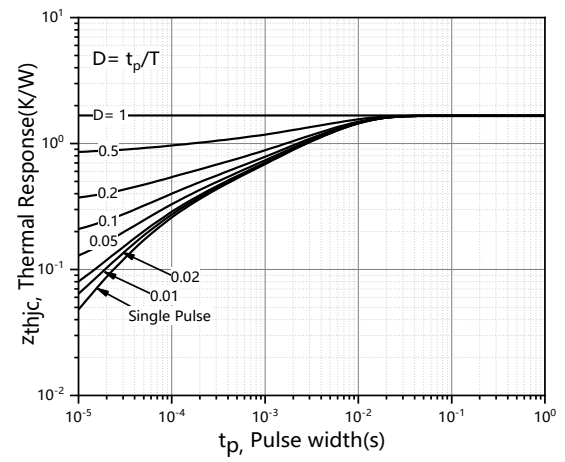


Figure 12. Max. transient thermal impedance