

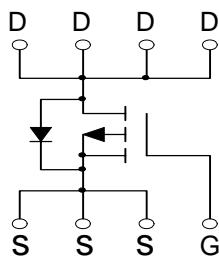
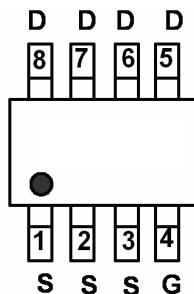
**General Features**

- $V_{DS} = -30V, I_D = -12A$
- $R_{DS(ON)} < 30m\Omega @ V_{GS}=-5V$
- $R_{DS(ON)} < 14m\Omega @ V_{GS}=-10V$

**Application**

- PWM applications
- Load switch
- Power management

SOP-8

**Absolute Maximum Ratings ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-12	A
Drain Current-Pulsed <sup>(Note 1)</sup>	$I_{DM}$	-48	A
Maximum Power Dissipation	$P_D$	3	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

**Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient <sup>(Note 2)</sup>	$R_{\theta JA}$	41.67	°C/W
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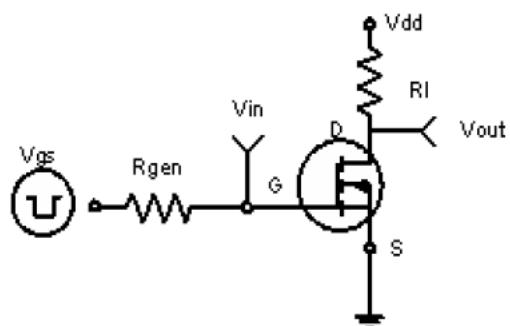
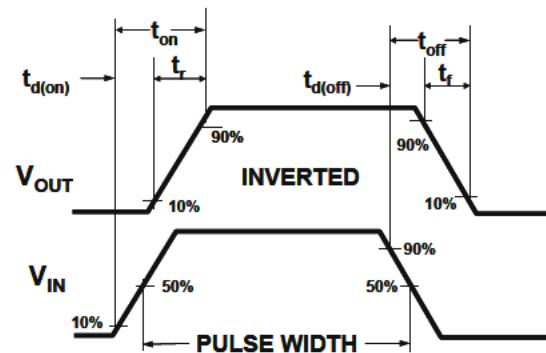
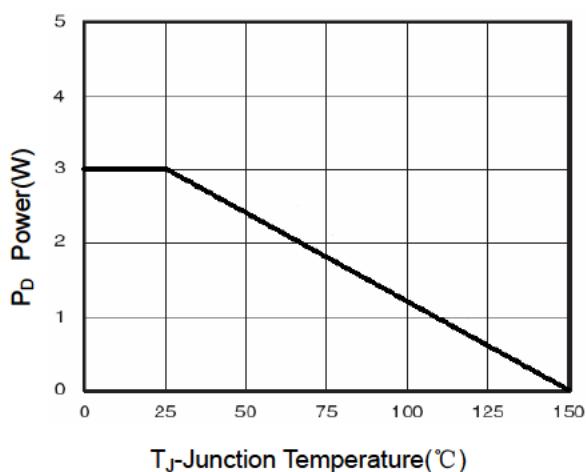
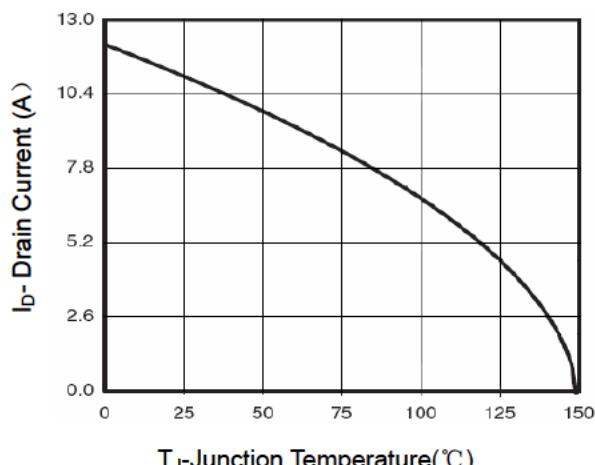
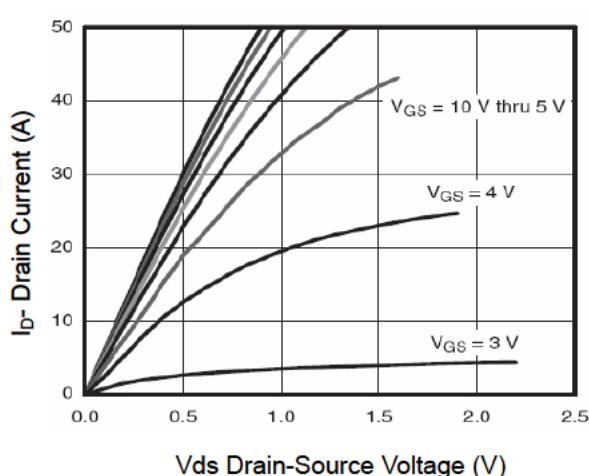
**Electrical Characteristics ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						

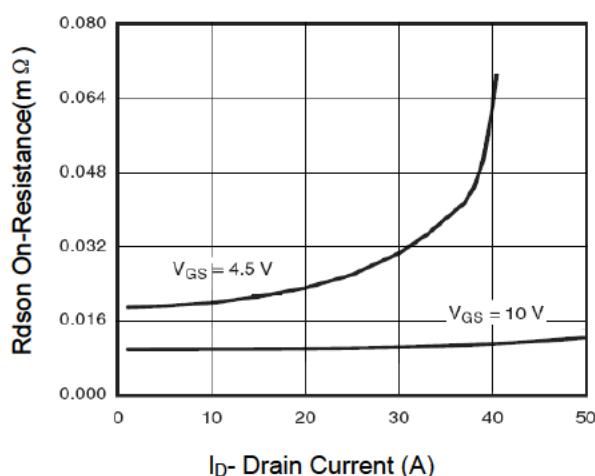
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-30V, V_{GS}=0V$	-	-	-1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.6	-3	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-12A$	-	11.5	14	$m\Omega$
		$V_{GS}=-5V, I_D=-7A$	-	18	30	$m\Omega$
Forward Transconductance	$g_{FS}$	$V_{DS}=-10V, I_D=-10A$	-	26	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{iss}$	$V_{DS}=-15V, V_{GS}=0V, F=1.0MHz$	-	1750	-	PF
Output Capacitance	$C_{oss}$		-	215	-	PF
Reverse Transfer Capacitance	$C_{rss}$		-	180	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, ID=-10A, V_{GS}=-10V, R_{GEN}=1\Omega$	-	9	-	nS
Turn-on Rise Time	$t_r$		-	8	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	28	-	nS
Turn-Off Fall Time	$t_f$		-	10	-	nS
Total Gate Charge	$Q_g$	$V_{DS}=-15V, I_D=-10A, V_{GS}=-10V$	-	24	-	nC
Gate-Source Charge	$Q_{gs}$		-	3.5	-	nC
Gate-Drain Charge	$Q_{gd}$		-	6	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{SD}$	$V_{GS}=0V, I_s=-2A$	-	-	-1.2	V

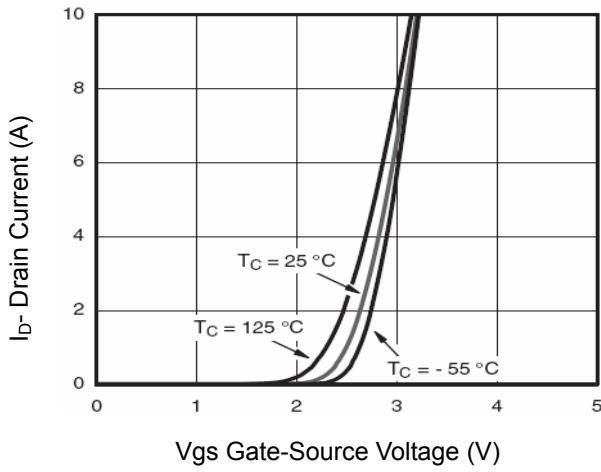
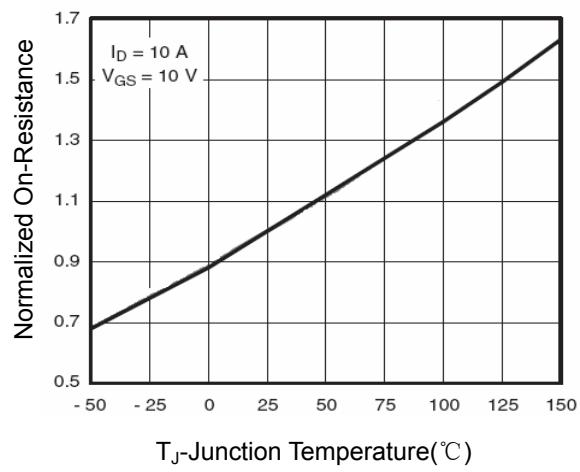
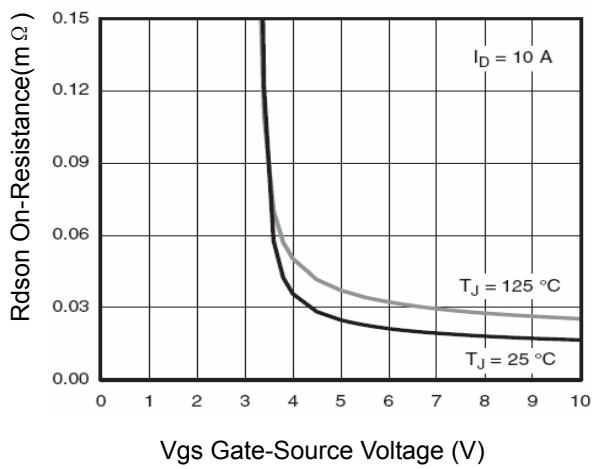
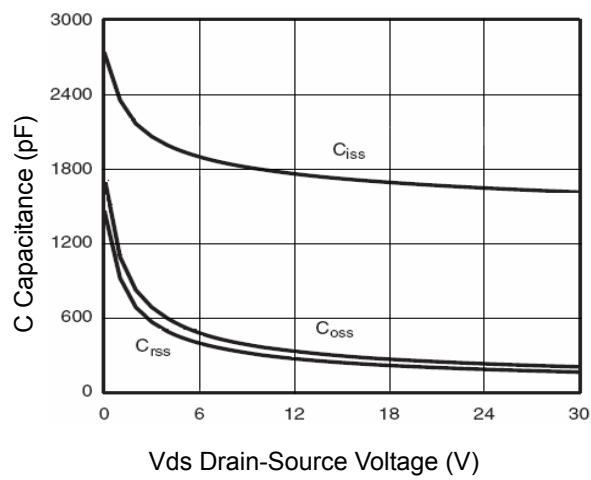
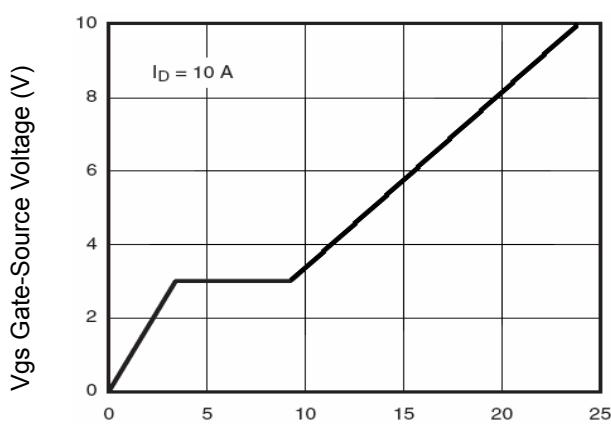
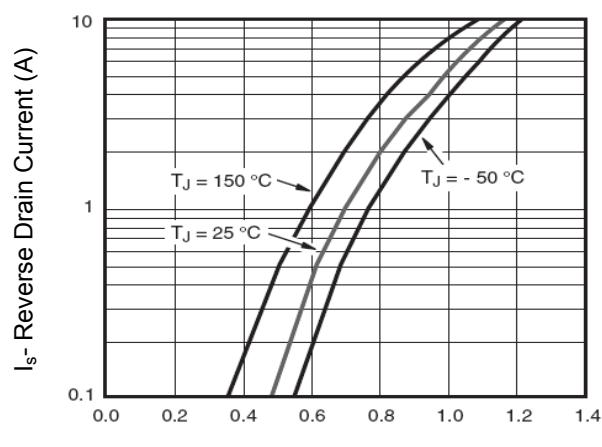
**Notes:**

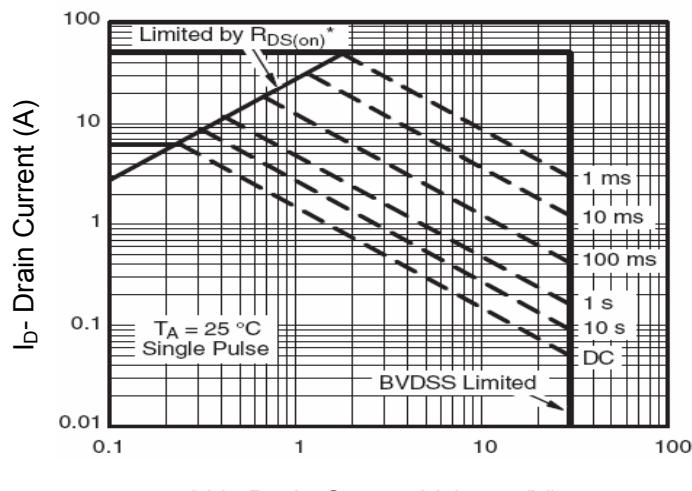
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

**Typical Electrical and Thermal Characteristics****Figure 1:Switching Test Circuit****Figure 2:Switching Waveforms**T<sub>J</sub>-Junction Temperature(°C)**Figure 3 Power Dissipation**T<sub>J</sub>-Junction Temperature(°C)**Figure 4 Drain Current**

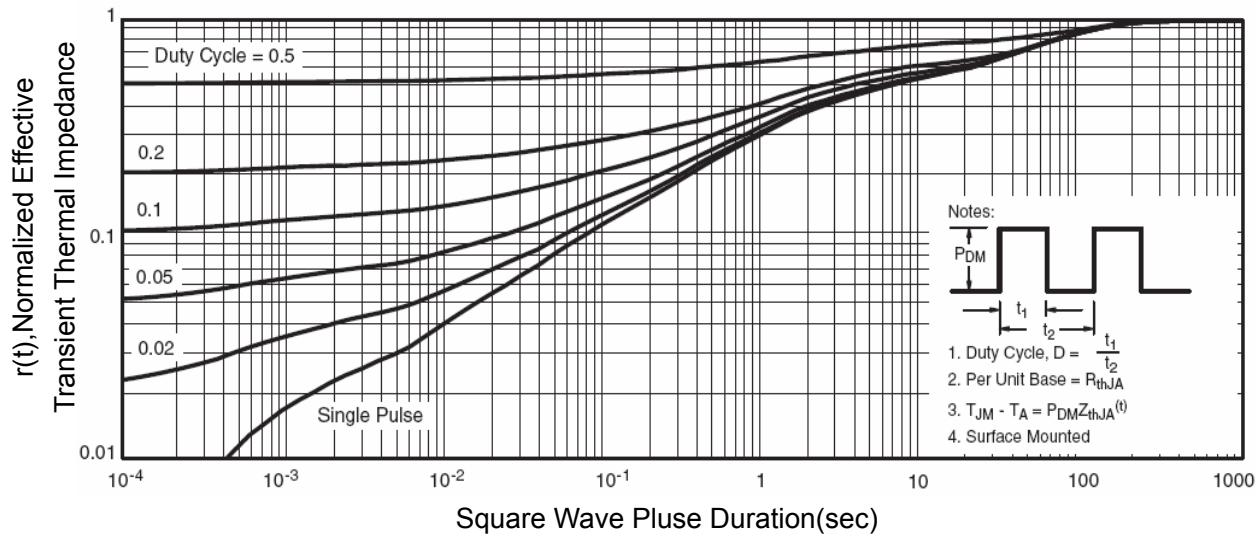
Vds Drain-Source Voltage (V)

**Figure 5 Output Characteristics**I<sub>D</sub>- Drain Current (A)**Figure 6 Drain-Source On-Resistance**

**Figure 7 Transfer Characteristics****Figure 8 Drain-Source On-Resistance****Figure 9  $R_{DS(on)}$  vs  $V_{GS}$** **Figure 10 Capacitance vs  $V_{DS}$** **Figure 11 Gate Charge****Figure 12 Source-Drain Diode Forward**



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**