#### TOSHIBA Diode Silicon Epitaxial Planar Type

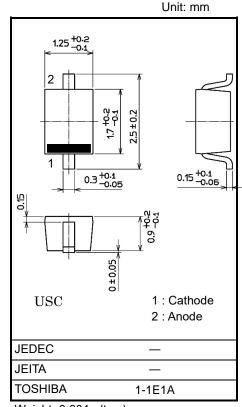
# **1SS352**

#### Ultra High Speed Switching Application

- AEC-Q101 Qualified (Note1)
- Small package
- Low forward voltage : V<sub>F</sub> (3) = 0.98 V (typ.)
- Fast reverse recovery time: trr = 1.6 ns (typ.)
- Small total capacitance :  $C_T = 0.5 \text{ pF}$  (typ.)
- Note1: For detail information, please contact our sales.

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V <sub>RM</sub>	85	V	
Reverse voltage	VR	80	V	
Maximum (peak) forward current	I <sub>FM</sub>	200	mA	
Average forward current	lo	100	mA	
Surge current (10ms)	IFSM	1	A	
Power dissipation	P <sub>D</sub> (Note 4)	200	mW	
Junction temperature	T <sub>j</sub> (Note 2)	150	°C	
	T <sub>j</sub> (Note 3)	125		
Storage temperature	T <sub>stg</sub> (Note 2)	-55 to 150	°C	
	T <sub>stg</sub> (Note 3)	-55 to 125		



Weight: 0.004g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: For devices with the ordering part number ending in H3F(T.

Note 3: For devices with the ordering part number in other than H3F(T.

Note 4: Mounted on a glass epoxy circuit board of 20 mm × 20 mm, Pad dimension of 4 mm × 4 mm.

#### Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Forward voltage	VF (1)	I <sub>F</sub> = 1 mA	_	0.62	_	V
	VF (2)	I <sub>F</sub> = 10 mA	_	0.75	—	
	VF (3)	I <sub>F</sub> = 100 mA	_	0.98	1.20	
Reverse current	I <sub>R (1)</sub>	V <sub>R</sub> = 30 V	_	_	0.1	μΑ
	I <sub>R (2)</sub>	V <sub>R</sub> = 80 V	_	_	0.5	
Total capacitance	CT	V <sub>R</sub> = 0 V, f = 1 MH <sub>z</sub>	_	0.5	3.0	pF
Reverse recovery time	t <sub>rr</sub>	IF = 10 mA, Fig.1	_	1.6	4.0	ns

Start of commercial production 1989-10

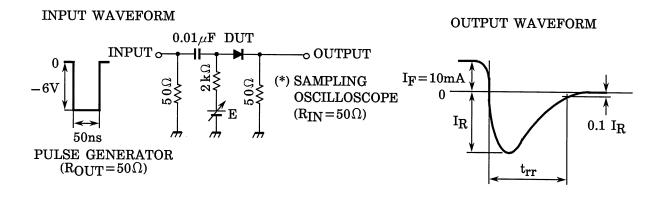


Fig.1 Reverse Recovery Time (trr) Test Circuit

#### Equivalent Circuit (Top View)



Marking



100m

10m

1m

100

10µL 0

- Ta = 100°C

(¥)

FORWARD CURRENT IF

 $I_F - V_F$ 

-25

0.8

FORWARD VOLTAGE  $V_F$  (V)

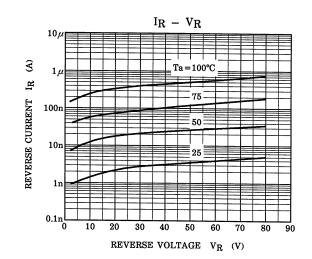
1.2

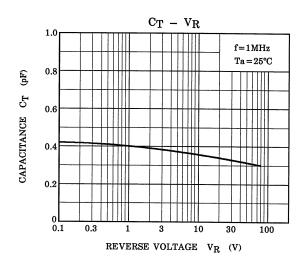
1.6

2.0

25

0.4





The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## TOSHIBA

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