

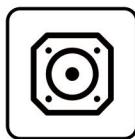


自主封測 品質把控 售後保障

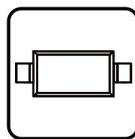
WEB | WWW.TDSEMIC.COM



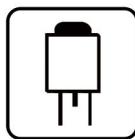
電源管理



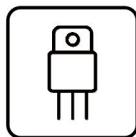
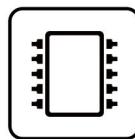
顯示驅動



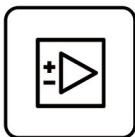
二三極管 LDO穩壓器



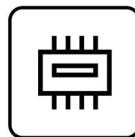
觸摸芯片



MOS管



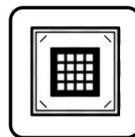
運算放大器



存儲芯片



MCU



串口通信

LM7905

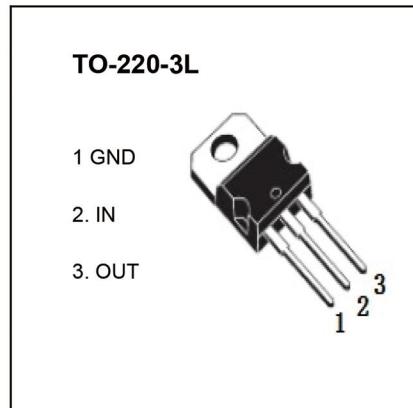
產品規格說明書

TO-220-3L Plastic-Encapsulate Voltage Regulators

Three-terminal negative voltage regulator

FEATURES

- Maximum output current I_{OM} : 1.2 A**
- Output voltage V_o : - 5V**
- Continuous total dissipation**
- P_D : 1.5 W ($T_a = 25^\circ C$)**
- 15 W ($T_c = 25^\circ C$)**



ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

Parameter	Symbol	Value	Unit
Input Voltage	V_i	-35	V
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	83.3	°C/W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	8.33	°C/W
Operating Junction Temperature Range	T_{OPR}	0~+150	°C
Storage Temperature Range	T_{STG}	-55~+150	°C

ELECTRICAL CHARACTERISTICS AT SPECIFIED VIRTUAL JUNCTION TEMPERATURE($V_i=-10V, I_o=500mA, C_i=2.2\mu F, C_o=1\mu F$, unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Output Voltage	V_o	25°C	-4.8	-5	-5.2	V
		-7V≤ V_i ≤-20V, $I_o=5mA-1A$, $P\leq 15W$	0-125°C	-4.75	-5	-5.25
Load Regulation	ΔV_o	$I_o=5mA-1.2A$	25°C	15	100	mV
		$I_o=250mA-750mA$	25°C	5	50	mV
Line Regulation	ΔV_o	-7V≤ V_i ≤-25V	25°C	12.5	50	mV
		-8V≤ V_i ≤-12V	25°C	4	15	mV
Quiescent Current	I_q	25°C	1.5	2	mA	
Quiescent Current Change	ΔI_q	-7V≤ V_i ≤-25V	0-125°C		0.5	mA
	ΔI_q	5mA≤ I_o ≤1A	0-125°C		0.5	mA
Output Noise Voltage	V_N	10Hz≤f≤100KHz	25°C	125		μV
Output Voltage Drift	$\Delta V_o / \Delta T$	$I_o=5mA$	0-125°C	-0.4		mV/°C
Ripple Rejection	RR	-8V≤ V_i ≤-18V, f=120Hz	0-125°C	54	60	dB
Dropout Voltage	V_d	$I_o=1A$	25°C	1.1		V
Peak Current	I_{pk}		25°C	2.0		A

TYPICAL APPLICATION

