

ATM7N65ATE

N-Channel Enhancement Mode Field Effect Transistor

Drain-Source Voltage: 650V

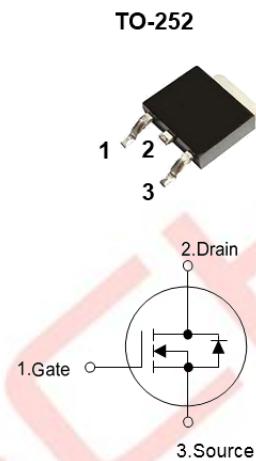
Drain Current: 8A

DESCRIPTION

The ATM7N65ATE is a high voltage and high current power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- ◆ $R_{DS(ON)} < 1.4\Omega @ V_{GS} = 10\text{ V}$
- ◆ Fast switching capability
- ◆ Avalanche energy specified
- ◆ Improved dv/dt capability, high ruggedness



ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	V
Avalanche Current (Note 2)	I_{AR}	8	A
Drain Current	Continuous	I_D	A
	Pulsed (Note 2)	I_{DM}	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	mJ
	Repetitive (Note 2)	E_{AR}	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.5	V/ns
Power Dissipation	TO-252	P_D	W
Junction Temperature		T_J	$^\circ\text{C}$
Operating Temperature		T_{OPR}	$^\circ\text{C}$
Storage Temperature		T_{STG}	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by T_J

3. $L=7.1\text{mH}$, $I_{AS}=8\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

4. $I_{SD} \leq 8\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

ATM7N65ATE

ELECTRICAL CHARACTERISTICS ($T_c = 25^\circ\text{C}$, unless otherwise specified)

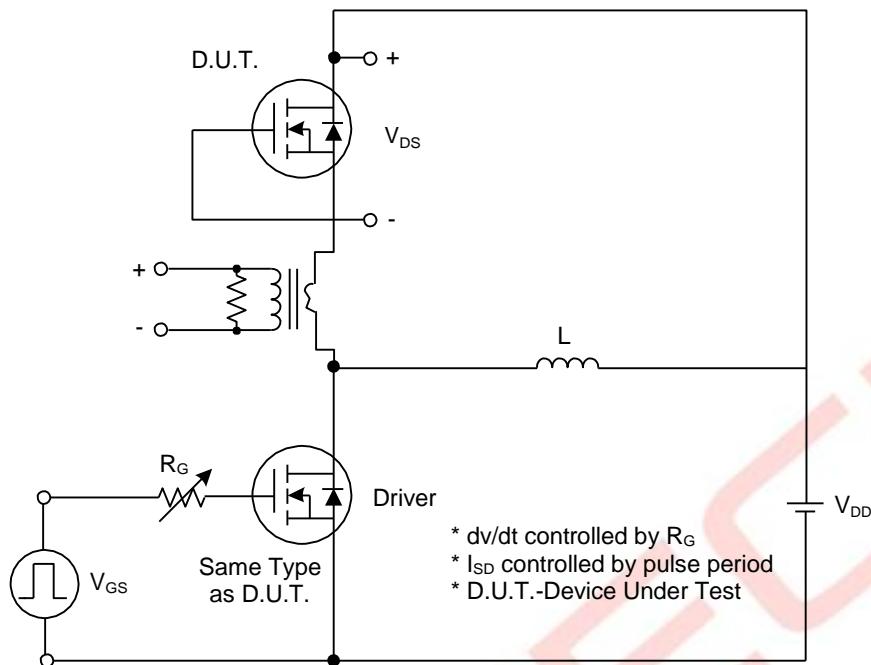
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	650			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 650 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			10	μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$			100	nA
	Reverse				-100	nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}} = 250 \mu\text{A}$, Referenced to 25°C		0.7		$\text{V}/^\circ\text{C}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 4 \text{ A}$		1.2	1.4	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$		1145	1255	pF
Output Capacitance	C_{OSS}			118	135	pF
Reverse Transfer Capacitance	C_{RSS}			19	25	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{\text{D(ON)}}$	$V_{\text{DD}} = 325 \text{ V}, I_{\text{D}} = 8 \text{ A}, R_{\text{G}} = 25 \Omega$ (Note 1, 2)		84	100	ns
Turn-On Rise Time	t_{R}			100	130	ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			275	320	ns
Turn-Off Fall Time	t_{F}			64.5	140	ns
Total Gate Charge	Q_{G}	$V_{\text{DS}} = 520 \text{ V}, I_{\text{D}} = 8 \text{ A}, V_{\text{GS}} = 10 \text{ V}$ (Note 1, 2)		115	130	nC
Gate-Source Charge	Q_{GS}			12		nC
Gate-Drain Charge	Q_{GD}			40		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 8 \text{ A}$			1.4	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}				8	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				32	A
Reverse Recovery Time	t_{RR}	$V_{\text{GS}} = 0 \text{ V}, I_{\text{S}} = 8 \text{ A}, dI_{\text{F}}/dt = 100 \text{ A}/\mu\text{s}$ (Note 2)		365		ns
Reverse Recovery Charge	Q_{RR}			3.4		μC

Notes: 1. Pulse Test: Pulse width $\leq 300 \mu\text{s}$, Duty cycle $\leq 2\%$

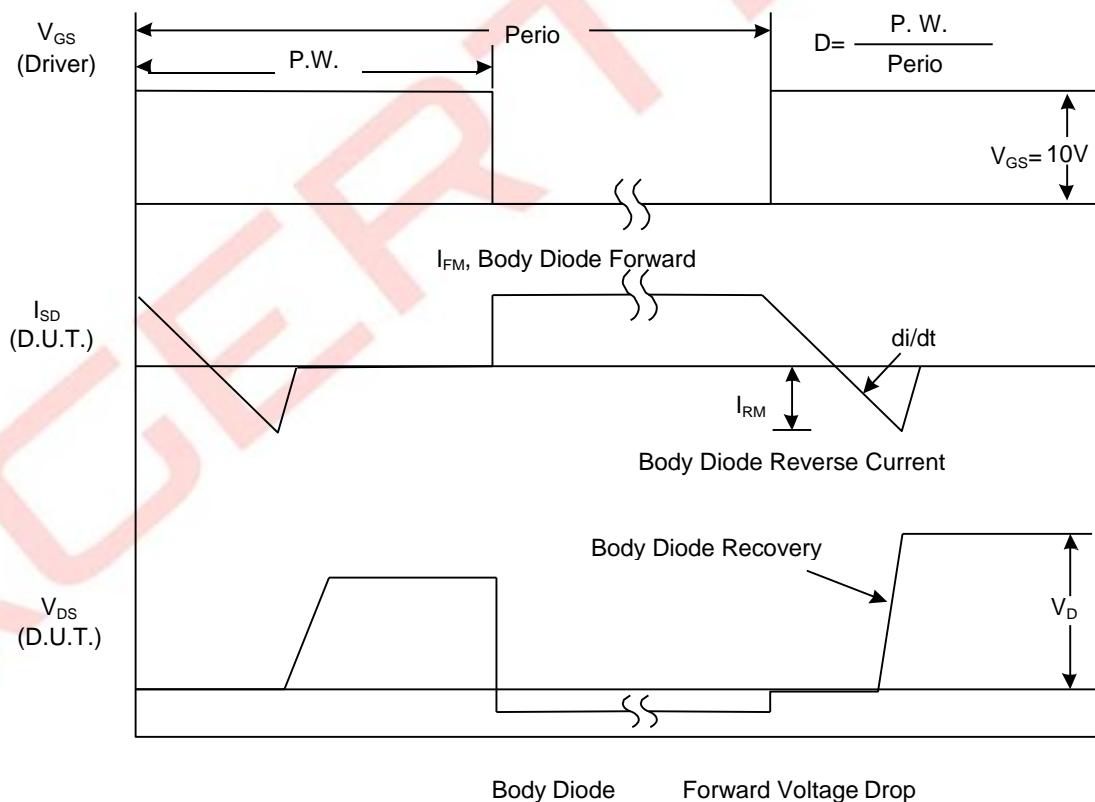
2. Essentially independent of operating temperature

ATM7N65ATE

TEST CIRCUITS AND WAVEFORMS



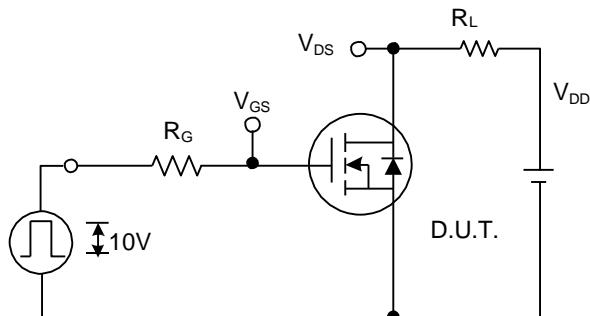
Peak Diode Recovery dv/dt Test Circuit



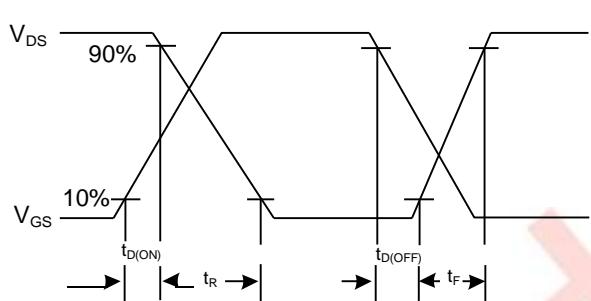
Peak Diode Recovery dv/dt Waveforms

ATM7N65ATE

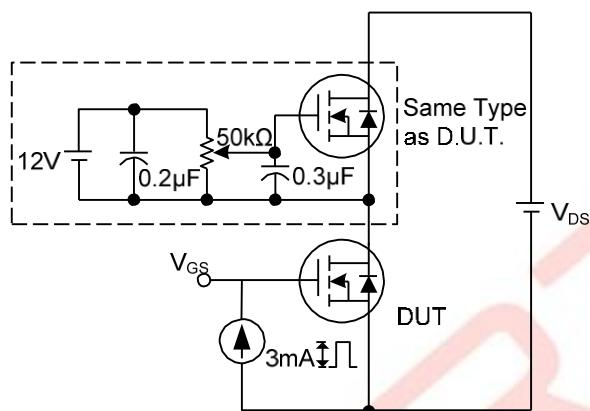
TEST CIRCUITS AND WAVEFORMS (Cont.)



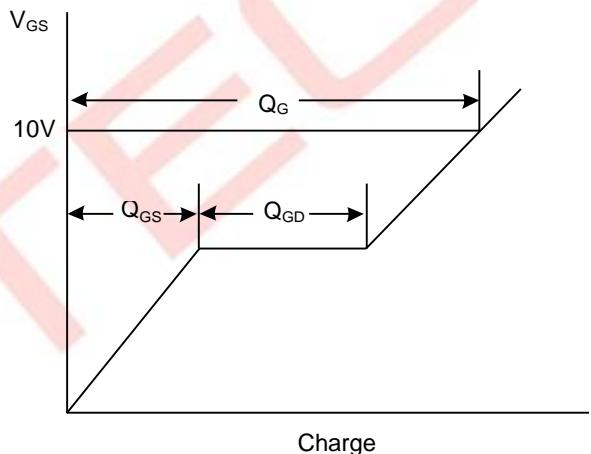
Switching Test Circuit



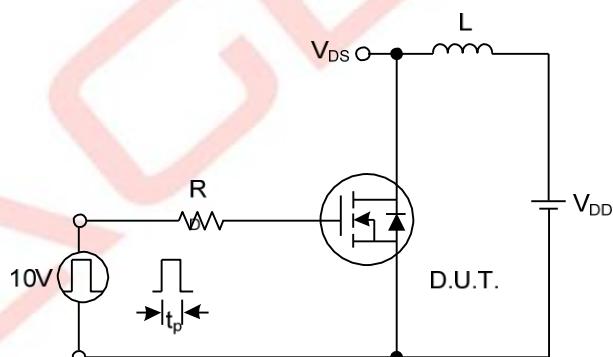
Switching Waveforms



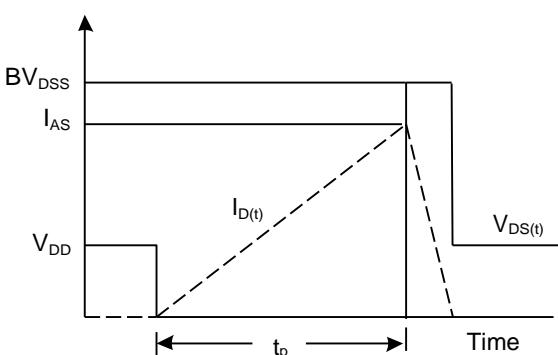
Gate Charge Test Circuit



Gate Charge Waveform



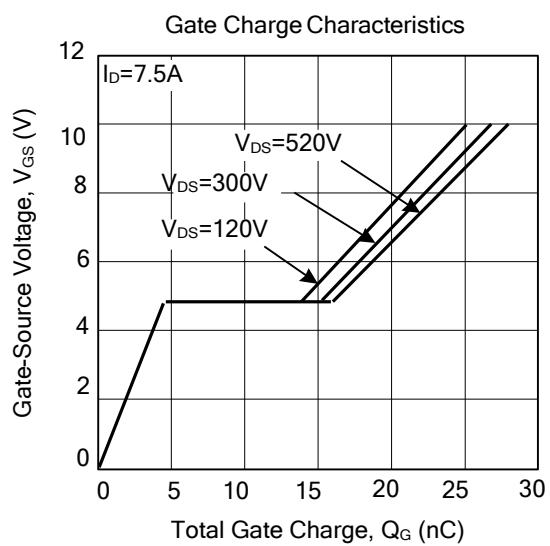
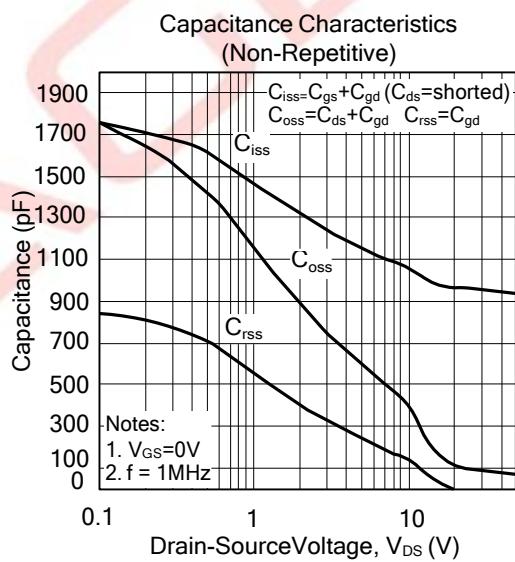
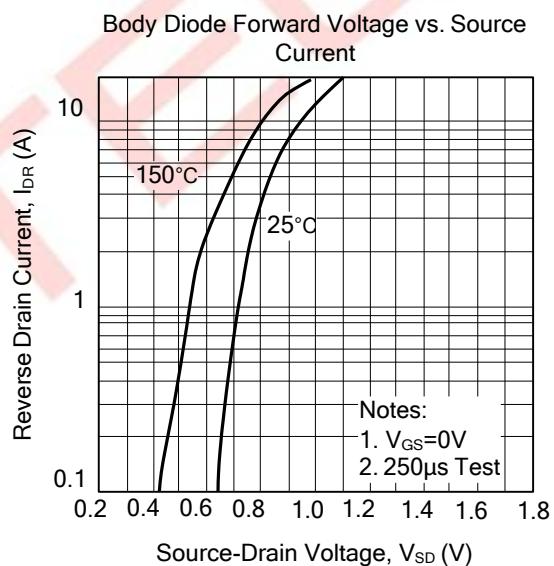
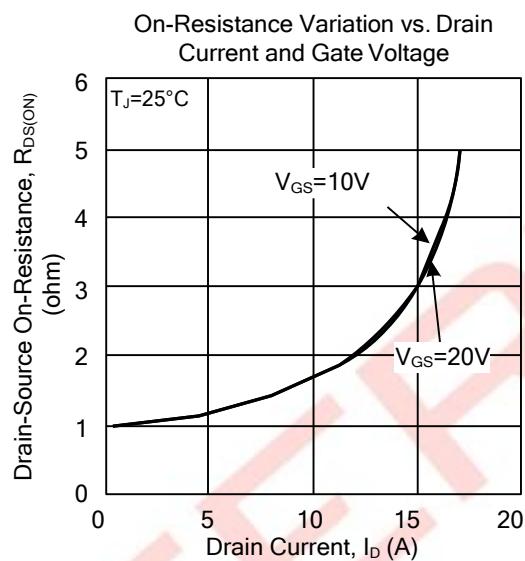
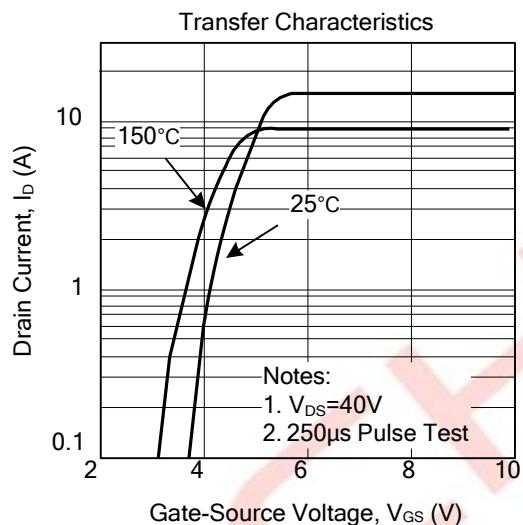
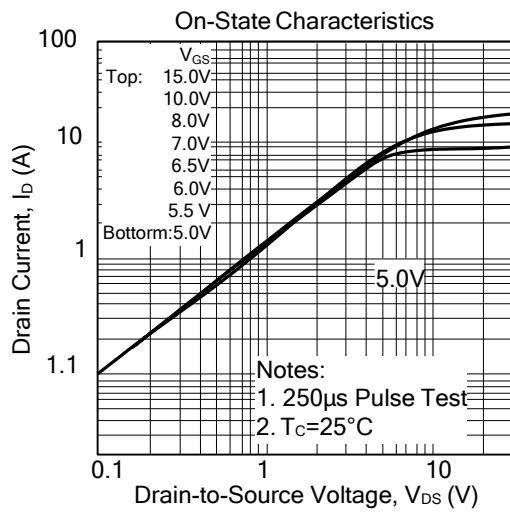
Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

ATM7N65ATE

TYPICAL CHARACTERISTICS CURVES



ATM7N65ATE

TYPICAL CHARACTERISTICS CURVES(Cont.)

