

ATM3401PSA

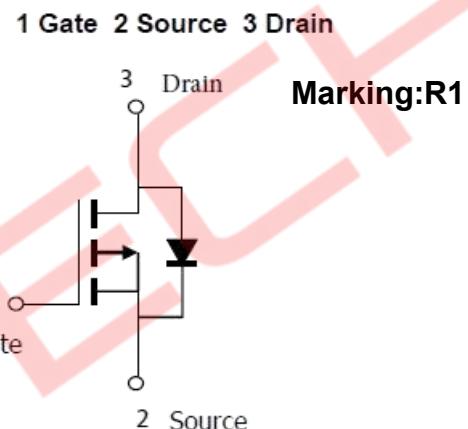
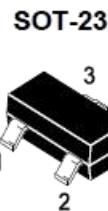
P-Channel Enhancement Mode Field Effect Transistor

Drain-Source Voltage: -30V

Drain Current: -4.2A

Features

- Trench FET Power MOSFET
- Exceptional on-resistance and maximum DC current capability
- $R_{DS(ON)} < 65m\Omega$ ($V_{GS} = -10V$)
- $R_{DS(ON)} < 75m\Omega$ ($V_{GS} = -4.5V$)
- $R_{DS(ON)} < 90m\Omega$ ($V_{GS} = -2.5V$)



Application

- DC/DC Converter
- Load Switch for Portable Devices
- Battery Switch

Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-4.2	A
Power Dissipation	P_D	1.2	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	104	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

ATM3401PSA

Electrical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

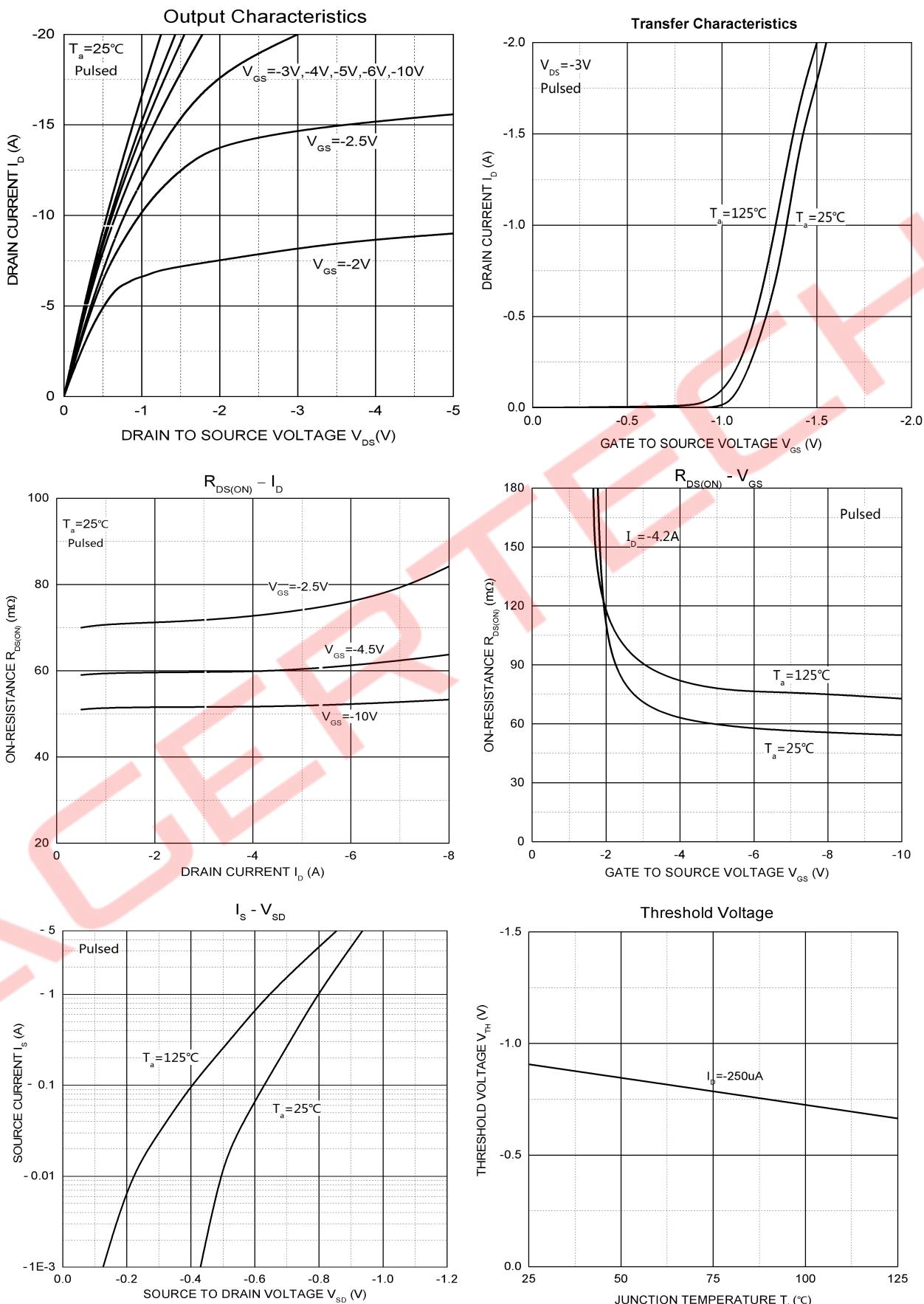
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 12\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.7	-0.9	-1.3	V
Drain-source on-resistance ¹⁾	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -4.2\text{A}$		50	65	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -4\text{A}$		60	75	
		$V_{\text{GS}} = -2.5\text{V}, I_D = -1\text{A}$		70	90	
Forward transconductance ¹⁾	g_{FS}	$V_{\text{DS}} = -5\text{V}, I_D = -4.2\text{A}$		10		S
Dynamic characteristics²⁾						
Input Capacitance	C_{iss}	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		954		pF
Output Capacitance	C_{oss}			115		
Reverse Transfer Capacitance	C_{rss}			77		
Switching characteristics²⁾						
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, V_{\text{DS}} = -15\text{V}, R_L = 3.6\Omega, R_{\text{GEN}} = 6\Omega$			6.3	ns
Turn-on rise time	t_r				3.2	
Turn-off delay time	$t_{\text{d}(\text{off})}$				38.2	
Turn-off fall time	t_f				12	
Source-Drain Diode characteristics						
Diode forward current	I_s				-2	A
Diode pulsed forward current	I_{SM}				-25	A
Diode Forward voltage ¹⁾	V_{DS}	$V_{\text{GS}} = 0\text{V}, I_s = -4.2\text{A}$			-1.2	V

Notes:

- 1) Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 2) Guaranteed by design, not subject to production testing.

ATM3401PSA

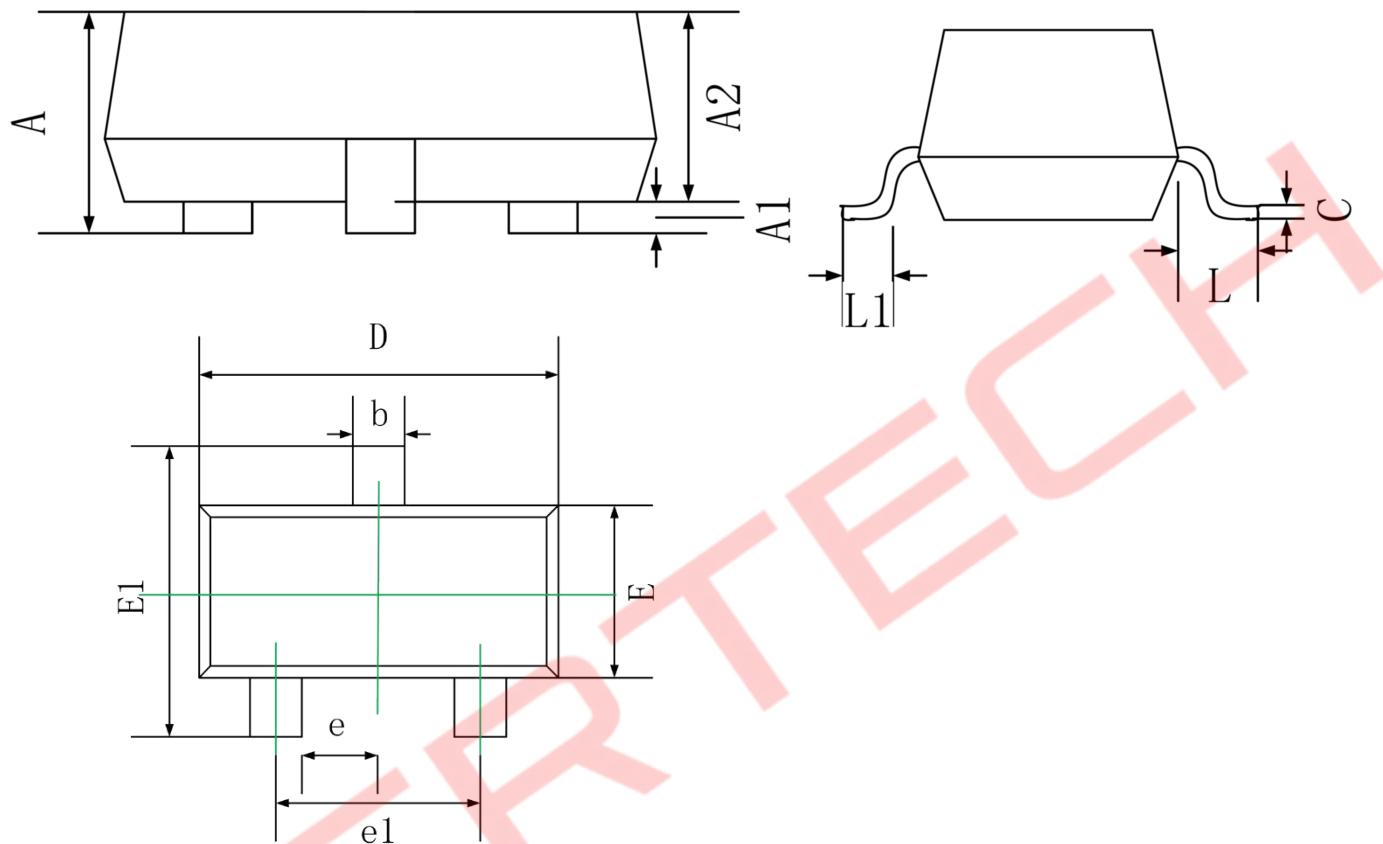
Typical Characteristics Curves



ATM3401PSA

Package Outline

SOT-23

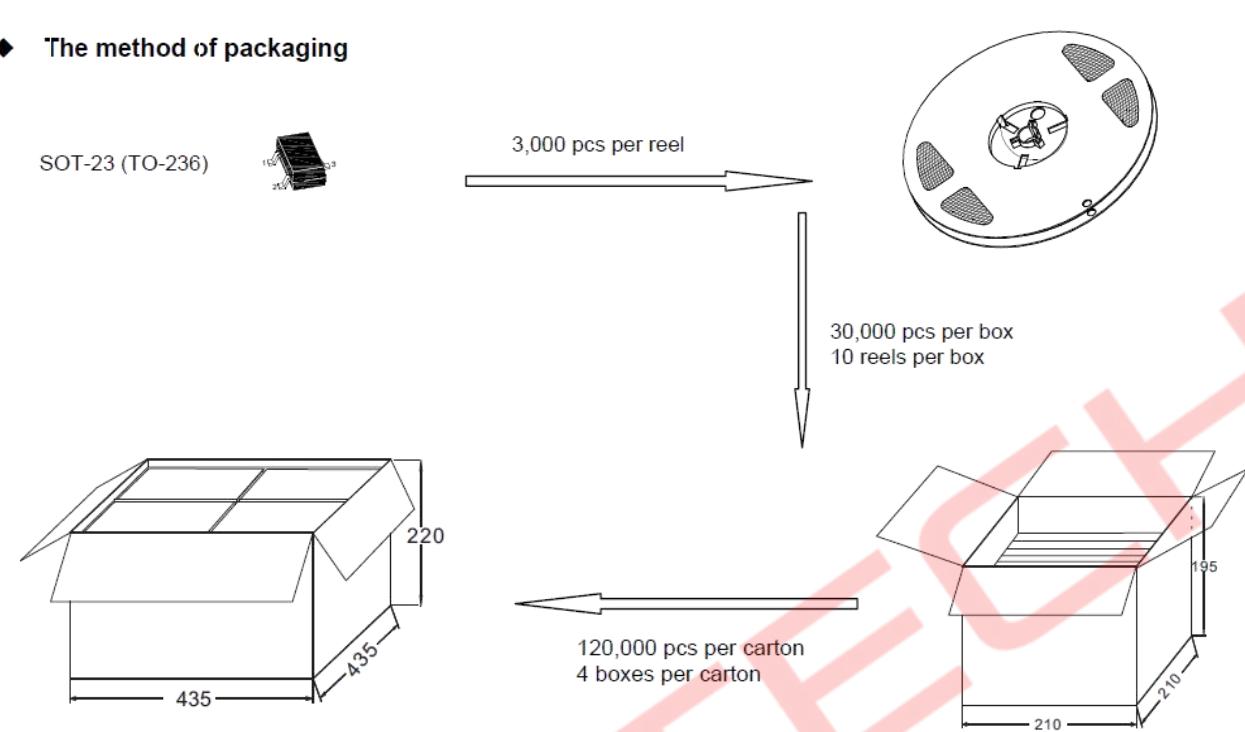


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.90	1.15
A1	0.00	0.10
A2	0.90	1.05
b	0.30	0.50
c	0.08	0.15
D	2.80	3.00
E	1.20	1.40
E1	2.25	2.55
e	0.95 REF.	
e1	1.80	2.00
L	0.55 REF.	
L1	0.30	0.50

ATM3401PSA

Package Specifications

◆ The method of packaging



◆ Embossed tape and reel data

